Module 1

1. List the registers used in 8086.
2. Describe function of the following signals of 8086:- i) INTR ii) READY iii) HOLD
3. What is the significance of pre-decoded instruction byte queue?
4. Explain the concept of segmented memory? What are it's advantages?
5. Explain the use of following signals of 8086:- i) $\overline{DEN}$ ii) $\overline{RQ}$/GT iii) $\overline{LOCK}$
6. Which are the index registers in 8086? Explain it's significance.
7. Explain the minimum mode working of 8086 with timing Diagrams?
8. Discuss the register organization of 8086.
9. Draw and discuss the flag register of 8086.
10. Draw and explain the internal architecture of 8086.
11. With the help of timing diagrams analyze the maximum mode working of 8086?
12. a) Explain the procedure for generating physical address and generate the physical address corresponding to the segment address 1055H and offset address 5555H. Also find their upper range and lower range.

    b) Compare the architectural and signal differences between 8086 and 8088.