HARI PRASAD R

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📍 Chennai

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EXECUTIVE SUMMARY

Result-driven Electronics and Communication Engineering student with a foundation in VLSI domain. Experienced in Machine Learning and Web Development projects. Interested in building smart solutions and eager to contribute technical expertise to impactful innovations. Proven ability to lead teams and deliver practical solutions for real-world projects.

EDUCATION

NARAYANA E-TECHNO SCHOOL
2020 ,SSLC
82.8%

JAWAHAR HS SCHOOL
2022,HSC
68.6%

RMD ENGINEERING COLLEGE
2022-2026
7.53(6th)

SKILLS

Soft Skill

- Teamwork, Collaboration
- Quick learner
- Communication

Technical Skill

- C and C++
- JAVA
- HTML,CSS,JS,REACT
- Verilog HDL
- Excel and BI
- UI and UX

AREA OF INTEREST

- C,C++
- Web Development

ACHIEVEMENTS

- Gold Medal - Tennis (Inter-department)
- Gold Medal - Ball Badminton (Inter-department)

PROJECTS

Plant Disease Detection Using ML and IoT   **JAN 2024**

Developed a machine learning model using CNN to detect plant leaf diseases and integrated IoT sensors for soil condition monitoring.

Underground Sewage Device   **March 2024**

Designed a sensor-based system to detect hazardous gas levels and alert maintenance personnel and assist in clearing clogs.

Overtake Assist   **JAN 2025**

Built an image processing model using YOLO and an ultrasonic sensor to detect whether the trailing vehicle is accelerating or decelerating

Hearing Aid Using FPGA   **ONGOING**

Developing FPGA-based hearing aid. Using MIMC, an FPGA board and a speaker to develop a low-latency, high-precision hearing aid.

VOLUNTARY EXPERIENCE

Internship

- Machine Learning with Python - DLK Tech (Jan 2024 - 5 days)
- UI/UX using FIGMA - CodSoft (June 2024 - 30days)
- Web Development - Internshala (May 2025 - 2 months)

HACKATHON

Team lead for SIH 2024 Hackathon, successfully leading them to Round 2 with a solution focused on Tyre maintanence in mining vehicles.

SYMPOSIUM

Power BI technical event in Chennai Institute of technology

COURSES

- Web Development - Internshala
- Java - Infosys Sprinboard
- C,C++ - Infosys Springboard
- Enhancing Soft Skills and Personality - NPTEL
- System Design Through Verilog - NPTEL
- Analog Circuits - NPTEL

DECLARATION I hereby declare that all the information in this resume is correct to the best of my knowledge.