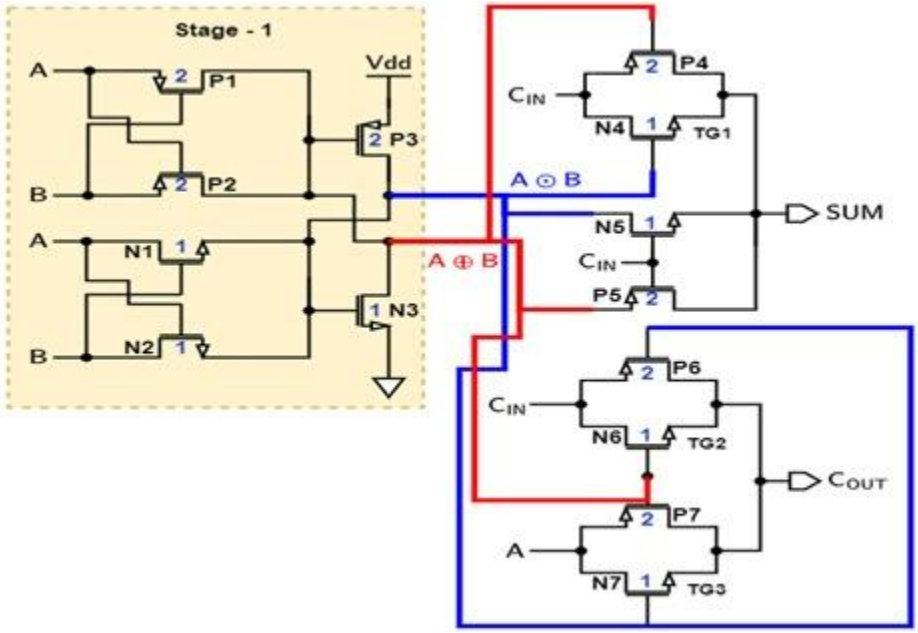
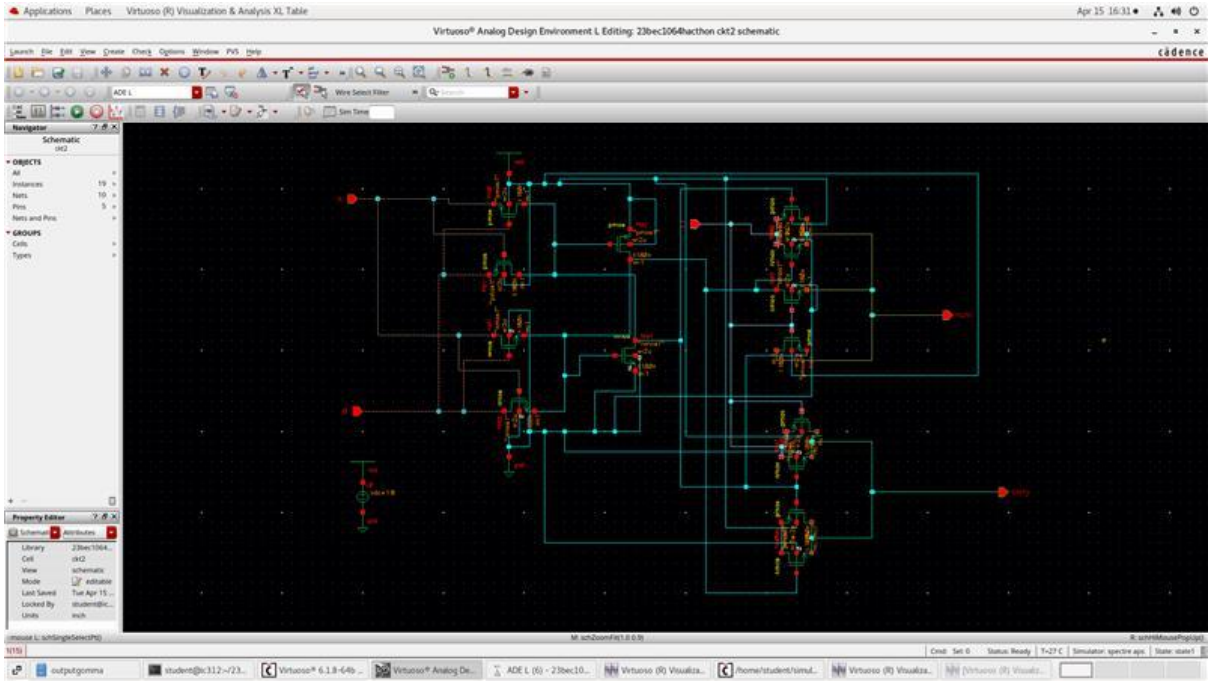
Adder 2

Circuit to be implemented

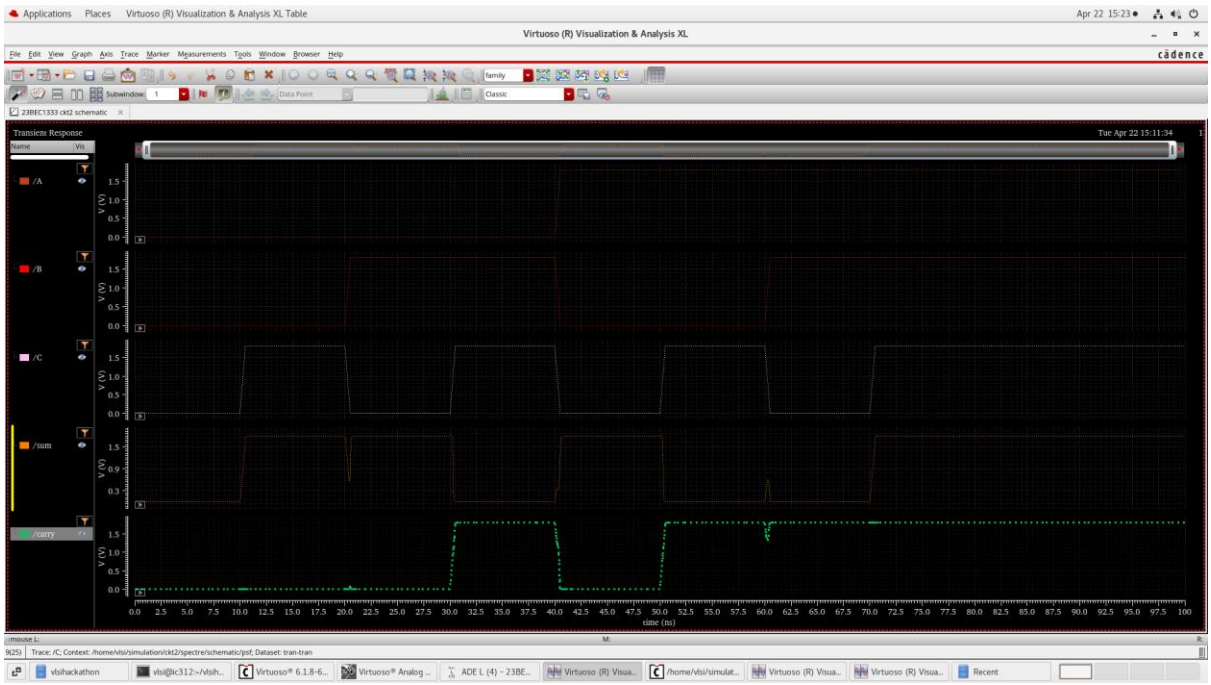


Implementation in cadence

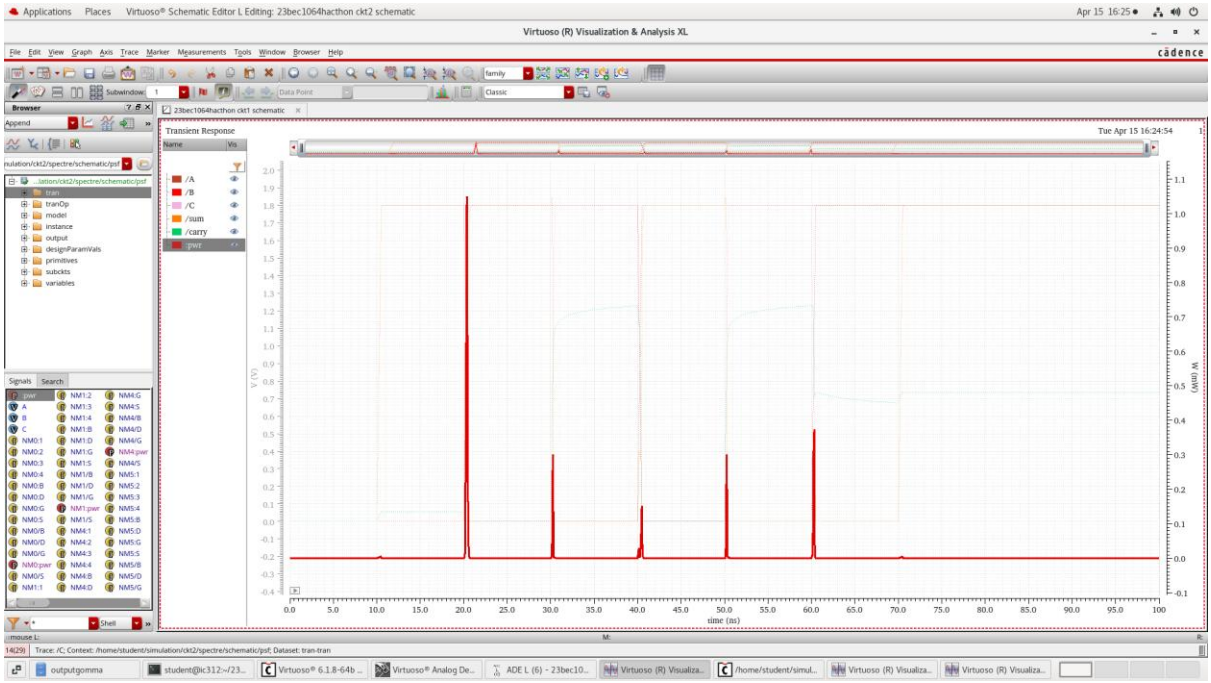
Before sizing



Timing diagram

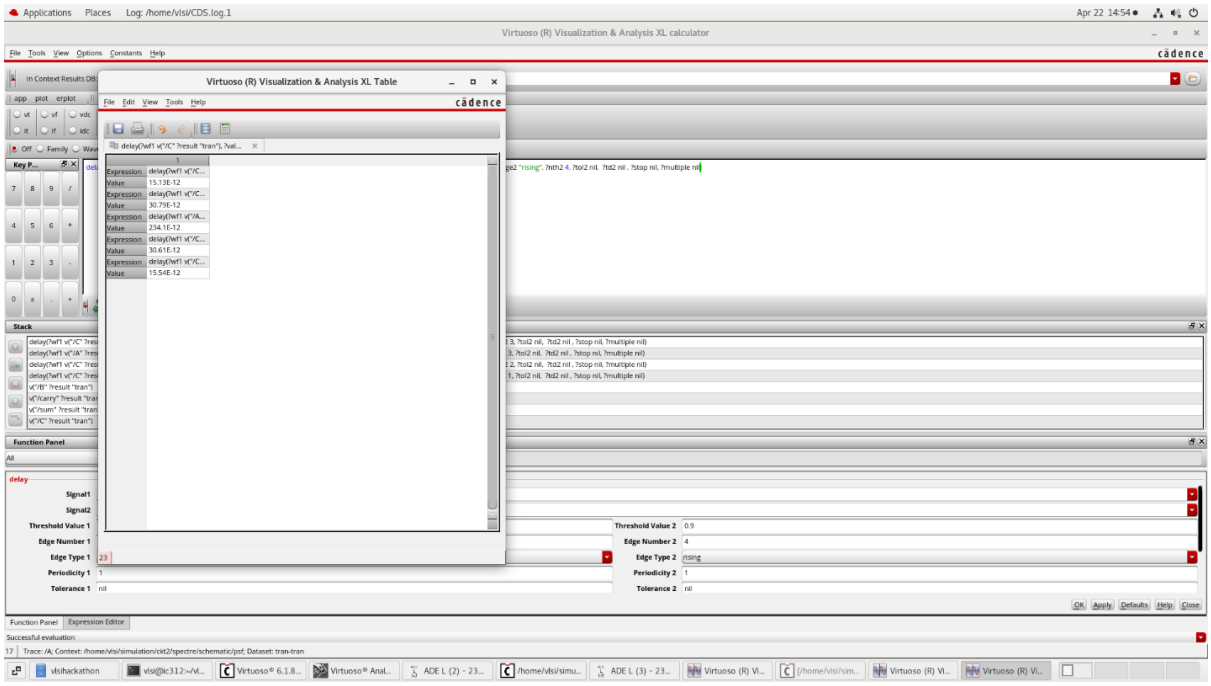


Power

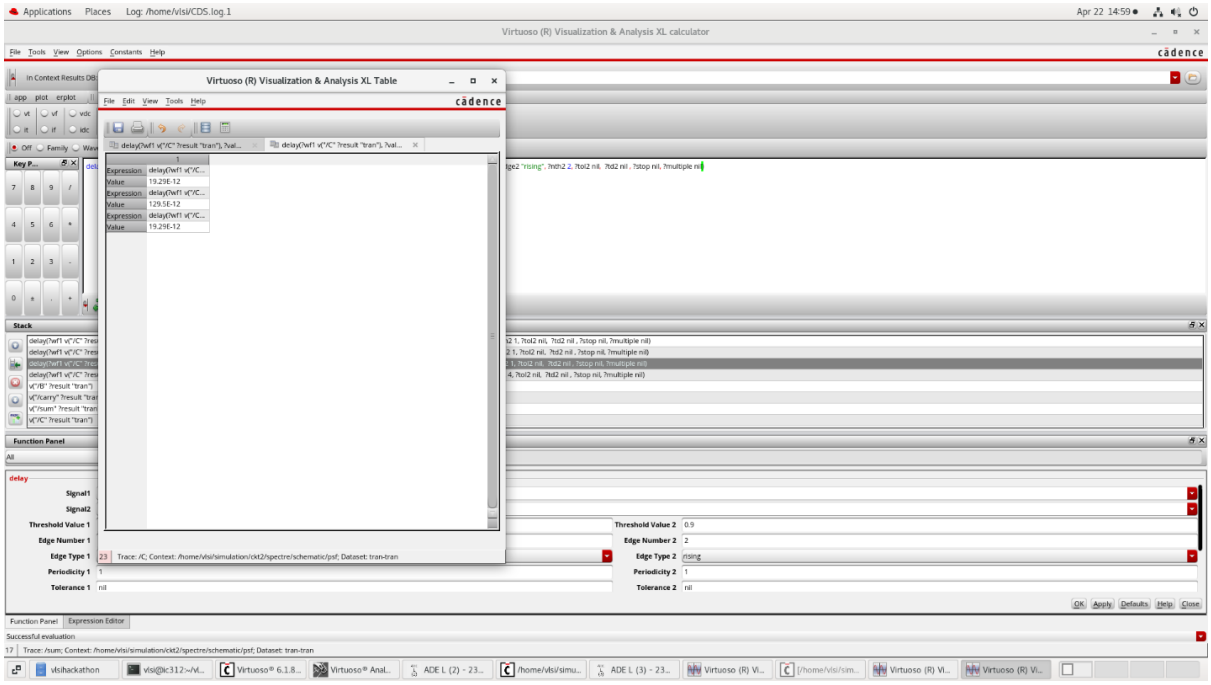


Delay value

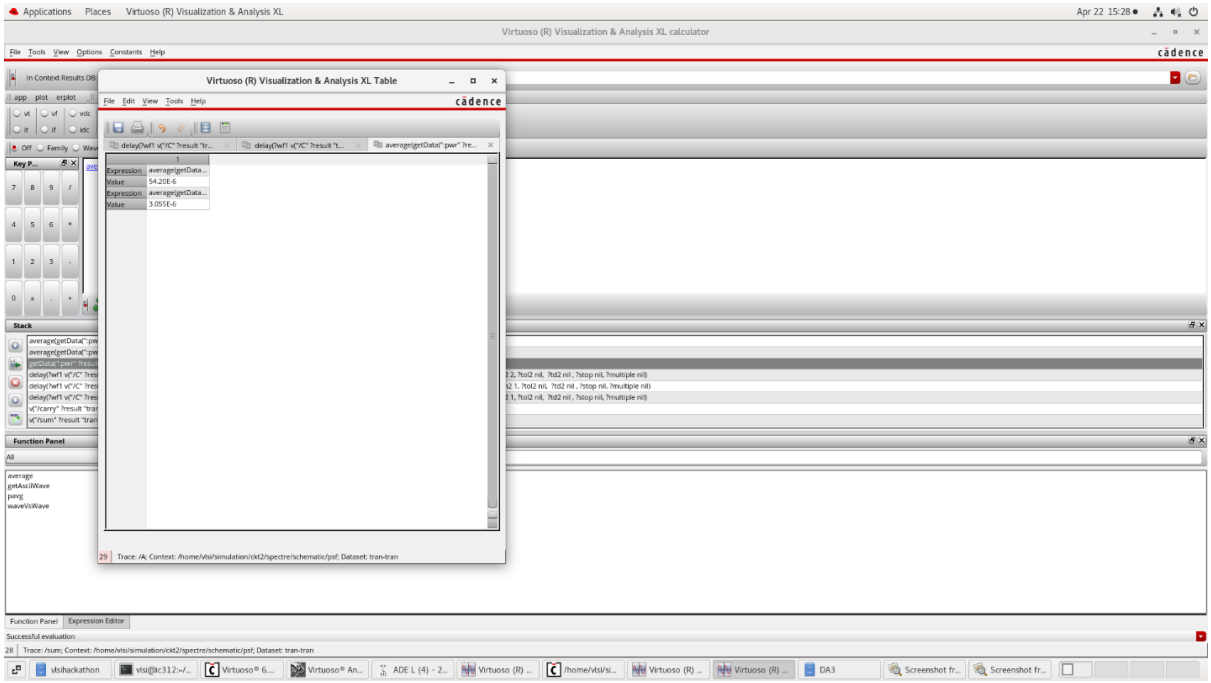
Sum



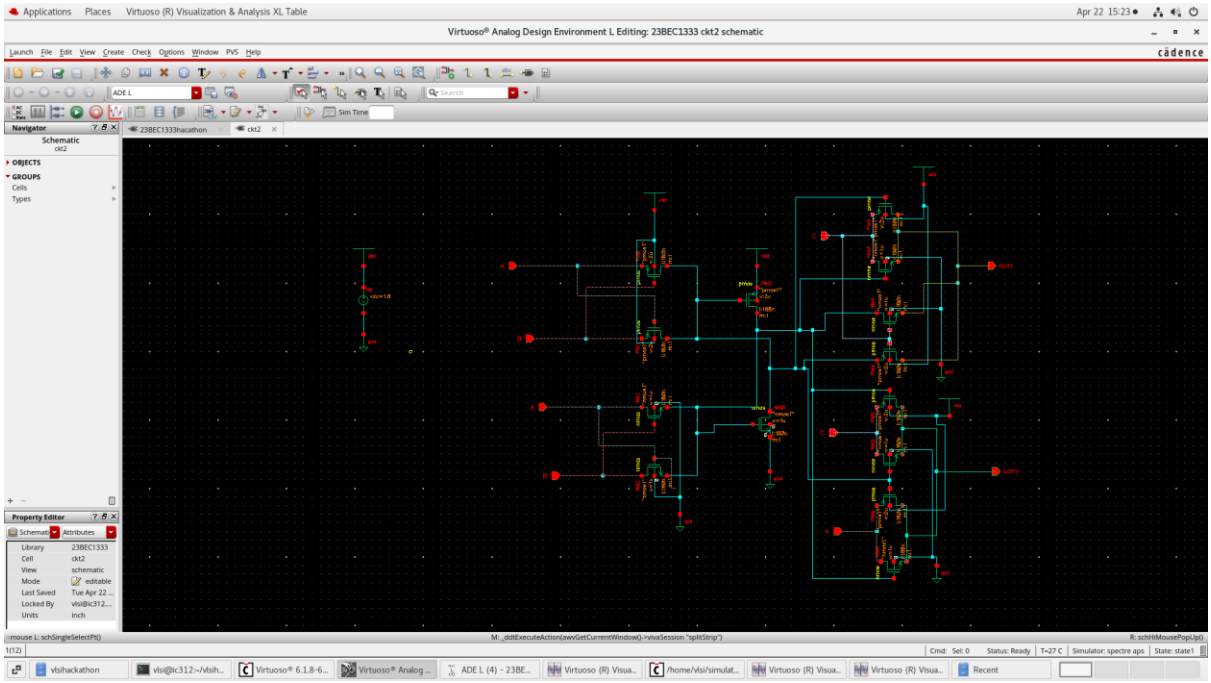
Carry



Power calculation

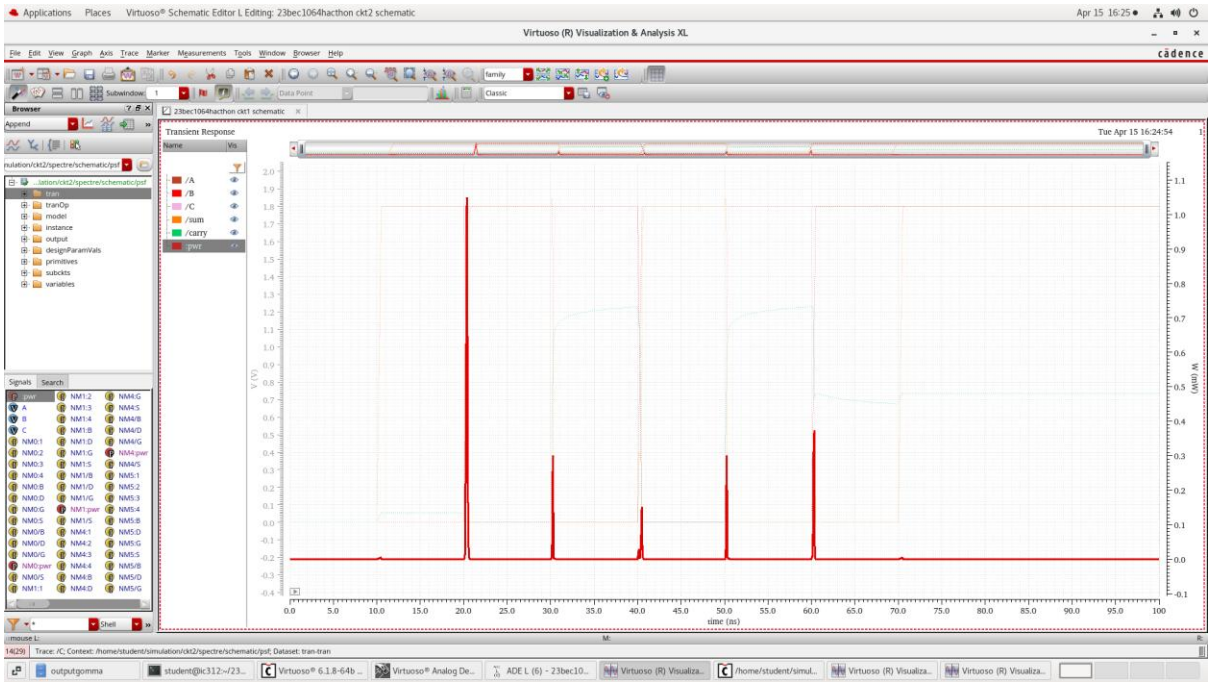


after sizing



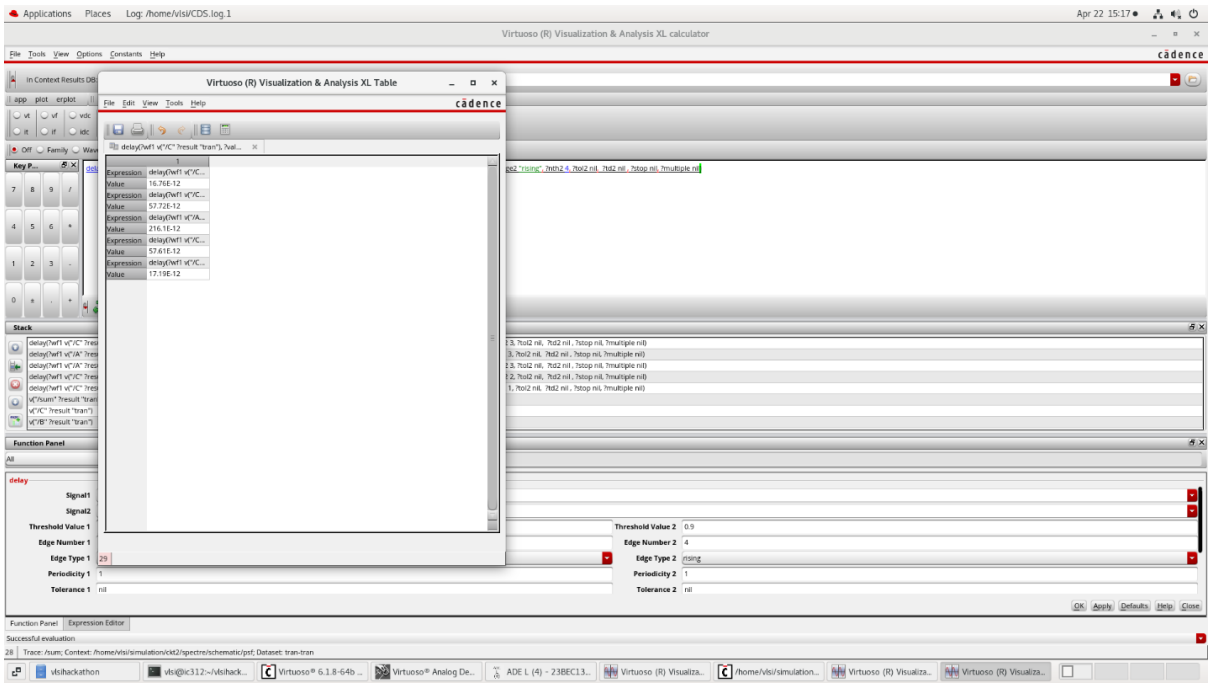
Timing diagram

Power

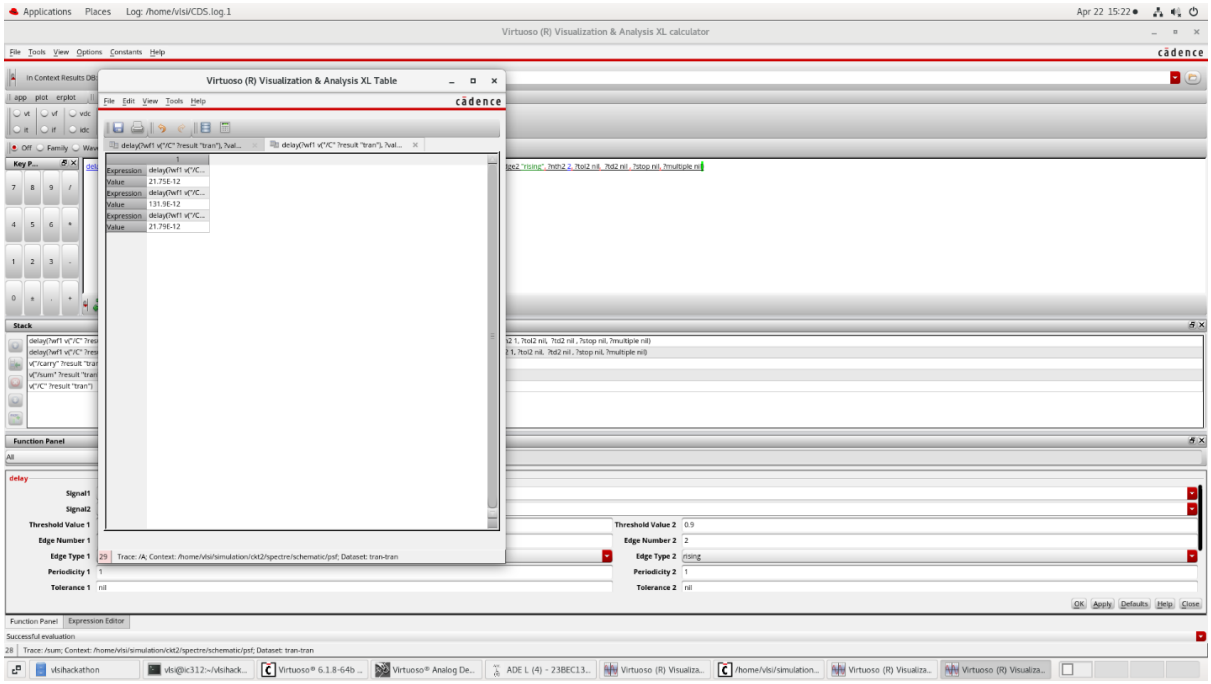


Delay value

Sum



Carry



Power calculation