



Design Rules Checker



☒ Refill all zones before performing DRC

☐ Test for parity between PCB and schematic

☐ Report all errors for each track

Violations (4)

Unconnected Items (0)

Schematic Parity (not run)

Ignored Tests (4)

▼ **Warning: Silkscreen overlap**

Reference 'R3'

Segment on F.Silkscreen

▼ **Warning: Silkscreen overlap**

Reference 'R2'

Segment on F.Silkscreen

▼ **Warning: Silkscreen overlap**

Reference 'D3'

Arc on F.Silkscreen

▼ **Warning: Silkscreen overlap**

Reference 'D2'

Arc on F.Silkscreen

Show: ☐ All

☒ Errors **0**

☒ Warnings **4**

☐ Exclusions

Save...

Delete Marker

Delete All Markers

Run DRC

Close

