Assignment 7

Start Assignment

Due Nov 26 by 11:59pm **Points** 100 **Submitting** a file upload **File Types** pdf

For assignment 7, we will further extend the single-cycle CPU to a system-on-chip (SoC) system which includes the pipelined MIPS processor, a factorial accelerator, and a simple general-purpose IO (GPIO) unit. You will also need to analyze the performance for both the software and hardware versions of factorial calculation. And the detailed description is here:

CMPE200_Assignment_7.pdf (https://sjsu.instructure.com/courses/1491749/files/70545764?wrap=1)

And here is the cover page:

CMPE_200_Assignment_7_Cover_Page.docx (https://sjsu.instructure.com/courses/1491749/files/70580581?wrap=1)

Also, for your convenience, the I/O & Interface lecture slides is here:

CMPE200_Lecture_3.5_IO&Interface.pdf (https://sjsu.instructure.com/courses/1491749/files/70097315?wrap=1)

CMPE200_Lecture_3.5_IO&Interface_readonly.pptx (https://sjsu.instructure.com/courses/1491749/files/70097316?wrap=1)