

# **CMPE200 Assignment 3 Task 2 Test Log** **Algorithm 2**

**Programmer's Name:** \_\_\_\_\_ Harish Marepalli

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Adr	MIPS Instruction	Machine Code	Registers				Memory Content	
			\$a0	\$s0	\$t0	\$t1	Word @ 0x00	Word @ 0x10
3000	ori \$a0, \$0, 5	0x34040005	0x00000005	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
3004	sw \$a0, 0(\$t0)	0xad040000	0x00000005	0x00000000	0x00000000	0x00000000	0x00000005	0x00000000
3008	ori \$t1, \$0, 1	0x34090001	0x00000005	0x00000000	0x00000000	0x00000001	0x00000005	0x00000000
300c	while: beq \$a0, \$0, done	0x10800004	0x00000000	0x00000000	0x00000000	0x00000078	0x00000005	0x00000000
3010	mult \$t1, \$a0	0x01240018	0x00000001	0x00000000	0x00000000	0x00000078	0x00000005	0x00000000
3014	mflo \$t1	0x00004812	0x00000001	0x00000000	0x00000000	0x00000078	0x00000005	0x00000000
3018	addi \$a0, \$a0, -1	0x2084ffff	0x00000000	0x00000000	0x00000000	0x00000078	0x00000005	0x00000000
301c	j while	0x08000c03	0x00000000	0x00000000	0x00000000	0x00000078	0x00000005	0x00000000
3020	done: sw \$t1, 16(\$t0)	0xad090010	0x00000000	0x00000000	0x00000000	0x00000078	0x00000005	0x00000078
3024	lw \$s0, 16(\$t0)	0x8d100010	0x00000000	0x00000078	0x00000000	0x00000078	0x00000005	0x00000078
3028								