

```
l209@cse:~/Documents/CS23I1027/Q2$ iverilog sample.v tb_4to1.v -o sample
l209@cse:~/Documents/CS23I1027/Q2$ vvp sample
VCD info: dumpfile mux4to1.vcd opened for output.
Time = 0 | data_in = 10100011 | select = 00 | output_y = 11
Time = 10 | data_in = 10100011 | select = 01 | output_y = 00
Time = 20 | data_in = 10100011 | select = 10 | output_y = 10
Time = 30 | data_in = 10100011 | select = 11 | output_y = 10
l209@cse:~/Documents/CS23I1027/Q2$
```