

EC1001: Digital Circuits

Assignment-2_Chapter2

- 1. Reduce the following Boolean expressions to a minimum number of literals:
 - (a) A'C' + ABC + AC'
 - (b) (x'y' + z)' + z + xy + wz
 - (c) A'B(D' + C'D) + B(A + A'CD)
 - (d) (A' + C)(A' + C')(A + B + C'D)
 - (e) A'BD' + ABC'D' + ABCD'
- 2. Draw logic diagrams of the circuits that implement the original and simplified expressions in Problem 1 (a), (b), (c).
- 3. Find the complement of the following expressions:
 - (a) x'y' + xy

(b)
$$ac + ab' + a'bc'$$

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- (c) z + z'(v'w + xy)
- 4. Express each function in sum-of-minterms and product-of-maxterms, and obtain the truth table.

(a)
$$F = (a' + c' + d')(ab + cd)$$
 (b) $F = (b + c'd')(a + bc')$

(c)
$$F = a'b'c' + acd + ab'd' + b'cd$$

- 5. Convert each of the following expressions into sum of products and product of sums:
 - (a) (u + x'w)(x + u'v)
 - (b) x' + z(x + y')(y + z')
- 6. Find the complement of F = AC + BD; then show that FF' = 0 and F + F' = 0.
- 7. Simplify the following Boolean functions T_1 and T_2 to a minimum number of literals:

Α	В	c	<i>T</i> ₁	T ₂
0	0	0	1	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	0	1
1	0	1	0	1
1	1	0	0	1
1	1	1	0	1

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- 8. Represent each of the following statements by a boolean Expressions.
 - (a) The company safe (S) should be unlocked only when Mr. Jones (J) is in the office or Mr. Evans (E) is in the office, and only when the company (C) is open for business, and only security guard (G) is present.
 - (b) The elevator door should open if the elevator is stopped, it is level with the floor, and the timer has not expired, or if the elevator is stopped, it is level with the floor, and a button is pressed. [Note: Assume the variables are, D: Elevator door opens, S: Elevator is stopped, F: Elevator is level with floor, T: Timer has expired, B: Button is pressed]
- 9. For the Boolean function

$$F = xy'z' + x'y'z' + xy + x'y$$

- (a) Obtain the truth table of F.
- (b) Draw the logic diagram, using the original Boolean expression.
- (c) Use Boolean algebra to simplify the function to a minimum number of literals.
- (d) Obtain the truth table of the function from the simplified expression and show that it is the same as the one in part (a).
- (e) Draw the logic diagram from the simplified expression, and compare the total number of gates with the diagram of part (b).
- 10. Determine whether the following Boolean equations are true or false.

(a)
$$v'z' + vz' + x'z = x' + xz'$$

(b)
$$x'y' + xz' + yz = y'z' + xy + x'z'$$