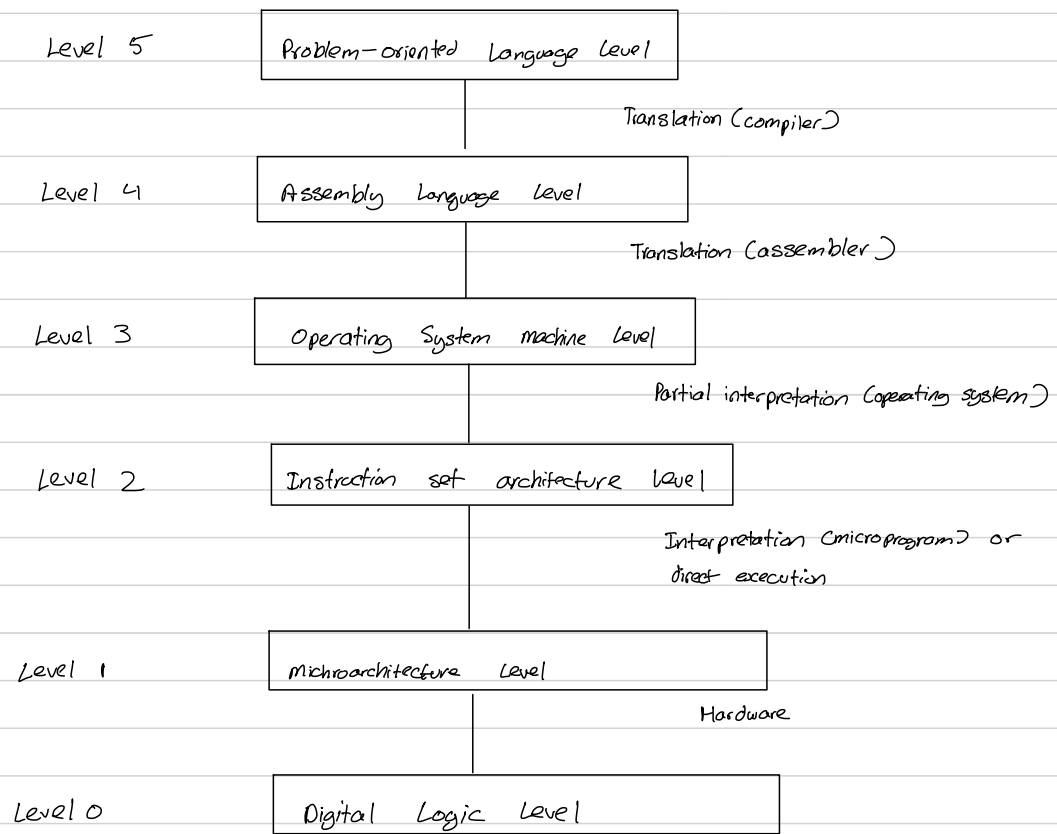



A Six Level Computer :



Exercise 1

1. 17 \rightarrow binary

	17	
1	8	1 0 0 1
0	4	
0	2	
0	1	
1	0	

2. 10010 $\Rightarrow 2^1 + 2^4 \Rightarrow 2 + 16 \Rightarrow 18$

3. $2^7 \Rightarrow 128$

4. $\begin{array}{r} 1\ 1\ 1 \\ 1\ 0\ 1\ 0 \\ \hline 0\ 1\ 1\ 0 \\ \hline 1\ 0\ 0\ 0\ 0 \end{array}$ $\therefore 1010 \Rightarrow 2^1 + 2^3 \Rightarrow 10$
 $0110 \Rightarrow 2^1 + 2^2 \Rightarrow \underline{6} +$
 $10000 \Rightarrow 2^4 \Rightarrow 16$

Exercise 2:

Question 1

1. $7 \times 1\text{ns} = 7\text{ns}$

2. $\text{IPS} = \frac{1}{10^{-9}} = 10^9 \text{IPS}$

3. $7\text{ns} + 19\text{ns} = \underline{26\text{ns}}$

5. 20 instructions in pipeline = 26ns

20 instructions without pipeline = $20 \times 7 = 140\text{ns}$

$\therefore \frac{140}{26} = 5.38$

Hence $5.38 \times 1000 \text{MIPS} \Rightarrow 5380 \text{MIPS}$

Question 2:

20752 \Rightarrow	20752		
0	10376		
0	5188		
0	2594		1 0 1 0 0 0 1 0 0 0 1 0 0 0 0 \rightarrow unsigned number
0	1297	0	5 0 1 0 1 0 0 0 1 0 0 0 1 0 0 0 0 \rightarrow signed number
1	648	1	2
0	324	0	1
0	162	1	0
0	81		
1	40		
0	20		
0	10		

0 1 01 0001 00001 000001

One complement $\rightarrow 0101000100010000$

Two's complement \rightarrow
$$\begin{array}{r} 0101000100010000 \\ + \quad \quad \quad \quad \quad \quad \quad 1 \\ \hline 0101000100010001 \end{array}$$

Question 3

1. 14356_{10}

0	14356	11100000010100	→ unsigned magnitude
0	7178	001110000010100	→ signed magnitude
0	2589	001110000010100	→ one's complement
1	1794	001110000010100	→ two's complement
0	897		
1	448		
0	224		
0	112		
0	56		
0	28		
0	14		
0	7		
1	3		
1	1		
1	0		

2. Symmetric means that there equal number of negative and positive numbers:

Symmetric : S_m, I'

Asymmetric : Excess 2^{n-1} , 2^c

Symmetric advantage : Straight forward

Symmetric disadvantage: We always have two zeros

Asymmetric advantage : Only one zero

1. disadvantage : representing more negative numbers.

Exercise 03

Question 1 - Endianness:

	Big Endian	Little Endian
0	$\begin{bmatrix} 0 \\ 1 \\ 0 \\ 1 \\ 1 \\ 0 \\ 0 \end{bmatrix}$	$\begin{bmatrix} 1 \\ 1 \\ 0 \\ 0 \\ 1 \\ 1 \\ 1 \end{bmatrix}$
1	$\begin{bmatrix} 1 \\ 0 \\ 0 \\ 1 \\ 0 \\ 1 \\ 1 \\ 0 \end{bmatrix}$	$\begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 1 \\ 1 \end{bmatrix}$
2	$\begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 1 \\ 1 \end{bmatrix}$	$\begin{bmatrix} 1 \\ 0 \\ 0 \\ 1 \\ 1 \\ 1 \\ 0 \\ 0 \end{bmatrix}$
3	$\begin{bmatrix} 1 \\ 1 \\ 0 \\ 0 \\ 1 \\ 1 \\ 1 \\ 1 \end{bmatrix}$	$\begin{bmatrix} 0 \\ 1 \\ 0 \\ 1 \\ 1 \\ 1 \\ 0 \\ 0 \end{bmatrix}$

2. "Welcome Class 2021" → Little Endian

R_0	-	e	m	a	c	i	e	w
R_1	0	2	-	s	s	a	l	c
R_2	x	x	x	x	x	x	l	z

→ Big Endian

Question 2

- $2^9 = \text{possible words}$
- $$001\ 0110\ 0001\ 1111$$

1's = 9 ⇒ not Even parity
⇒ Error

d) 1101 0011 1000 1000

$\# 1's = 7 \Rightarrow$ not even parity

\Rightarrow Error

e) If there are more than two bit-flips

Question 3

1. Data $\leftarrow [0101 \rightarrow n \text{ bits}$
 Check Bits $\leftarrow \times \times \times \times \rightarrow r \text{ bits}$
 CODE WORD

	1	2	3	4	5	6	7
code word	0	1	0	0	1	0	1
1	0		0		1		1
2		1	0			0	1
4				0	1	0	1

0 1 0 0 1 0 1

● = check bits

If there is a computer error and the parity bit changes due to computer error.

	1	2	3	4	5	6	7
code word	1	1	0	0	1	0	1
1	1		0		1		1
2		1	0			0	1
4				0	1	0	1

0 1 0 0 1 0 1

● = check bits

2. 1, 2, 4, 8, 16, 32, 64, 128, 256, 512, 1024, 2048

c) 9 check bits

d) 12 check bits

Question 5.

1. L1 - 3 cycles - 5% miss ratio

RAM - 180 cycles - ?

$0.95 \cdot 3 \text{ cycles} + 0.05 \cdot 180 \text{ cycles}$

$\Rightarrow 2.85 \text{ cycles} + 9.00 \text{ cycles}$

$\Rightarrow 11.85 \text{ cycles}$

2. L1 - 3 cycles - 5% miss ratio

RAM - 180 cycles - ?

L2 - 9 cycles - 20% miss ratio

L3 - 90 cycles - 30% miss ratio

$$\begin{array}{l} \text{L1 access} \\ 0.95 \cdot 3 \text{ cycles} \end{array} + \begin{array}{l} \text{L1 miss + L2 access} \\ 0.05 \cdot 0.8 \cdot 9 \text{ cycles} \end{array} + \begin{array}{l} \text{L1 miss, L2 miss, L3 access} \\ 0.05 \cdot 0.2 \cdot 0.7 \cdot 90 \text{ cycles} \end{array} + \begin{array}{l} \text{L1 miss, L2 miss, L3 miss, RAM} \\ 0.05 \cdot 0.2 \cdot 0.3 \cdot 180 \text{ cycles} \end{array}$$

$$\begin{array}{l} \text{L1 access : } 2.85 \text{ CPI} \\ \text{L2 access : } 0.36 \text{ CPI} \\ \text{L3 access : } 0.63 \text{ CPI} \\ \text{RAM : } 0.54 \text{ CPI} \\ \hline 4.38 \text{ CPI} \end{array}$$

- 3.
- L1 \longrightarrow 3 cycles \longrightarrow 10%
 - L2 \longrightarrow 9 cycles \longrightarrow 10%
 - L3 \longrightarrow 90 cycles \longrightarrow 15%

$$\begin{array}{l} \text{L1} \\ 0.90 \cdot 3 \text{ cycles} \end{array} + \begin{array}{l} \text{L2} \\ 0.10 \cdot 0.90 \cdot 9 \text{ cycles} \end{array} + \begin{array}{l} \text{L3} \\ 0.10 \cdot 0.10 \cdot 0.85 \cdot 90 \text{ cycles} \end{array} + \begin{array}{l} \text{RAM} \\ 0.10 \cdot 0.10 \cdot 0.15 \cdot 180 \text{ cycles} \end{array}$$

$$\begin{array}{l} L_1 = 2.7 \text{ CPI} \\ L_2 = 0.81 \text{ CPI} \\ L_3 = 0.765 \text{ CPI} \\ \text{RAM} = 0.27 \text{ CPI} \\ \hline 4.545 \text{ CPI} \end{array}$$

Exercise 4

Question 1:

1. 14356_{10}

	14356	11101010010100	\longrightarrow unsigned magnitude
0	7178	0011101010010100	\longrightarrow signed magnitude
0	3589	0011101010010100	\longrightarrow one's complement
1	1794	0011101010010100	\longrightarrow two's complement
0	897		
1	448	0011101010010100	
0	224	\rightarrow 1000000000000000	
0	117	1011101010010100	\longrightarrow excess 2^{n-1}
1	58		
0	29		
1	14		
0	7		
1	3		
1	1		
1	0		

2.	Positive number		Binary	Decimal
		Signed magnitude :	011111	31
		One's complement :	011111	31
		Two's complement :	011111	31
		Excess 2^5 :	111111	31

3.	Negative number		Binary	Decimal
		Signed magnitude :	111111	-31
		One's complement :	100000	-31
		Two's complement :	100000	-32
		Excess 2^5 :	000000	-32

Question 2:

1. $-33_{10} =$

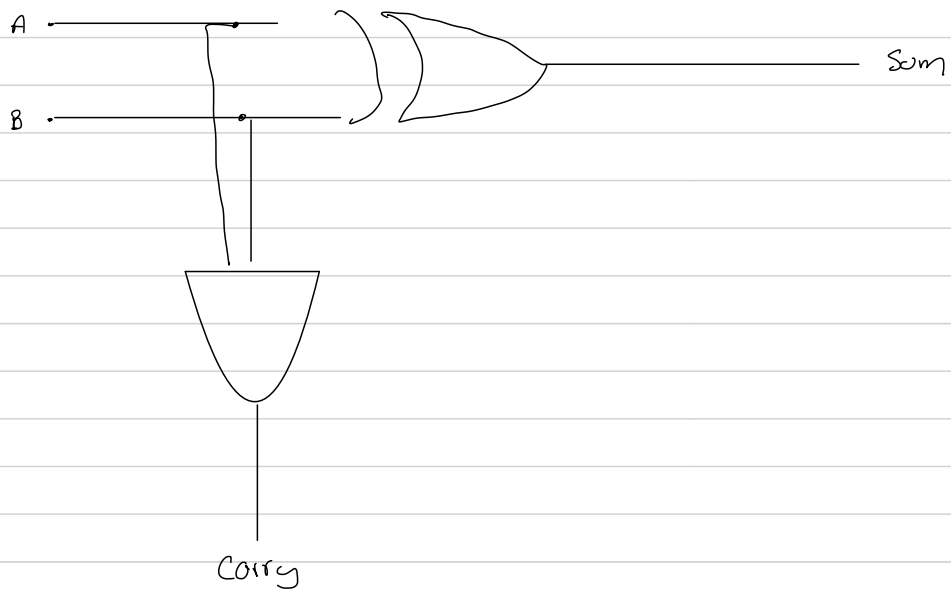
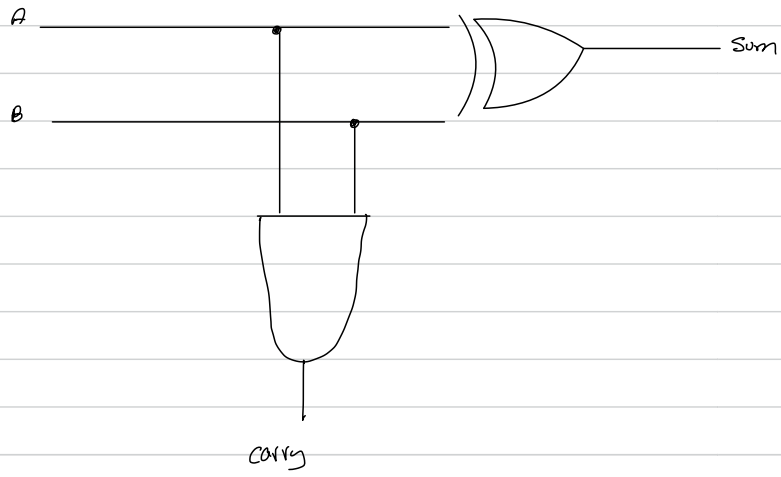
	33	
1	16	10100001 \rightarrow Original magnitude
0	8	11011110 \rightarrow Signed magnitude
0	4	11011110
0	2	+ 0000001
0	1	11011111 \rightarrow Two's complement
1	0	

	33	1001010 \rightarrow Original magnitude
0	32	01001010 \rightarrow Signed magnitude / One's complement
1	16	
0	8	
1	4	\therefore 11011111
0	2	01001010
0	1	00101001
1	0	00101001 $\rightarrow 2^0 + 2^3 + 2^5 \rightarrow 1 + 8 + 32 \rightarrow 41$ (Correct) & (no overflow)

Question 4.

1.	0x03	7
	0x04	5
	0x05	1
	0x06	-

1-bit half adder



Exercise 6

Question 2:

	a	b	c	p	q	r	e
1.	0	0	0	0	0	0	0
	0	0	1	1	1	1	0
	0	1	0	1	1	0	0
	0	1	1	1	0	1	0
	1	0	0	1	0	0	0
	1	0	1	0	1	1	1
	1	1	0	0	1	0	1
	1	1	1	0	0	1	1

00 01 10 11

b

0	0	0	0
0	1	1	1

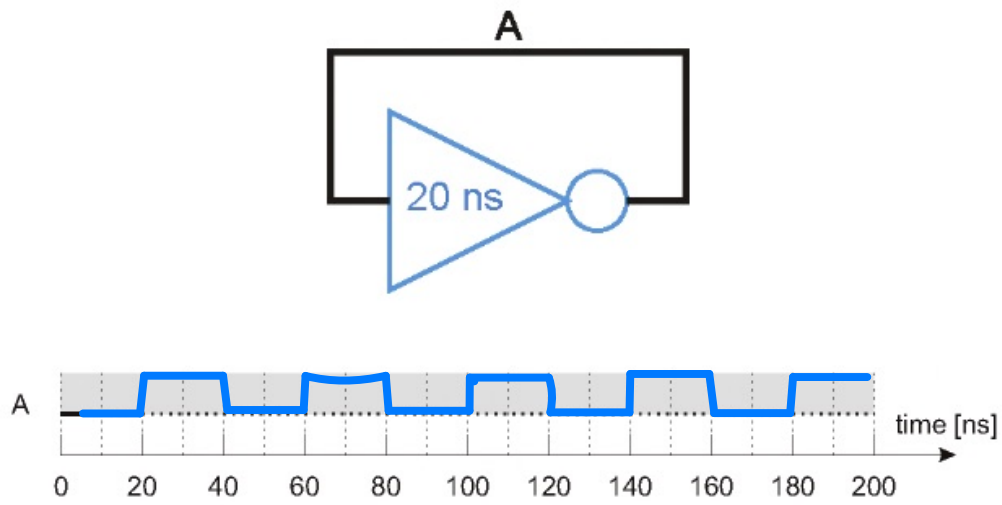
c

$$f(a,b,c) = ba + ca$$

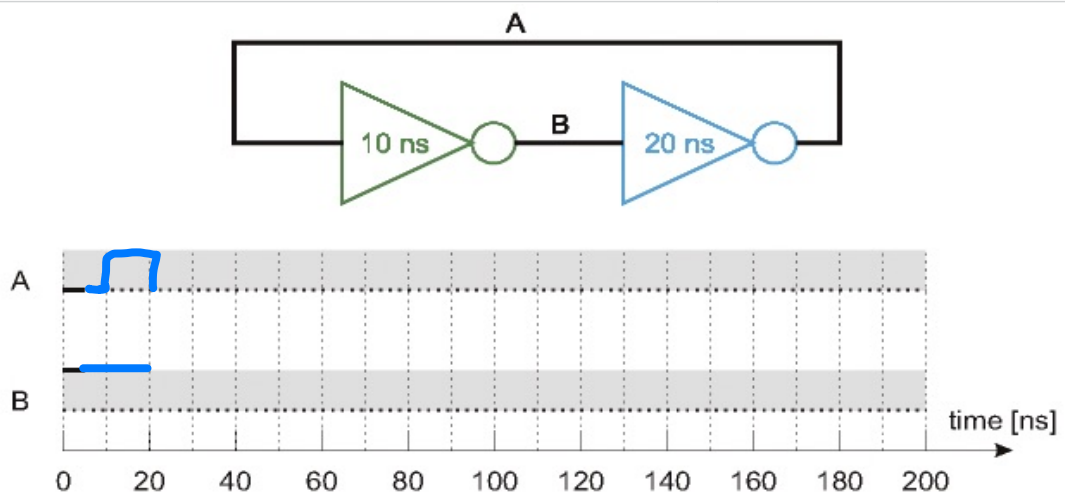
$$= \bar{a} \cdot (cb + \bar{c})$$

Exercise 5

1.



2.



Question 2:

Exercise 1

Question 1.

1. 17 in binary

	1	17
↑	0	8
	0	4
	0	2
	1	1
		0

$$\therefore 10001$$

2. 10010 in unsigned decimal

$$\Rightarrow \begin{array}{ccccc} 1 & 0 & 0 & 1 & 0 \\ 2^4 & 2^3 & 2^2 & 2^1 & 2^0 \end{array} = 2^4 + 2^1 = 16 + 2 = 18$$

3. 7 bits $\rightarrow 2^7 - 1 = 128 - 1 = 127$

4. 1010 + 0110

$$\begin{array}{r} \Rightarrow 1010 \\ \quad 0110 \\ \hline 10000 = 16 \end{array} \quad \begin{array}{l} \rightarrow 2^3 + 2^1 = 10 \\ \rightarrow \underline{2^2 + 2^1} = 6 \\ \quad 16 \end{array}$$

Exercise 2

Question 1.

3. Program consisting of 20 instruction

1st instruction = 7 nanoseconds
rest 19 instructions = 19 nanoseconds

\therefore 26 nano seconds

5. 20 instruction with Pipelining = 26 ns
20 instruction without Pipelining = 20×7
= 140 ns

$$\text{Speed Up Ratio} = \frac{140}{26} = 5.38$$

we should clock non-pipelined data-path with frequency 5.38x faster than the pipelined one.

Question 2

1.	0	2	0	7	5	2
	0	1	0	3	7	6
	0		5	1	8	8
	0		2	5	9	4
	1		1	2	9	7
	0			6	4	8
	0			3	2	4
	0			1	6	2
	1				8	1
	0				4	0
	0				2	0
	0				1	0
	1				5	
	0				2	
	1				1	
					0	

101000100010000 - unsigned

0101000100010000 - signed 16-bit

0101000100010000 - one's complement

0101000100010000 - Two's complement

0101000100010000

+ 1000000000000000

1101000100010000 - Excess- $2^{16}-1$

```

      101000100010000
    + 010100010001000
    -----
    1101000100010000
          
```

Question 3

1.	1 4 3 5 6	
0	7 1 7 8	1 1 0 0 0 0 0 0 1 0 1 0 0 → unsigned
0	3 5 8 9	0 0 1 1 1 0 0 0 0 0 0 1 0 1 0 0 → signed
1	1 7 9 4	0 0 1 1 1 0 0 0 0 0 0 1 0 1 0 0 → 1's complement
0	8 9 7	0 0 1 1 1 0 0 0 0 0 0 1 0 1 0 0 → 2's complement
1	4 4 8	
0	2 2 4	0 0 1 1 1 0 0 0 0 0 0 1 0 1 0 0
0	1 1 2	+ 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0	5 6	1 0 1 1 1 0 0 0 0 0 0 1 0 1 0 0 → Excess 2^{15}
0	2 8	
0	1 4	
0	7	
1	3	
1	1	
1	0	

2. Largest + Number in 6 bits

Signed Number:

Decimal: $2^{5-1} = 32-1 = 31$

Binary: 011111

One's Complement:

Decimal: $2^{5-1} = 32-1 = 31$

Binary: 011111

Two's Complement:

Decimal: $2^{5-1} = 32-1 = 31$

Binary: 011111

Excess 2^{n-1} :

Decimal: 31

Binary: 111111

3. Largest - number in 6 bits

Signed Magnitude:

decimal : $2^5 - 1 = -31$

Binary : 111111

One's Complement:

decimal : -31

Binary : 100000

Two's Complement:

decimal : -32

Binary : 100000

Excess 2^5 :

decimal : -32

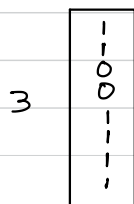
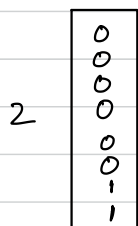
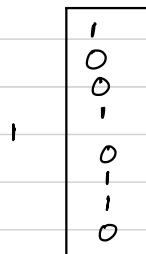
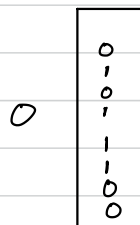
Binary : 000000

Exercise 3

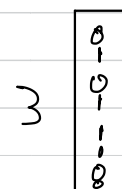
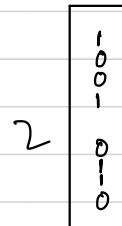
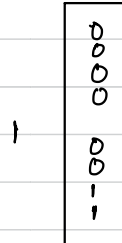
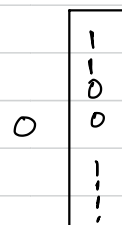
Question 1

1.

Big Endian



Little Endian



2.

WELCOME CLASS 2021
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18

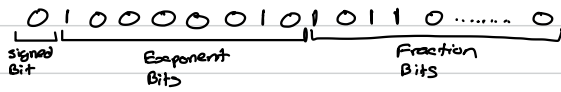
R₀: - | E | M | O | C | L | E | W |

R₁: 0 | 2 | - | S | S | A | L | C |

R₂: x | x | x | x | x | x | 2 | 1 |

Floating Point

1.



$$\begin{array}{r}
 0.0625 \\
 16 \overline{) 100} \\
 \underline{96} \\
 0040 \\
 \underline{32} \\
 0800 \\
 \underline{800} \\
 0
 \end{array}$$

I] $s = 0$ (positive bit)

II] $(10000010)_2 = 2^1 + 2^7 = 2 + 128 = 130$

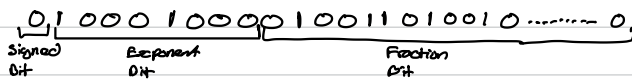
exp. bias = $130 - 128 = 2 = e$

III] $m = 2^{-1} + 2^{-3} + 2^{-4}$

$$\begin{aligned}
 &= \frac{1}{2} + \frac{1}{8} + \frac{1}{16} \\
 &= 0.5 + 0.125 + 0.0625 \\
 &= 0.6875
 \end{aligned}$$

$\therefore 1.6875 \times 10^2$

2.



I] $e = 2^3 + 2^7 = 8 + 128 = 136$

exp. bias = $136 - 128 = 8$

II] $2^{-2} + 2^{-5} + 2^{-6} + 2^{-8} + 2^{-11}$

$\Rightarrow \frac{1}{4} + \frac{1}{32} + \frac{1}{64} + \frac{1}{256} + \frac{1}{2048}$

$\Rightarrow \frac{2^9 + 2^6 + 2^5 + 2^3}{2048}$

$\Rightarrow \frac{512 + 64 + 32 + 8}{2048} \quad 768 \quad 80$

$\Rightarrow \frac{616}{2048} \quad 308 \quad 154 \quad 77$

$$\begin{array}{r}
 256 \overline{) 0.300} \\
 \underline{770} \\
 768 \\
 \hline
 002000
 \end{array}$$

$\frac{616}{2048} = \frac{77}{256}$