# HAROLD G. MAMITAG, ECE

03/2019 - 06/2020



12/2012 - 04/2013

**Industrial Instruments Operator** Texas Instruments, Baguio City

- Successfully set-up Solaris (Unix) server used for IC test production line.
- Conducted basic network support such as patch cabling, cable management.
- Performed backup of IC test data from TI Baguio City to TI Clark for safe keeping.

# SUMMARY

An accomplished Electronics and Communications Engineer with a total of seven years experience in the Semiconductor and Telecommunications industry in a multinational and multicultural environment. Highly proficient in software feature design and testing including the setup of hardware and software infrastructures. A persistent and innovative team leader who collaborates well, solves tough problems, and delivers quality results.

## WORK EXPERIENCE

# Architecture and **Specifications Engineer**

Nokia, Quezon City

- Lead the incremental delivery of Specifications Team's backlog as a Scrum Master and facilitated the scrum team events.
- Improved the Feature Commitment Efficiency (FCE) from 50% to 90% per iteration, exceeding the target of 88%.
- Migrated the WCDMA Capacity and Performance specifications from IBM DOORS to Sphinx which lowered the operational cost for proprietary documentation software.
- Created specifications for the WCDMA downlink digital filter enabling the 2G, 3G, and 4G mobile radio access technologies to share the licensed 2100 MHz radio spectrum.

# Research and Development Engineer 01/2017 - 02/2019 Nokia, Quezon City

- Subject matter expert for Transmit Accelerator Coprocessor (TAC)
- Automated the manual software pre-integration tasks using Jenkins.
- Maintained the good health of Continuous Integration (CI) jobs for faster software integration and co-authored CI traffic light to facilitate downtime
- Updated the software development process upon migration from SVN to Git enabling the faster delivery of DSP software.

#### **Test Product Engineer**

05/2013 - 12/2016

Texas Instruments, Baguio City

- Successful offload and ramp up of automotive power management Integrated Circuit (IC), USB (Universal Serial Bus) and Airbag ICs in test log points.
- Improved yield for the high lot rejection rate ICs from 70% to 90% by screening out the defects in probe.
- Simplified IC test flow from 100% burn-in to only sampling burn-in or completely no burn-in which reduced the operational cost for non-value-added testing.

# SKILLS

#### OS and Virtualization

- Ubuntu Linux, RedHat, Solaris
- Amazon Web Services
- VirtualBox

#### Web Development

- Django
- Nginx/Apache web servers
- MySQL Database

#### **Programming Languages**

- Python programming
- C Programming

#### **Software Documentation**

- ReStructuredtext and Sphinx
- Atlassian Software: Jira, Confluence
- IBM DOORS
- Universal Mark-up Language (UML)
- MS Office: Word, Excel, Powerpoint

#### Software version control and test automation

- Git, Gerrit, GitLab, GitHub, SVN
- Jenkins CI
- Robot Test Framework
- Bash and DOS scripting

#### **Quality Control tools**

DMAIC, Pareto, Histogram, Scatter Plots

#### IC Automated Test Equipment

Eagle ETS 384, Eagle ETS 81, Teradyne IFlex, Teradyne UltraFlex

#### **IC Handlers**

Strip test, Delta Edge, Yokogawa HS-2000, Advantest M-4841

## EDUCATION

# **Electronics and Communications** Engineering

06/2007 - 04/2012

Saint Louis University, Baguio City

#### LICENSE

#### **Electronics Engineer**

11/2012 - Present

License # 0054008

Professional Regulation Commission