

# HAROUN HABEEB

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## EDUCATION

<b>Indian Institute of Technology, Delhi</b>	2017
B. Tech in Computer Science and Engineering	GPA: 9.15/10

## RESEARCH EXPERIENCE

<b><u>Using Symmetries in PGMs to reduce computation in MAP inference</u></b>	2016
<i>Under supervision of Dr. Mausam and Dr. Parag Singla, Dept. of CSE, IIT Delhi</i>	
<ul style="list-style-type: none"><li>Created notions of symmetry to achieve speedups in MAP inference on MRF</li></ul>	

## INTERNSHIPS

<b><u>Summer Analyst, Deutsche Bank Centre, Mumbai</u></b>	Summer 2016
<b>Project:</b> Extraction of Correlation Risk premium from Asynchronous Markets	
<ul style="list-style-type: none"><li>Analysed behavior of correlation between asynchronous equities. Developed necessary tools.</li></ul>	

## SCHOLASTIC ACHIEVEMENTS

<ul style="list-style-type: none"><li>Received <b>Merit Scholarship</b> for Top 7% GPA in a class of 100 for 2 semesters</li></ul>	2016
<ul style="list-style-type: none"><li>Received <b>Summer Undergraduate Research Award</b>.</li></ul>	2015
<ul style="list-style-type: none"><li>Campus Runner-up for Philip's Blueprint business plan competition .</li></ul>	2014
<ul style="list-style-type: none"><li>Secured <b>All India Rank 61</b> among 1,400,000 students in the Joint Entrance Exam, IIT-JEE</li></ul>	2013
<ul style="list-style-type: none"><li>Received <b>Gold Medal</b> and Certificate of Merit (top 35) at INPhO, National Olympiad</li></ul>	2012
<ul style="list-style-type: none"><li>Received KVPY Fellowship and NTSE Scholarship from Govt. of India</li></ul>	2011,2009

## TECHNICAL PROJECTS

<b><u>Currency Note Identification</u></b> ( <a href="#">link</a> , course project)	Fall 2016
<ul style="list-style-type: none"><li>Implemented LDKL for classifying notes from photos and studied model compression.</li></ul>	
<b><u>Implementing Virtualization in JOS</u></b> ( <a href="#">link</a> , course project)	Spring 2016
<ul style="list-style-type: none"><li>Implemented hardware virtualization for 64 bit MIT-JOS written in C, tested using bochs.</li><li>Virtualization using Intel VT-x through VMCS with Extended Page Table support.</li></ul>	
<b><u>Parallelized Sudoku Solver</u></b> ( <a href="#">link</a> , course project)	Spring 2016
<ul style="list-style-type: none"><li>Implemented parallelized versions of brute force solver, A* search using C, OpenMP.</li></ul>	
<b><u>Game Playing AI - Quoridor</u></b> ( <a href="#">link</a> , course project)	Spring 2015
<ul style="list-style-type: none"><li>Implemented a Quoridor player using MCTS and RL in C++.</li></ul>	
<b><u>3D Peer to Peer Multiplayer Game</u></b> ( <a href="#">link</a> , course project)	Spring 2015
<ul style="list-style-type: none"><li>Developed 3D P2P racing-FPS game featuring AI and robust network using UDP.</li></ul>	

## EXTRA CURRICULAR ACTIVITIES

<b><u>Chief Editor, Board for Student Publications, IIT Delhi</u></b> (Received Best Chief Editor Award)	2015-16
<b><u>Vice Chair, ACES-ACM, Official Chapter of ACM at IIT-Delhi</u></b>	2015-16
<b><u>Debating</u></b> (National, Member of <b>Advisory Panel</b> for the Debating Society, IIT Delhi.)	2013-present
<ul style="list-style-type: none"><li>Invited adjudicator, N.V. Thadani Memorial debate. Best Adjudicator, Manzar.</li><li>Winner, Clash of Titans debating tournament.</li></ul>	

## TECHNICAL SKILLS

**Languages:** Python, C/C++, MATLAB, R, Java, SML. **Libraries:** Mallet, OpenGM, Theano, scikit-learn, boost