

# ECG Task List/Project Plan

Assignment Component	Team Member/s	Estimated Time to Complete	Team Approval?
Requirements	JB, HM, DS, HS		Y
Acceptance Tests	JB, HM, DS, HS		Y
High Level Design	JB, HM, DS, HS		Y
Low Level Design	JB, HM, DS, HS		Y
Task List, Project Plan	JB		Y
Boot Screen	HS	1W	Y
Menu	HM, HS	1W	Y
A-D (Polling/Timer/Interrupt)	DS	1W	Y
Sliders	HM	1W	Y
Buttons	HM	3D	Y
Queues	HM	2D	Y
DMA	DS	3W	Y
Scheduler (RR/Time Slice/RT)	HM, JB	3W	Y
POST	JB, HM	1W	Y
Mutex Data Protection	HM, DS	3W	Y
Data Logging	HS, JB	3W	Y
Database	HS, JB	3W	Y
Filtering	DS, HS	3W	Y
Display Pulse	HM	3W	Y
Testing	JB, HM, DS, HS	3W	Y
Code Reviews	JB, HM, DS, HS	N/A	Y
Meetings	JB, HM, DS, HS	N/A	Y

# BPM Task List/Project Plan

Assignment Component	Team Member/s	Estimated Time to Complete	Team Approval?
Requirements	JB, HM, DS, HS		Y
Acceptance Tests	JB, HM, DS, HS		Y
High Level Design	JB, HM, DS, HS		Y
Low Level Design	JB, HM, DS, HS		Y
Task List, Project Plan	JB		Y
I2C	DS	3W	Y
HMI via Wireless	JB	3W	Y
Browser Menu	HM, HS	1W	Y
Queues	HM	1W	Y
MAX3000 Sensor Interrupts	DS, HS	3W	Y
MAX3000 Internal Buffer	DS, HS	3W	Y
Scheduler (RR/Time Slice/RT)	JB, HM	2W	Y
POST	JB, HM	1W	Y
Mutex Data Protection	HM, DS	3W	Y
Data Logging	HS, JB	3W	Y
Database	HS, JB	3W	Y
Filtering	DS, HS	3W	Y
Display Pulse	HM	3W	Y
Testing	JB, HM, DS, HS	3W	Y
Code Reviews	JB, HM, DS, HS	N/A	Y
Meetings	JB, HM, DS, HS	N/A	Y