

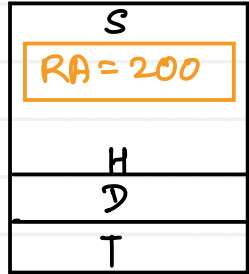


**Sunbeam Institute of Information Technology**  
**Pune and Karad**

## **Module - Embedded Operating System**

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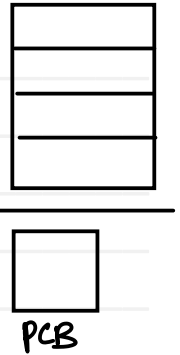
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```
main() {
  ① signal(SIGINT, sig_handler);
  while(1) {
    ③ f → 200
    ⑩ }
}
```

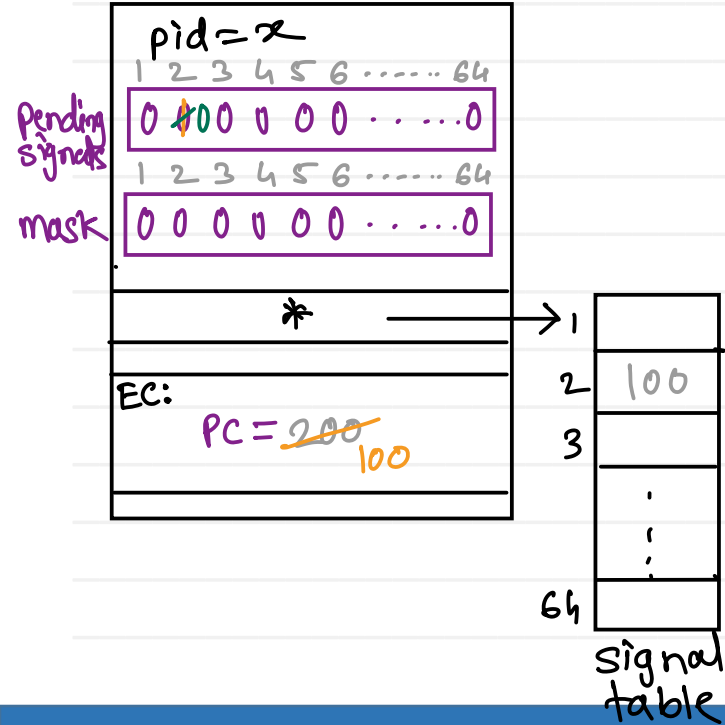
```
100 sig_handler() {
  ⑨ }
}
```

```
main() {
  ② kill(x, SIGINT);
}
```



```
interrupt_handler() {
  ④ 1) save execution context of
    2) find address of ISR from IVT
    3) call ISR()
  ⑤ }
  ⑥ 4) pid = CPU_scheduler();
  ⑧ 5) signal_delivery(pid);
  6) CPU_dispatcher(pid);
}
```

```
signal_delivery(pid) {
  ⑦ 1) find PCB of process (pid)
    2) find all pending signals
    3) for each pending signal
      a. create dummy stack frame
        on user space stack of
        process, so that process
        will resume where it is
        paused.
      b. update execution context in
        PCB. pc = addr of signal handler
      c. clear pending bit of signal
}
```





Thank you!!!

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