

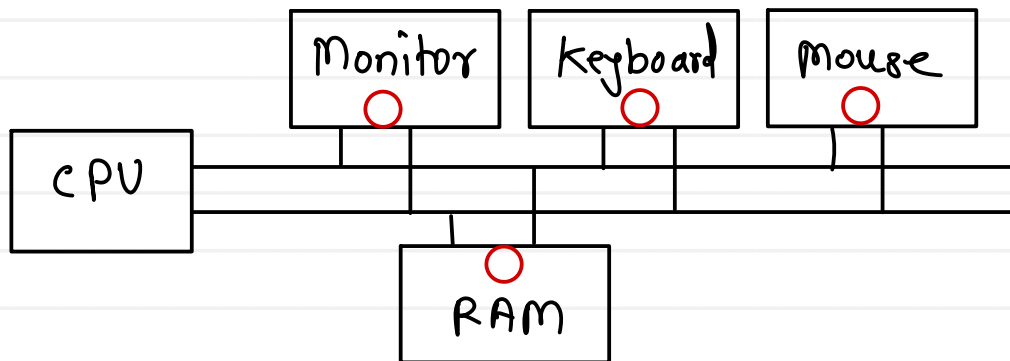


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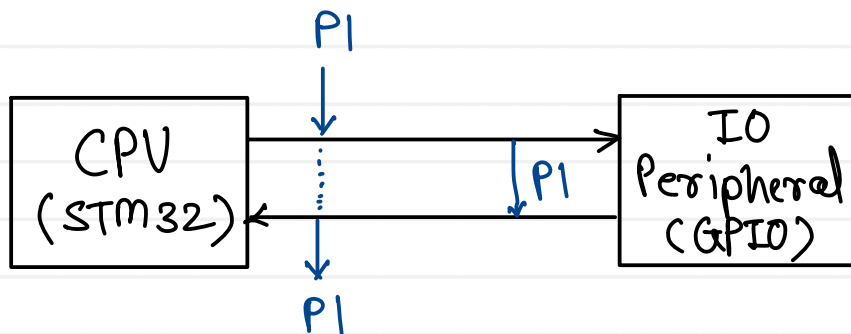
Module - Embedded Operating System

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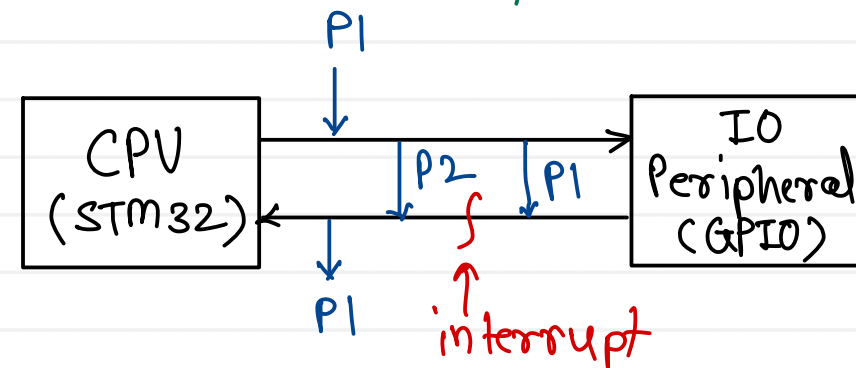
Synchronous (single task)
H/w technique : Polling

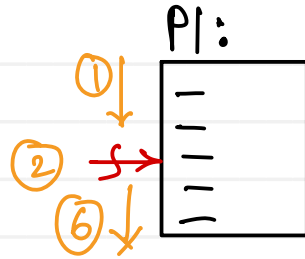


IO Device table

Devices	Status (Idle/Busy)	waiting queue
Mouse	Idle
Keyboard	Busy

Asynchronous (multi-tasks)
H/w technique : Interrupt

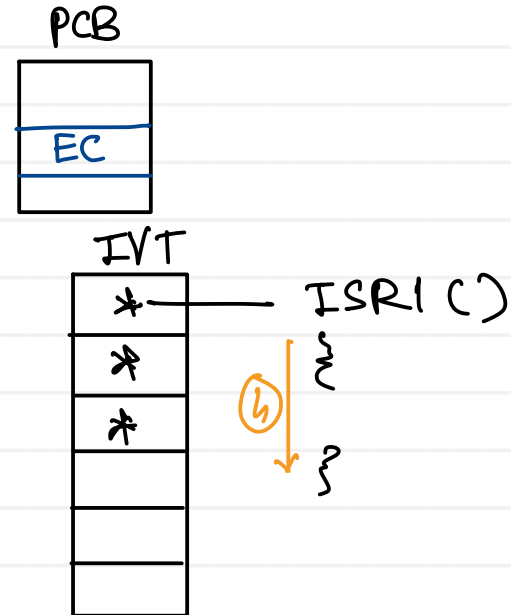


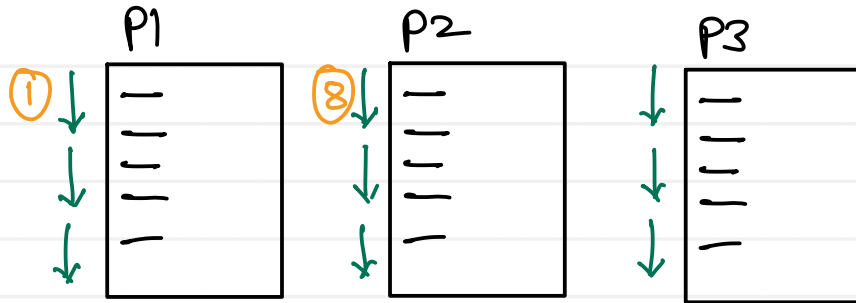


```

interrupt_handler() {
    1) Save execution context of current
       running process into its PCB
    2) find address of ISR from IVT
    3) call ISR
    4) restore execution context of paused
       process from its PCB
}

```

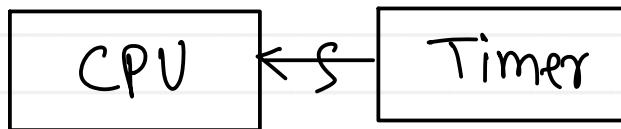




interrupt_handler() {

- 1) Save execution context of running process in its PCB.
 - 2) Find address of ISR from IVT
 - 3) call ISR
 - 4) pid = CPU_scheduler()
 - 5) CPU_dispatcher(pid)
- }

ISR() {
?
}



-timer period can be configured at the time kernel customization into kernel configuration parameters

CONFIG_HZ = 100/250/300/1000
1000 interrupts/sec

```
int CPU_scheduler() {
    if Left time > 0;
        select same process to
        execute on CPU
    else
        select next process to
        execute on CPU
    return pid;
}
```

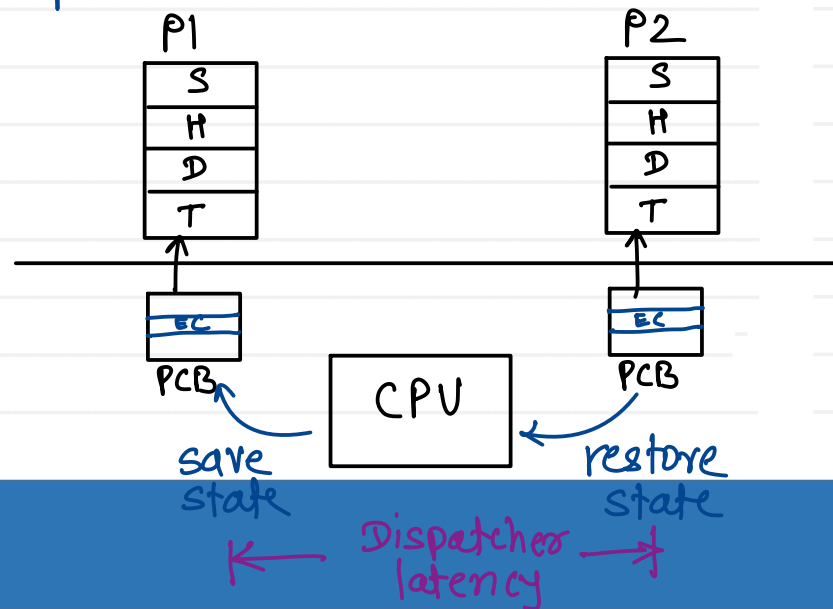
```
void CPU_dispatcher(pid) {
    restore execution context
    of selected process on CPU
}
```

Execution context :

- values of CPU registers
- Execution context is stored into PCB of each process

Context switching :

- changing the process of CPU
- save execution context of current running process & restore execution context of next process, is called as context switching.
- CPU Dispatcher does the context switching



- time required to change the process of CPU is called as kernel response time

- Dispatcher latency :
time from save state to restore state.

System call

Program :

```

_
_
_
exit(5)
_
_

```

user
mode

```

_exit(5) {
    mov r0, #5
    mov r7, #1
    swi
}

```

library
functions

system
call
API

library
(libc)

- system mode is always changed on interrupts

User mode $\xleftrightarrow{\text{interrupt}}$ kernel mode

ARM : 5 mode bits
7 modes
6 - privileged
1 - unprivileged

x86 : 2 mode bits
00 - ring 0 more } privileged
01 - ring 1 }
10 - ring 2 less
11 - ring 3 - unprivileged

kernel
mode

```

swi_handler( ) {
    1) Save execution context of
       running process.
    2) get syscall number from
       r7 register & find address
       of actual system call from
       system call table.
    3) call system call implementation
    4) restore execution context of
       running process
}

```

system
call
table

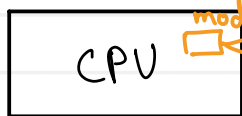


```

sys_exit( )
{
}

```

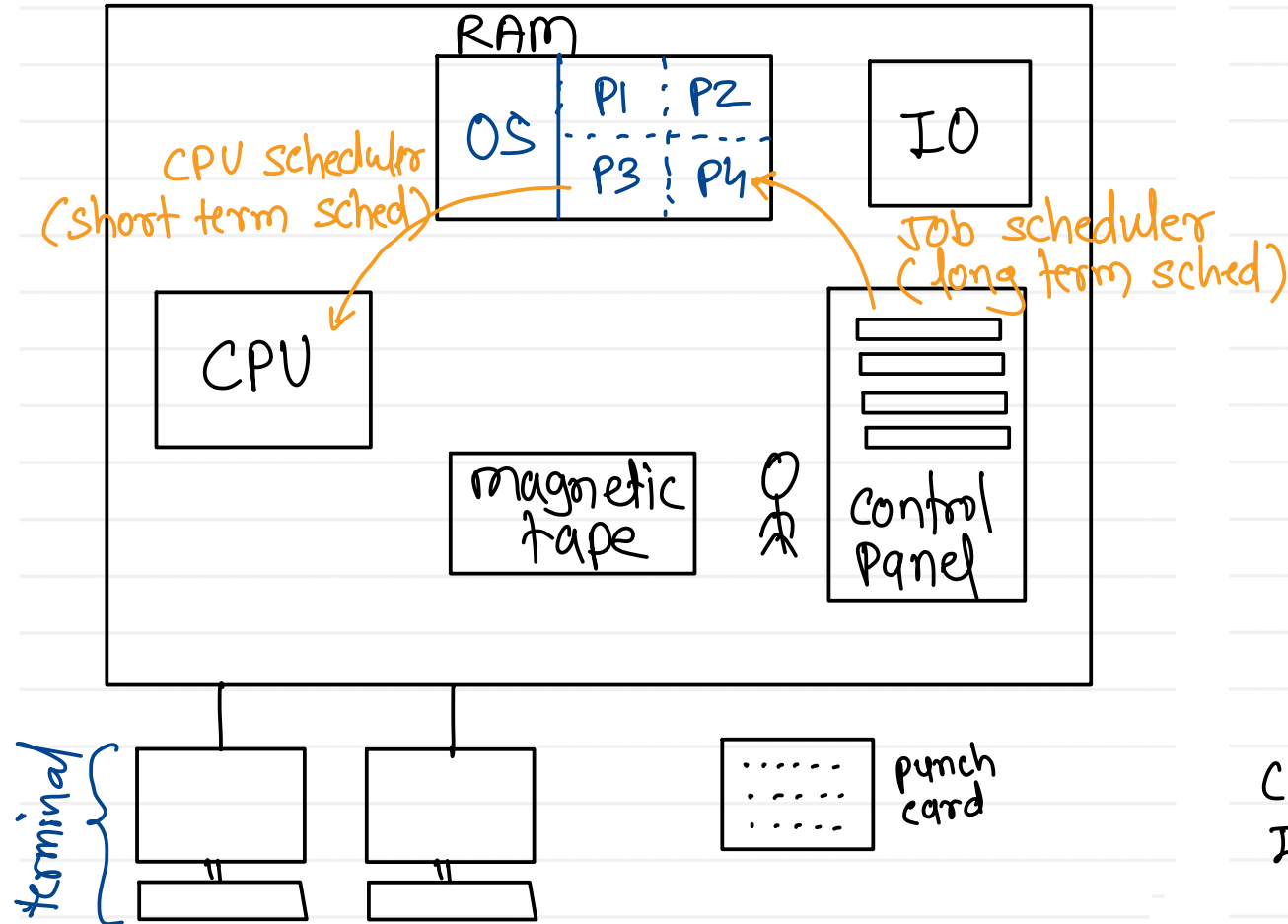
system
call
implementation



mode 0 kernel / system / monitor mode (privileged)
1 user mode (unprivileged)

- all instructions are allowed
can access all CPU registers

Types of Operating system



1) Resident monitor

2) Batch system

3) Multiprogramming system:

- multiple programs are loaded into memory.

Degree of multiprogramming

- no. of programs loaded inside memory.

CPU burst : time spend by process on CPU

IO burst : time spend by process on IO

$\text{CPU burst} > \text{IO burst}$; **CPU bound process**

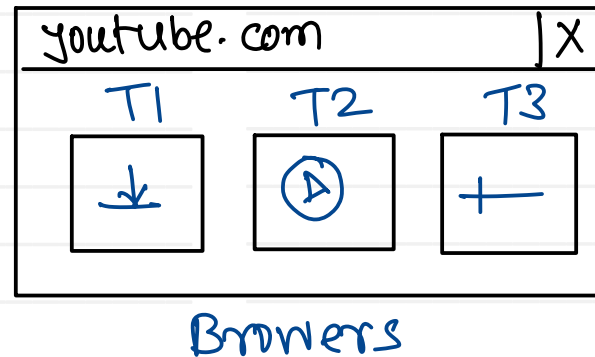
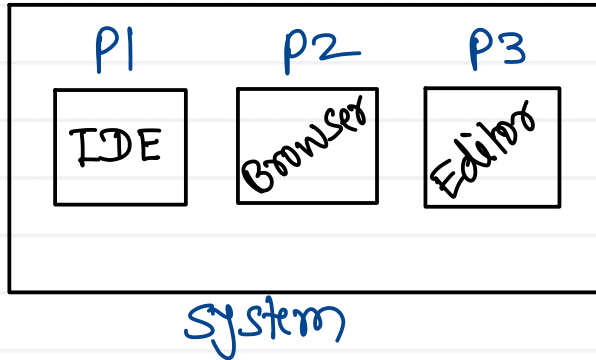
$\text{IO burst} > \text{CPU burst}$: **IO bound process**

- mixture of CPU bound & IO bound processes is loaded inside RAM

4) Time sharing system: (multitasking)
- CPU time is shared in all the processes
of memory
response time < 1 sec

- there are two types of multitaskings

1. Process based multitasking 2) thread based multitasking (multithreading)



- within system multitasking - within process multitasking

s) Multiuser system :

- multiple terminals are connected to system
(keyboard + monitor)

e) Multiprocessing system

- multiple CPUs are putted together in single chip. such chips are called as multiprocessor / multicore
- OS can schedule multiple processes for multiple cores, means multiple instructions will be processed parallel
 - 1) Symmetric multiprocessing
 - 2) Asymmetric multiprocessing



Thank you!!!

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