

## **Module - Micro controller Programming and Interfacing**

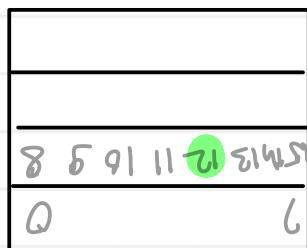
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$$\begin{array}{r}
 \text{bit-word-addr} = 0x4240 \ 0280 \\
 \hline
 \text{bit-number}_4 = 0x0000 \ 0010 \\
 \text{byte-offset} * 32 = 0x0040 \ 02A0 \\
 \text{bit-band-base} = 0x4200 \ 0000
 \end{array}$$



$$\text{bit} - \text{bit} = 12$$

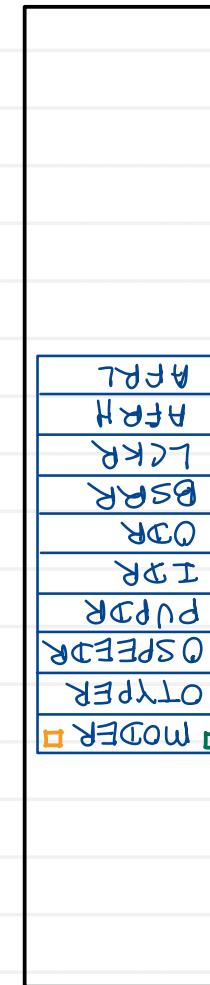
$$\begin{aligned}
 \text{bit-word-addr} &= \text{bit-band-base} \\
 &\quad + (\text{bit-number}_4 * 4) \\
 &\quad + (\text{byte-offset} * 32)
 \end{aligned}$$



Bit alias  
Band alias



0x4240 0000



0x4010 0000

AFRL
AFRH
LCKR
BSRR
ODR
IDR
PUPDR
USPER
OTYPE
MODER

14  
10  
0C  
08  
04  
0x4002 0000

Bit  
Band

$$\begin{array}{r} 100 \\ \times 4 \\ \hline 400 \\ + 128 \\ \hline 528 \end{array}$$

$$\begin{array}{r} 100 \\ \times 4 \\ \hline 400 \\ + 32 \\ \hline 160 \end{array}$$

bit-number \* 4 =  $1 \times 4 = 4$   
 byte-size \* 32 =  $4 \times 32 = 160$   
 bit-band-aliging =  $160$

$$\begin{array}{r} 100 \\ \times 4 \\ \hline 400 \\ + 32 \\ \hline 32 \end{array}$$

bit-number \* 4 =  $1 \times 4 = 4$   
 byte-size \* 32 =  $1 \times 32 = 32$   
 bit-band-aliging =  $32$

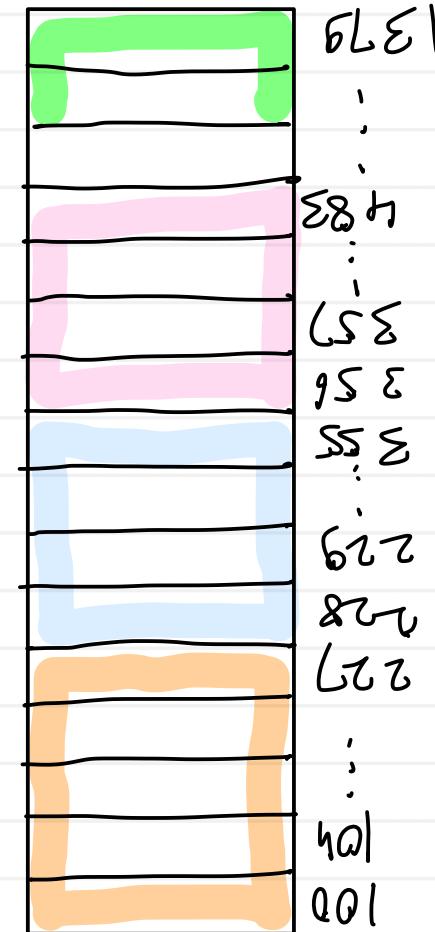
Model - bit = 9

128

$$\begin{array}{r} 100 \\ \times 4 \\ \hline 400 \\ + 32 \\ \hline 432 \end{array}$$

bit-number \* 4 =  $7 \times 4 = 28$   
 byte-size \* 32 =  $0 \times 32 = 000$   
 bit-band-aliging =  $100$

Model - bit = 7



Bit band alias

00	7	0	15	16	23	31	36
----	---	---	----	----	----	----	----

Model	00	04	08	12	16	20	24	28	32	36
AERL										
AFRH										
LCKR										
BSRR										
ODR										
IDR										
PUPDR										
DSPEDR										
QTPER										
Model	00	04	08	12	16	20	24	28	32	36

beq safty;  
o'glo CMPL  
[Lx] 'ge A 45  
LDA 47, =flag

≡

(0 == flag) while

while(1){flag;

3Q-

beq safty;  
o'glo CMPL  
[Lx] 'ge A 45  
LDA 47, =flag

≡

(0 == flag) while

if flag;

3Q-

beq safty;  
o'glo CMPL  
[Lx] 'ge A 45  
LDA 47, =flag

≡

(0 == flag) while

QQ-

## Volatile keyword

Optimization:- O(none)

void EXITIO\_IrqHandler(void) {  
int flag = 0;  
if(flag == 0:  
void EXITIO\_IrqHandler(void) {  
int flag = 0;

MOV ZF, flag  
JNE [L1], [L1]  
: ;  
MOV ZF, flag  
JNE [L1], [L1]

while(1){  
int main(void){  
while(1);  
}

MOV ZF, flag  
JNE [L1], [L1]  
start: MOV ZF, flag  
beq start  
CMP ZF, #0  
JNE [L1], [L1]

~~extrem softwar constucts~~

extrem constucts  
make variable scope is limited (function / file)  
extrem : variaable scope is through out the program  
constuct : make variaable local to compiller  
extrem softwar constucts

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Thank you!!!

