

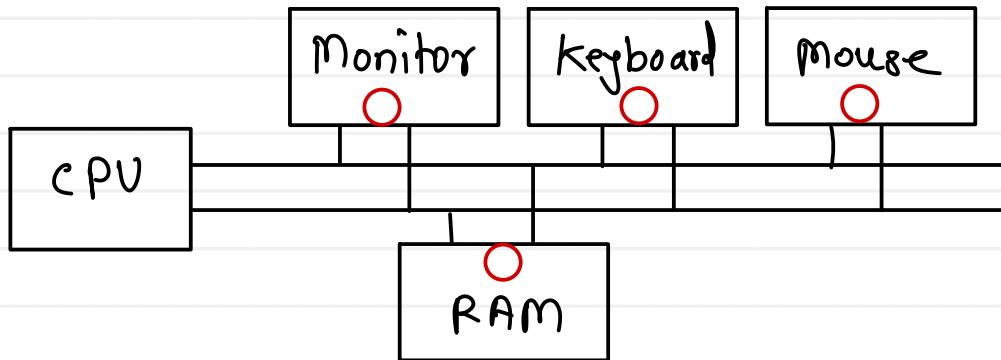


**Sunbeam Institute of Information Technology
Pune and Karad**

Module - Embedded Operating System

Trainer - Devendra Dhande
Email – devendra.dhande@sunbeaminfo.com

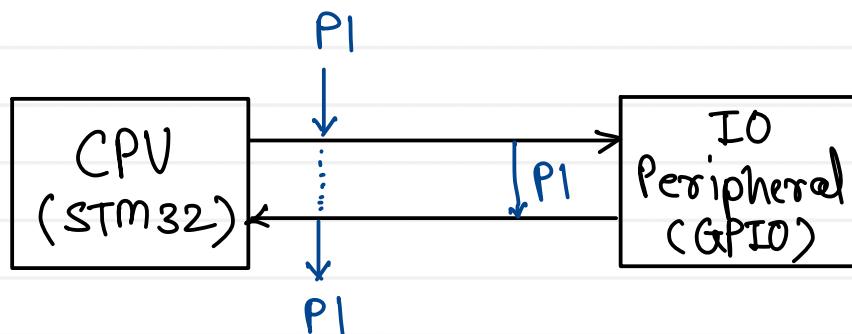
Types of I/O



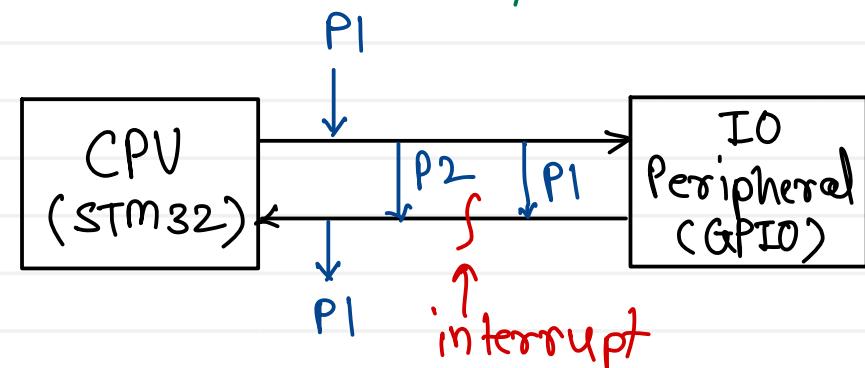
IO Device table

Devices	Status (Idle/Busy)	Waiting queue
Mouse	Idle	-----
Keyboard	Busy	-----

Synchronous (single task)
H/W technique : Polling

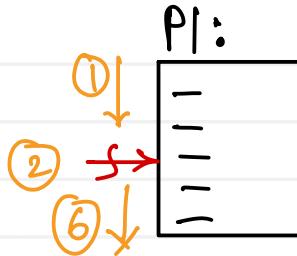


Asynchronous (multi-tasks)
H/W technique : Interrupt

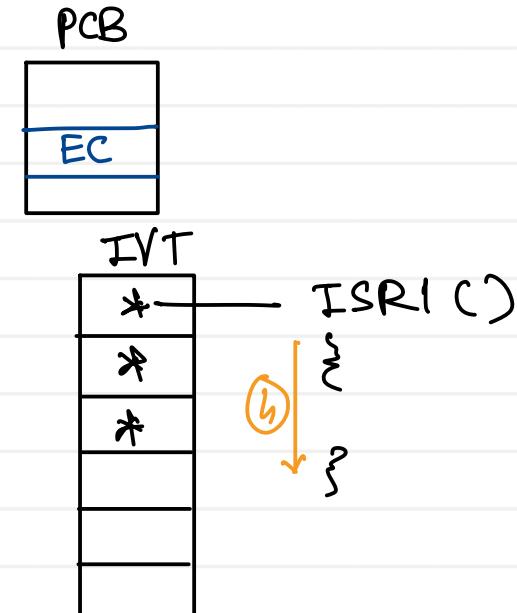


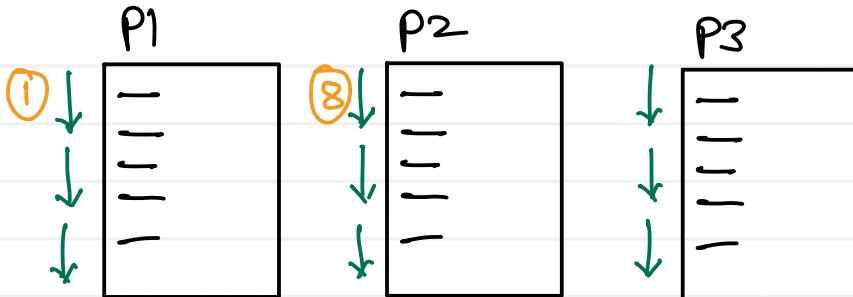


Interrupt handling



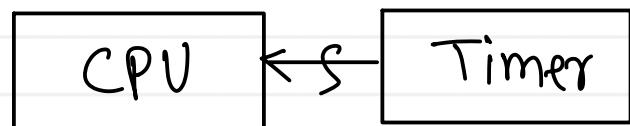
interrupt_handler() {
 ③ 1> save execution context of current
 running process into its PCB
 2> find address of ISR from IVT
 3> call ISR
 ⑤ 4> restore execution context of paused
 process from its PCB
}





```
interrupt_handler( ) {
```

- 1> Save execution context of running process in its PCB .
- 2> Find address of ISR from IVT
- 3> call ISR
- 4> pid = CPU_Scheduler()
- 5> CPU_Dispatcher(pid)



-timer period can be configured at the time kernel customization into Kernel configuration parameters

$$\text{CONFIG_HZ} = 100 / 250 / 300 / \underline{1000}$$

1000 interrupt/sec

```
int CPU_Scheduler( ) {
```

if Left time > 0 ;
select same process to execute on CPU
else
select next process to execute on CPU
return pid;

```
void CPU_Dispatcher( pid ) {
```

restore execution context
of selected process on CPU

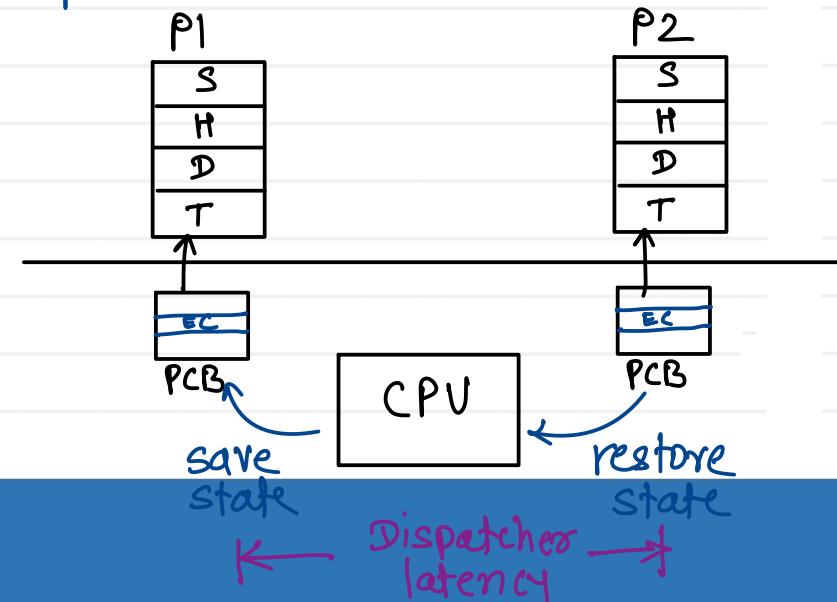
Context switching

Execution context :

- values of CPU registers
- Execution context is stored into PCB of each process

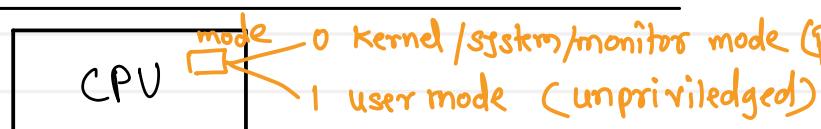
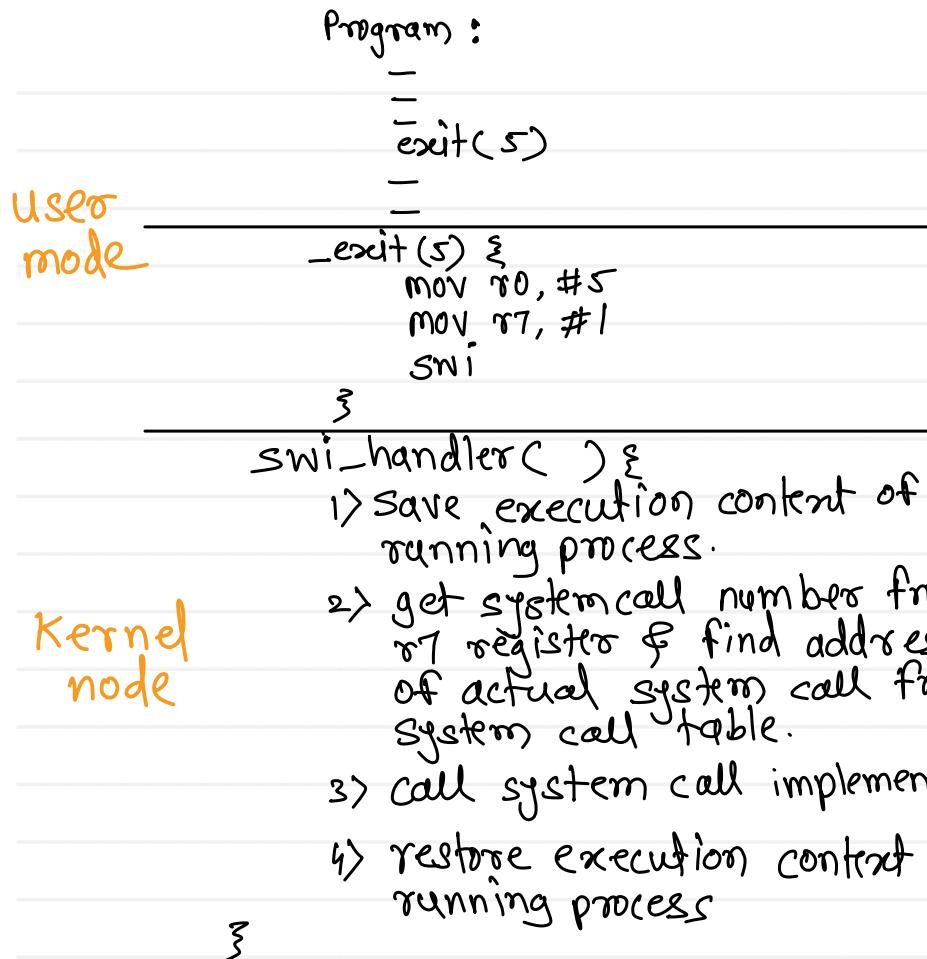
Context switching :

- changing the process of CPU
- save execution context of current running process & restore execution context of next process, is called as context switching.
- CPU Dispatcher does the context switching



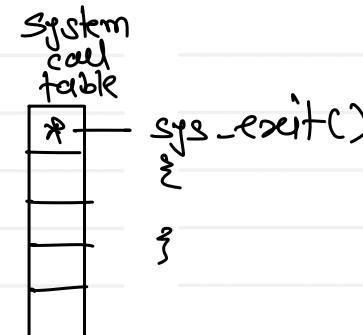
- time required to change the process of CPU is called as kernel response time
- Dispatcher latency : time from save state to restore state.

System call



library functions

System call API



library (libc)

system call implementation

- system mode is always changed on interrupts

User mode $\xrightarrow{\text{interrupt}}$ Kernel mode

ARM : 5 mode bits

7 modes

6 - privileged

1 - unprivileged

X86 : 2 mode bits

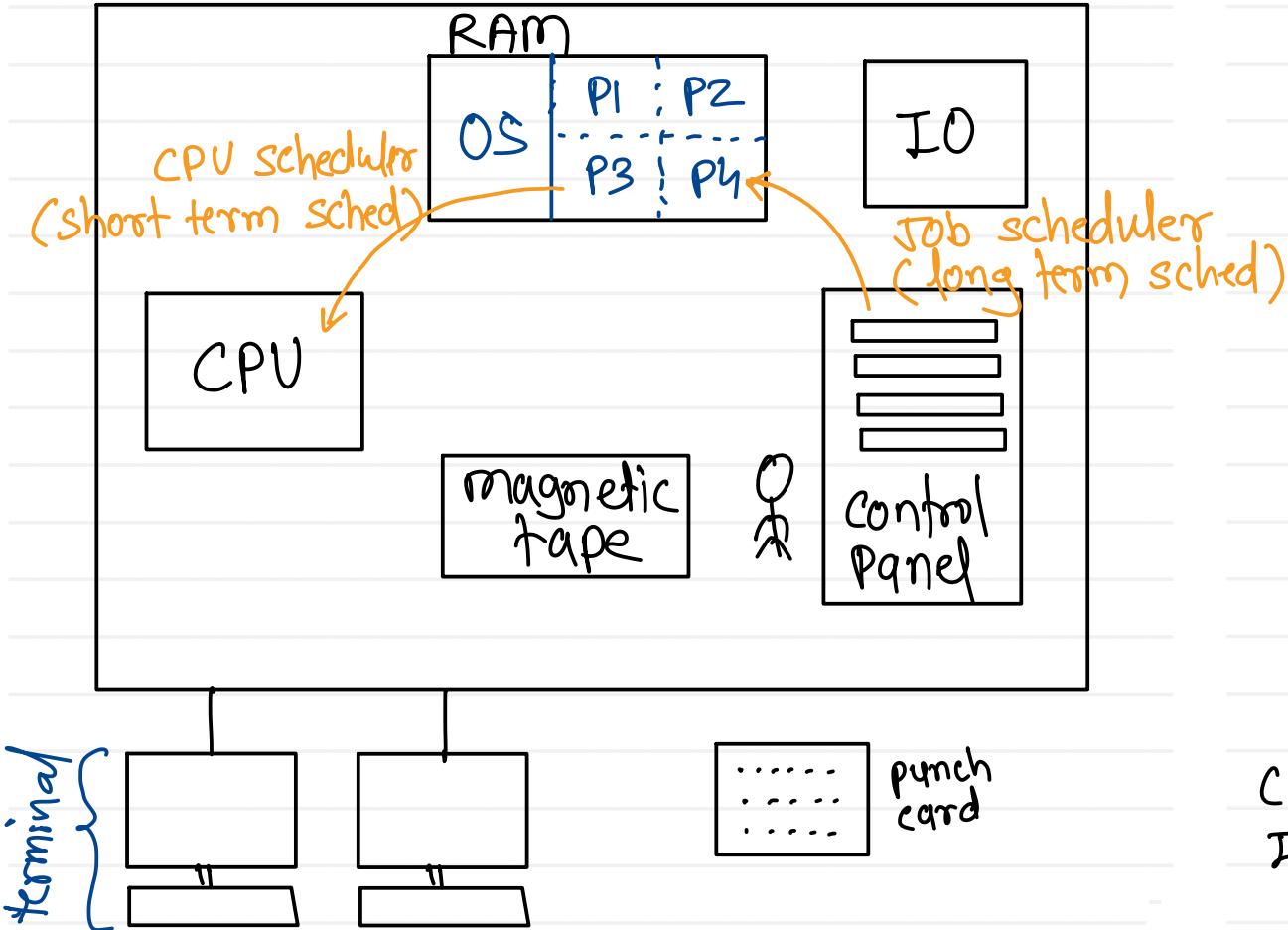
00 - ring 0 more

01 - ring 1 !

10 - ring 2 less

11 - ring 3 - unprivileged

Types of Operating system



- 1> Resident monitor
- 2> Batch system
- 3> Multiprogramming system:

- multiple programs are loaded into memory.

Degree of multiprogramming
- no. of programs loaded inside memory.

CPV burst : time spent by process on CPU
IO burst : time spent by process on IO

CPV burst > IO burst : CPU bound process
IO burst > CPV burst : IO bound process

- mixture of CPU bound & IO bound processes is loaded inside RAM

Types of Operating system

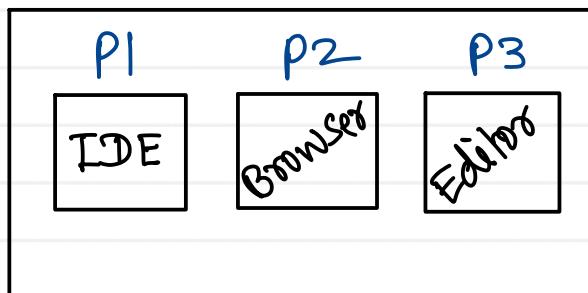
4) Time Sharing system: (Multitasking)

- CPU time is shared in all the processes
of memory

response time < 1 sec

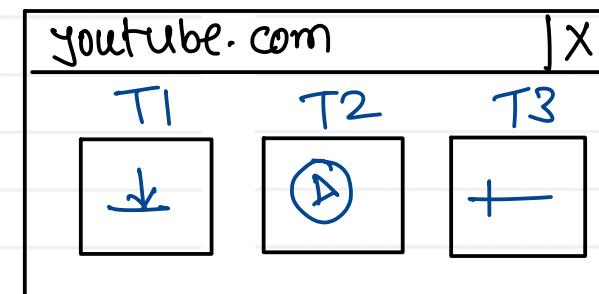
- there are two types of multitaskings

1. Process based Multitasking



System

2) thread-based multitasking (multithreading)



Browsers

- within system multitasking

- within process multitasking



Types of Operating system

s) Multiuser system:

- multiple terminals are connected to system
(keyboard + monitor)

c) Multiprocessing system

- multiple CPUs are putted together in single chip. such chips are called as multiprocessor / multicore

- OS can schedule multiple processes for multiple cores , means multiple instructions will be processed parallel

1> Symmetric multiprocessing

2> Asymmetric multiprocessing





Thank you!!!

Devendra Dhande

devendra.dhande@sunbeaminfo.com