

1. The registers should be according to the MIPS registers (\$zero, \$s0, etc., NOT \$r1, \$r1, etc.)
2. jump and branches should use labels
3. lw, sw instructions should be according to the MIPS instruction format discussed in class (see test cases provided).

Make sure your previous assignment runs on the testcases provided in the preparatory material, then proceed with Assignment-4.