CSA Lab Assingnment

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<u>AC-1216</u>

Q. Simulate the machine for the following indirect memory refrence instruction with I=1 and address part stores 070. The instruction to be stored at address 011 in RAM. Initialize The memory word at address 070 with the operand C8F3 and AC with B158. Determine the contents of AC, DR, PC, AR and IR in hexadecimal after the execution.

- 1. AND I
- 2. ADD_I
- 3. LDA_I
- 4. STA_I
- 5. BUN_I
- 6. BSA_I
- 7. ISZ 1



AND_I

HLT

Memory data

Add	Data
11	1050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
E	0
IR	0
PC	11

Name	Data
AC	8050
AR	1
DR	C8F3
E	0
IR PC	E001
PC	13



ADD_I

HLT

Memory data

Add	Data
11	3050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
E	0
IR	0
PC	11

Name	Data
AC AR	7A4B
	1
DR	C8F3
Е	1
IR	E001
PC	13



LDA_I

HLT

Memory data

Add	Data
11	5050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
Е	0
IR PC	0
PC	11

Name	Data
AC AR	C8F3
AR	1
DR	C8F3
Е	0
IR	E001
PC	13



STA_I

HLT

Memory data

Add	Data
11	7050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
E	0
IR	0
PC	11

Name	Data
AC AR	B158
	1
DR	C8F3
Е	0
IR	E001
PC	13



BUN_I

HLT

Memory data

Add	Data
11	1050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
E	0
IR	0
PC	11

Name	Data
AC AR	B158
	1
DR	C8F3
Е	0
IR	E001
PC	13



BSA_I

HLT

Memory data

Add	Data
11	1050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
E	0
IR	0
PC	11

Name	Data
AC AR	B158
	1
DR	C8F3
Е	0
IR	E001
PC	13



ISZ_I

HLT

Memory data

Add	Data
11	1050
12	E001
50	70
70	C8F3

Register data before execution

Name	Data
AC	B158
AR	0
DR	0
E	0
IR	0
PC	11

REGISTER DATA After Execution [HALT]

Name	Data
AC AR	B158
	1
DR	C8F3
Е	0
IR	E001
PC	13