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UART_Verification_env

⋮ ⋮ ⋮ ⋮



baud_rate_generator.sv

transmitter.sv

reciever.sv

TB_uart.sv

uart_dump.vcd X

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uart_dump.vcd



1198341244

t= bin

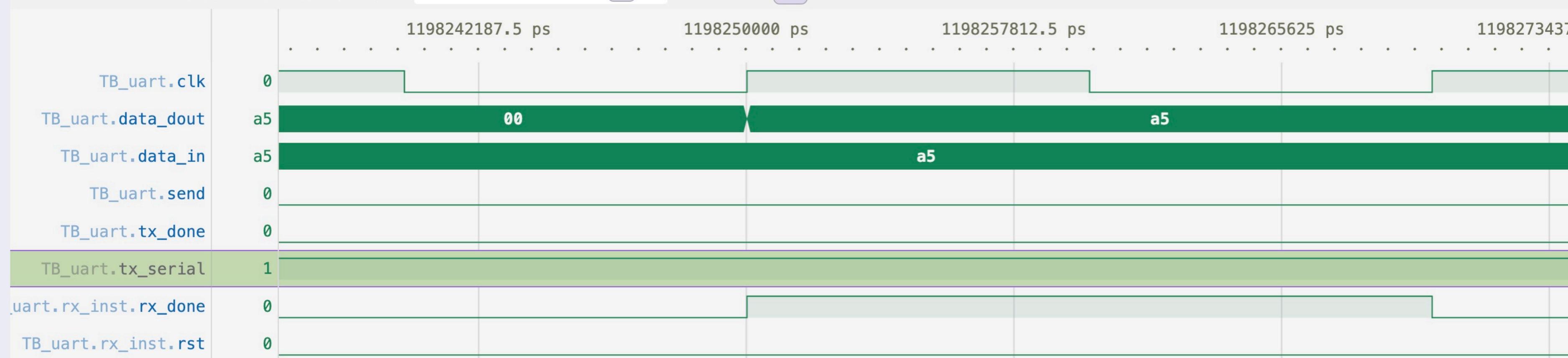
← →



auto

Auto Reload

...



x

Run Testcases

0 ⚠ 0

BLACKBOX Agent

SystemVerilog: 0 indexed objects

Open Website

BLACKBOXAI: Open Chat

Selected signal: tx_serial

Time: 1,198,341,244 ps



```
harsh@Harshs-MacBook-Air UART_Verification_env % verilator --trace --binary TB_uart.sv baud_rate_generator.sv transmitter.sv reciever.sv
```

```
brew/Cellar/verilator/5.036/share/verilator/include/verilated.cpp  
c++ -Os -I/opt/homebrew/opt/openjdk@11/include -I. -MMD -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include/vltstd -DVM_COVERAGE=0 -DVM_SC=0 -DVM_TIMING=1 -DVM_TRACE=1 -DVM_TRACE_FST=0 -DVM_TRACE_VCD=1 -DVM_TRACE_SAIF=0 -faligned-new -fbracket-depth=4096 -fcf-protection=none -Xclang -fno-pch-timestamp -Qunused-arguments -Wno-bool-operation -Wno-c++11-narrowing -Wno-constant-logical-operand -Wno-non-pod-varargs -Wno-parentheses-equality -Wno-shadow -Wno-sign-compare -Wno-tautological-bitwise-compare -Wno-tautological-compare -Wno-uninitialized -Wno-unused-but-set-parameter -Wno-unused-but-set-variable -Wno-unused-parameter -Wno-unused-variable -DVL_TIME_CONTEXT -std=gnu++20 -c -o verilated_vcd_c.o /opt/homebrew/Cellar/verilator/5.036/share/verilator/include/verilated_vcd_c.cpp  
c++ -Os -I/opt/homebrew/opt/openjdk@11/include -I. -MMD -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include/vltstd -DVM_COVERAGE=0 -DVM_SC=0 -DVM_TIMING=1 -DVM_TRACE=1 -DVM_TRACE_FST=0 -DVM_TRACE_VCD=1 -DVM_TRACE_SAIF=0 -faligned-new -fbracket-depth=4096 -fcf-protection=none -Xclang -fno-pch-timestamp -Qunused-arguments -Wno-bool-operation -Wno-c++11-narrowing -Wno-constant-logical-operand -Wno-non-pod-varargs -Wno-parentheses-equality -Wno-shadow -Wno-sign-compare -Wno-tautological-bitwise-compare -Wno-tautological-compare -Wno-uninitialized -Wno-unused-but-set-parameter -Wno-unused-but-set-variable -Wno-unused-parameter -Wno-unused-variable -DVL_TIME_CONTEXT -std=gnu++20 -c -o verilated_timing.o /opt/homebrew/Cellar/verilator/5.036/share/verilator/include/verilated_timing.cpp  
c++ -Os -I/opt/homebrew/opt/openjdk@11/include -I. -MMD -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include/vltstd -DVM_COVERAGE=0 -DVM_SC=0 -DVM_TIMING=1 -DVM_TRACE=1 -DVM_TRACE_FST=0 -DVM_TRACE_VCD=1 -DVM_TRACE_SAIF=0 -faligned-new -fbracket-depth=4096 -fcf-protection=none -Xclang -fno-pch-timestamp -Qunused-arguments -Wno-bool-operation -Wno-c++11-narrowing -Wno-constant-logical-operand -Wno-non-pod-varargs -Wno-parentheses-equality -Wno-shadow -Wno-sign-compare -Wno-tautological-bitwise-compare -Wno-tautological-compare -Wno-uninitialized -Wno-unused-but-set-parameter -Wno-unused-but-set-variable -Wno-unused-parameter -Wno-unused-variable -DVL_TIME_CONTEXT -std=gnu++20 -c -o verilated_threads.o /opt/homebrew/Cellar/verilator/5.036/share/verilator/include/verilated_threads.cpp  
python3 /opt/homebrew/Cellar/verilator/5.036/share/verilator/bin/verilator_includer -DVL_INCLUDE_OPT=include VTB_uart.cpp VTB_uart__024root__DepSet_hbea8fa7e__0.cpp VTB_uart__024root__DepSet_he5c4e0aa__0.cpp VTB_uart_main.cpp VTB_uart_Trace__0.cpp VTB_uart__024root__Slow.cpp VTB_uart__024root__DepSet_hbea8fa7e__0__Slow.cpp VTB_uart__024root__DepSet_he5c4e0aa__0__Slow.cpp VTB_uart_Syms.cpp VTB_uart_Trace__0__Slow.cpp VTB_uart_TraceDecls__0__Slow.cpp > VTB_uart_ALL.cpp  
c++ -Os -I/opt/homebrew/opt/openjdk@11/include -I. -MMD -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include -I/opt/homebrew/Cellar/verilator/5.036/share/verilator/include/vltstd -DVM_COVERAGE=0 -DVM_SC=0 -DVM_TIMING=1 -DVM_TRACE=1 -DVM_TRACE_FST=0 -DVM_TRACE_VCD=1 -DVM_TRACE_SAIF=0 -faligned-new -fbracket-depth=4096 -fcf-protection=none -Xclang -fno-pch-timestamp -Qunused-arguments -Wno-bool-operation -Wno-c++11-narrowing -Wno-constant-logical-operand -Wno-non-pod-varargs -Wno-parentheses-equality -Wno-shadow -Wno-sign-compare -Wno-tautological-bitwise-compare -Wno-tautological-compare -Wno-uninitialized -Wno-unused-but-set-parameter -Wno-unused-but-set-variable -Wno-unused-parameter -Wno-unused-variable -DVL_TIME_CONTEXT -std=gnu++20 -c -o VTB_uart_ALL.o VTB_uart_ALL.cpp  
echo "" > VTB_uart_ALL.verilator_deplist.tmp  
c++ -Wl,-U,__Z15vl_time_stamp64v,-U,__Z13sc_time_stampv verilated.o verilated_vcd_c.o verilated_timing.o verilated_threads.o VTB_uart_ALL.a -pthread -lpthread -o VTB_uart  
rm VTB_uart_ALL.verilator_deplist.tmp  
- Verilator Report: Verilator 5.036 2025-04-27 rev UNKNOWN.REV  
- Verilator: Built from 0.079 MB sources in 5 modules, into 0.071 MB in 11 C++ files needing 0.000 MB  
- Verilator: Walltime 4.207 s (elab=0.001, cvt=0.005, bld=4.196); cpu 0.010 s on 1 threads  
● harsh@Harshs-MacBook-Air UART_Verification_env % ./obj_dir/VTB_uart  
[TB] Sending 0xA5  
[TB] Received: 0xa5  
- TB_uart.sv:77: Verilog $finish  
- Simulation Report: Verilator 5.036 2025-04-27  
- Verilator: $finish at 1ms; walltime 0.037 s; speed 32.401 ms/s  
- Verilator: cpu 0.037 s on 1 threads; alloced 0 MB  
◆ harsh@Harshs-MacBook-Air UART_Verification_env %
```