BIRLA INSTITUTE OF TECHNOLOGY DEOGHAR CAMPUS QUIZ: FIRST

Name:				Roll. No:		
SUBJECT: CS 239 Operating System				Time: 50 minutes		
Sem.: IVth Branch: CSE Session: S				Full Marks: 10		
Instru	ctions:					
1.	Attempts all the Que	estions.		.1		
	Overwriting means			2.01		Set: C
Q 1. V	Which of the following	ng is NOT a c	characteristic	of MLQ sch	eduling?	[1mark]
	a) Each queue has its o c) Processes can move					y assigned to processes define queue priorities
Q. 2.	The spawning of pro	ess means				
						[1mark]
О.3. Т	he register					
2.5. 1	2.3. The register includes the instruction address, condition code, and other information to control instruction sequencing and to determine the					
	state of the CPU.	-	on to control in	struction seque	memg and to t	[1 Mark]
Q. 4.	Thread shares with other threads belonging to the same process its					[1 Mark]
	(a) code section and data section (b) program Counter					unter
	(c) register set and stack (d) thread id					
Q. 5.	Suppose that the following processes arrive for execution at the times indicated. Each process will run the listed amount of time.					
	Process	Arrival	Time	CPU Burs	t Time	Priority
	P1	0	**************************************	11		2
	P2	5	ereta are et al. et a la constitución de la constit	28	and ships in the control of makes	0
	P3	12		10	1	3
	P4 P5	9		16		4
	Answer the followi			10		<u> </u>
	Draw the Gantt chart and calculate the average Turn-around time for the following Scheduling algorithms. (a) Priority (Preemptive) Scheduling algorithm (b) Priority (Non Preemptive) Scheduling algorithm [2marks]					[2marks]
Q. 6.	- 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1					
	Eq. (7.4)	••••		Σ.		<