

CERTIFICATE

UNIVERSITY

Hochschule Ravensburg Weingarten, University of

Applied Sciences

FACULTY

Electrical Engineering and Computer Science

PROGRAM

Electrical Engineering and Embedded Systems

DEGREE

Master of Engineering (M.Eng.)

System Analysis and Simulation with LabView

SURNAME

Phaneendra

FIRST NAME

Harsha

DATE OF BIRTH

December 8, 1994

BORN IN

Chitradurga, Karnataka/India

MODULS	MARKS	
MODULS		
Mathematics for Engineers	fair	3,8
Communication 1	satisfactory	3,5
Communication 2	good	2,0
Circuit and Systems 1	good	2,0
Circuit and Systems 2	very good	1,4
Signalprocessing 1	fair	3,6
Signalprocessing 2	good	1,8
Implementation of Closed Loop Digital Control Systems	very good	1,4
Embedded Control	very good	1,5
Embedded Computing including Engineering Project: GPS-Free Geolocation Detection using LoRa	satisfactory	3,0
OPTIONAL SUBJECT	very good	1,3

MASTER-THESIS

1,7 good

Detecting Free Parking Slots in an Occupancy Grid Map and Deployment

SUPPLEMENTARY SUBJECT

German as a Foreign Language A1

successful passed

Final Mark

good 2,2

Weingarten, November 18, 2020

Prof. Dr.-Ing. Thomas Spägele

Rector of the University

Prof. Dipl.-Math. Ekkehard Löhmann Dean of Faculty Electrical Engineering and

Computer Science



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Exmatrikulationsbescheinigung

Herr Harsha Phaneendra

Geburtsname:

Phaneendra

Geburtsdatum:

08. Dezember 1994

Geburtsort:

Chitradurga, Karnataka/In

Anschrift:

Trippstadter Straße 25, 67663 Kaiserslautern

Matrikelnummer:

30141

Studiengang:

El. and Embedded Systems

war vom 01. September 2017 bis 28. Februar 2021 an der Hochschule Ravensburg-Weingarten eingeschrieben.

Urlaubssemester:

0

HOCHSCHULE HOCHSCHULE

Dienstsiegel

Kim Warneck

Zulassungsamt/Studierenden-Service Admissions Office/Department of student affairs

Bearbeiter/in: kw

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16.02.2021

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Mitglied der



