Visvesvaraya Technological University, Belagavi
Karnataka State, INDIA 67076

TRANSCRIPT AS PER RECORDS   **We do not have GPA scheme of evaluation**

Name : HARSHA P
University Seat No : 1SK12EC020
Year of Entrance : 2012
Year of Leaving : 2016
Degree Received : Bachelor of Engineering
(Electronics & Communication Engineering)

1. Duration of the Course : 4 Years
2. Medium of Instruction : English
3. First class with distinction (FCD) : Not less than 70% of the aggregate marks in first attempt
4. First class (FC) : Less than 70% but not less than 60% of the aggregate marks in first attempt
5. Second class (SC) : Less than 60% of the aggregate marks in first attempt

SUBJECTS	Hours Per Week Lecture	Hours Per Week Drawing/ Practical	Marks Obtained	Max. Marks
I Semester				
1 Engineering Mathematics - I	4		83	125
2 Engineering Chemistry	4		82	125
3 Computer Concepts & C Programming	4		86	125
4 Computer Aided Engineering Drawing	2	4	115	125
5 Basic Electronics	4		91	125
6 Computer Programming Lab		3	64	75
7 Engineering Chemistry Lab		3	68	75
8 Environmental Studies	2		49	75
First Attempt Total: 589 /775 ; Class : FCD ; # 1				
II Semester				
1 Engineering Mathematics - II	4		68	125
2 Engineering Physics	4		94	125
3 Elements of Civil Engg. & Engg. Mechanics	4		103	125
4 Elements of Mechanical Engineering	4		87	125
5 Basic Electrical Engineering	4		104	125
6 Workshop Practice		3	65	75
7 Engineering Physics Lab		3	65	75
8 Constitution of India & Professional Ethics	2		59	75
First Attempt Total: 586 /775 ; Class : FCD ; # 1				
III Semester				
1 Engineering Mathematics-III	4		106	125
2 Analog Electronic Circuits	4		112	125
3 Logic Design	4		80	125
4 Network Analysis	4		103	125
5 Electronic Instrumentation	4		78	125
6 Field Theory	4		64	125
7 Analog Electronics Lab		3	65	75
8 Logic Design Lab		3	70	75
First Attempt Total: 678 /900 ; Class : FCD ; # 1				
IV Semester				
1 Engineering Mathematics - IV	4		77	125
2 Microcontrollers	4		83	125
3 Control Systems	4		97	125
4 Signals & Systems	4		111	125
5 Fundamentals of HDL	4		93	125
6 Linear ICs & Applications	4		97	125
7 Microcontrollers Lab		3	66	75
8 HDL Lab		3	62	75
First Attempt Total: 686 /900 ; Class : FCD ; # 1				

SUBJECTS	Hours Per Week Lecture	Hours Per Week Drawing/ Practical	Marks Obtained	Max. Marks
V Semester				
1 Management & Entrepreneurship	4		91	125
2 Digital Signal Processing	4		75	125
3 Analog Communication	4		90	125
4 Microwaves & Radar	4		61	125
5 Information Theory & Coding	4		68	125
6 Fundamentals of CMOS VLSI	4		59	125
7 DSP Lab		3	63	75
8 Analog Communication Lab. + LIC Lab.		3	70	75
First Attempt Total 577 / 900 ; Class: FC ; # 1				
VI Semester				
1 Digital Communication	4		89	125
2 Microprocessors	4		56	125
3 Microelectronics Circuits	4		76	125
4 Antennas & Propagation	4		83	125
5 Operating Systems	4		73	125
6 Programming in C++	4		92	125
7 Advanced Communication Lab.		3	70	75
8 Microprocessor Lab.		3	66	75
First Attempt Total 605 / 900 ; Class: FC ; # 1				
VII Semester				
1 Computer Communication Networks	4		71	125
2 Optical Fiber Communication	4		85	125
3 Power Electronics	4		82	125
4 Embedded System Design	4		86	125
5 DSP Algorithms & Architecture	4		77	125
6 Image Processing	4		86	125
7 VLSI Lab.		3	72	75
8 Power Electronics Lab.		3	63	75
First Attempt Total 622 / 900 ; Class: FC ; # 1				
VIII Semester				
1 Wireless Communication	4		99	125
2 Digital Switching Systems	4		84	125
3 Network Security	4		87	125
4 Ad-hoc Wireless Networks	4		87	125
5 Project Work		6	196	200
6 Seminar		3	49	50
First Attempt Total 602 / 750 ; Class: FCD ; # 1				
Grand total of V to VIII Semester 2406 out of 3450 (max.)				

Class of the Degree * : First Class

06/10CIP/06/10CIV18/28 is not considered for Grand Total and the Class Declaration

AUTHENTIC

*Based on First Attempt Marks of V to VIII Semesters   # Number of Attempts taken to clear the semester

Registrar (Evaluation)