SAI-TEC

A Novel Approach Towards Technology

CERTIFICATE

This is to certify that Mr/Ms HARSHA. P

has completed training course in FPGA DESIGN

LEVEL-1 during the period JAN'15 to FEB'15.



DIRECTOR

For SAI-TEC

Reviews

Authorises Signature

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CERTIFICATE

This is to certify that Mr/Ms HARSHA. P.

has completed training course in ADVANCED VLSI

DESIGN during the period APRIL 2015 to JUNE, 2015



DIRECTOR

For SAI-TEC

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Authorised Signatory

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