PONNADA HARSHA VARDHAN

III Year Undergraduate Student

Computer Science & Engineering (Dual Degree) Indian Institute of Technology

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Sex: Male

Languages: English, Hindi

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ACADEMIC RECORD:

Degree	Year	Institute/College	Rank
B. Tech & M. Tech (Dual Degree) Computer Science and Engineering	2019	Indian Institute of Technology Kharagpur	8.40 / 10 (till 5 th semester)
Intermediate Public Examination	2014	Sri Chaitanya Junior College Hyderabad	96.7%
SSC Examination	2012	Sri Chaitanya Techno School	9.7/10

PROJECTS UNDERTAKEN / INTERNSHIPS:

Automatic Extraction of Derivative Nouns in Sanskrit Language

Project in Natural Language Processing- Mentor: Prof. Pawan Goyal

DETAILS:

- Implemented a graph based semi supervised learning approach for designing an automated system to extract the source word from Derivative Nouns in Sanskrit language
- Used Modified Adsorption to check whether the word is derived from a noun or a verb by training the pairs of source word and the derived word
- Worked with Junto software for generating the graph for Modified Adsorption

Right-House (Euprime)

Right-House is a startup based on Data Modelling and Computation by Euprime

DETAILS:

- Worked as a backend Developer in designing a housing model which gives the users the best house based on the user preference and their interests
- Scraped and modelled a huge data for grading the houses on user interests.
 Key-Learning: Start-up exposure, proficiency in Python, basics of frontend development, Linux, Ability to implement code fast, Ability to face challenging deadlines

ACADEMIC PROJECTS:

Students Auditorium Management System

Project in Software Engineering- Mentor: Prof. Partha Pratim Das

DETAILS:

Worked Designed a software which helps students to view, book and cancel tickets for various shows in the auditorium.

Developed the Graphical User Interface using NetBeans. Implemented the back-end in Java. Worked with MySQL Relational Database Management System to store the generated data.

Compiler for tinyC

Project in Compilers Course- Mentor: Prof. Partha Pratim Das

DETAILS:

Designed and implemented a compiler for tinyC.

Considering a subset of gnu functionalities this compiler with the help of Flex and Bison can generate correct executable code for a C program.

Processor for 16-bit machine

Project in Computer Organization and Architecture- Mentor: Prof. Chittaranjan Mandal

DETAILS:

Designed, implemented and tested Multi-cycle and Single-cycle processors.

Designed the General assembly instruction format for a specific instruction set.

Designed the Data path and the Controller around the Instruction format as Finite State Machines. Implemented and tested the processors in Verilog HDL using the Xilinx tool.

ACADEMIC ACHIEVEMENTS:

- A scholar of Kishore Vaigyanic Protsahana Yojana.
- Selected for the regionals of **ACM ICPC 2016-17** held at Amritapuri.
- Secured a seat in the prestigious institutes of the country, **Indian Institute of Science** and **Indian Statistical Institute** out of very few hundreds of people all over the country
- Acquired an **All India Rank of 1842** in **Joint Entrance Exam (IIT JEE 2014)** out of 1,50,000 candidates (an approximate percentile of 99)
- Secured a rank of 1350 in JEE Mains 2014 out of 15,00,000 candidates thus ranking among top 0.01%.
- Secured a rank of AIR 17 in Association of Mathematics Teachers of India
- Ranked 448 in Andhra Pradesh EAMCET-2014 among 2,00,000 students

SKILLS:

CATEGORY	DESCRIPTION
Programming Languages	C, C++, Java, Python
Technologies	Eclipse, Visual Studio, Microsoft SQL Server, Junto, Linux, MySQL

EXTRA ACADEMIC ACTIVITIES:

- Captain, Maths Olympiad, Azad Hall
 - Part of a bronze winning team, General Championship Technology, IIT Kharagpur
 - Represented a hall of 700 members in the Maths Olympiad Event, General Championship Technology, IIT Kharagpur
- NSO (National Sports Organization) IIT Kharagpur
- Dramatics:
 - Inter-hall Silver winning dramatics team 2015
 - Active Member of Prasthanam Technology Dramatics Society