COA

1.What is Flip - Flop ? Explain anyone in detail
2.What is Address ? Explain full adder in detail.
3.Explain Encoder III detail
4.Write a short note on Demultiplexer.
5.Explain fixed point representation.
6.Explain Parity Bit with Error Detection Code.
7.Explain major components of CPU.
8.Write a short note on General Register Organization.
9.Explain I/O interface III detail.
10.Write a short note on rop.

2019

1.Explain AND, OR, NOT Logic Gates.
2.Explain SR Flip Flop in detail.
3.Explain Multiplexer (4 x 1)
4.Explain IC in detail.
5.Explain error detecting code using parity bit
6.Write a note on fixed point representation.
7.Explain major components of CPU.
8.Explain General Register Organization.
9.Explain DMA Controller.
10.Explain Input Out Processor (IOP).

2018

1.Explain General Register Organization
2.Explain ALU
3.Explain 4 x 1 Multiplexer
4.Write a note on Boolean algebra
5.State and prove De - Morgan’s theorems.
6.Explain stack organization
7.Explain DMA Controller
8.Explain K-map with example
9.Explain Error Detecting Codes.
10.Explain Bi-Directional Shift Register

2017

1.Explain Kamaugh Map with example.
2.What is Flip Flop? Explain SR-flip flop.
3.What is decoder? Explain 3 X 8 decoder.
4.Explain Asynchronous 4 bit Binary Counter.
5.Explain error detecting code using parity bit.
6.Write a note on fixed point representation.
7.Explain register stack.

8.Explain general register organization.
9.Write a note on DMA controller.
10.Write a note on IOP.

2015

1.Explain Stack Organization.
2. Explain Master slave Flip-Flop
3.. Explain Polish Notation with its advantages
4.Explain general register organization.
5.Explain register with parallel load.
6.What is combinational circuit? Explain with types.
7.What is Flip Flop? Explain with types.
8.Prove following Boolean algebra.
(a) AB + A(B+C)+B(B+C)=B+AC
(b) (X+Y')(X'+Y) =XY + X'Y'
9.Write note on Error Detection Code.
10.Explain Asynchronous 4 bit Binary Counter