

Full form - COA

Date :

Unit-1

- ① EX-OR = Exclusive OR
- ② EX-NOR = Exclusive NOR
- ③ SOP = sum of products
- ④ POS = products of sum
- ⑤ K-MAP = Karnaugh map
- ⑥ CK = clock pulse
- ⑦ SR = set - reset
- ⑧ D-FF = Data FF / Delay
- ⑨ FF = flip flop
- ⑩ JK-FF = Jump & Kick flip flop
- ⑪ T-FF = Toggle flip flop
- ⑫ S / C = sum / carry
- ⑬ TTL = Transistor-Transistor logic (2017)
- ⑭ ECL = Emitter Coupled logic.

Unit-2

- ① IC = Integrated Circuit
- ② MUX = Multiplexer
- ③ DE-mux = DE-Multiplexer
- ④ SSI = small scale Integration
- ⑤ MSI = Medium scale Integration
- ⑥ LSI = large scale Integration
- ⑦ VLSI = Very large scale Integration
- ⑧ BCD = Binary code Decimal
- ⑨ SISO = Serial-In, Serial-Out
- ⑩ SIPO = Serial In, Parallel out
- ⑪ PISO = parallel In, Serial out
- ⑫ PIPO = parallel In, parallel out

Unit -4

- ① CPU = Central processing Unit
- ② ALU = Arithmetic logic unit
- ③ AR = Accumulator Register
- ④ LIFO = Last In First Out
- ⑤ SP = stack pointer.
- ⑥ AR = Address Register.
- ⑦ PC = Program Counter.
- ⑧ DR = Data Register.
- ⑨ RPN = Reverse Polish Notation

Unit -5

- ① DMA - Direct Memory Access.
- ② PIO - Programmed Input Output.
- ③ IOP = Input Output processor.
- ④ BR = Bus Request
- ⑤ BG = Bus Grant
- ⑥ RD = Read
- ⑦ WP = Write
- ⑧ PD = Peripheral Devices.
- ⑨