

Harshal Santosh Phapale Pune | phaaaaaaaa@gmail.com | 9132355555 Objective VLSI
Engineering student seeking internship or entry-level opportunity in semiconductor and chip design.
Education B.E. in VLSI (2nd Year) Anantrao Pawar College of Engineering, Pune Skills Verilog, C,
Linux, MATLAB, Digital Electronics Courses Completed online courses in VLSI, Digital Design, and
Semiconductor fundamentals. LinkedIn Available