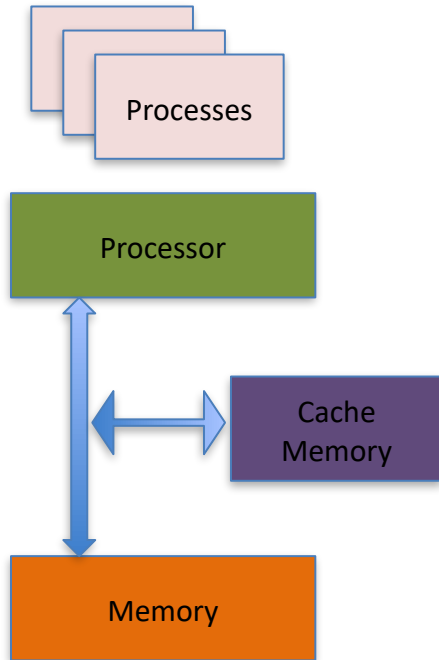


Access Control

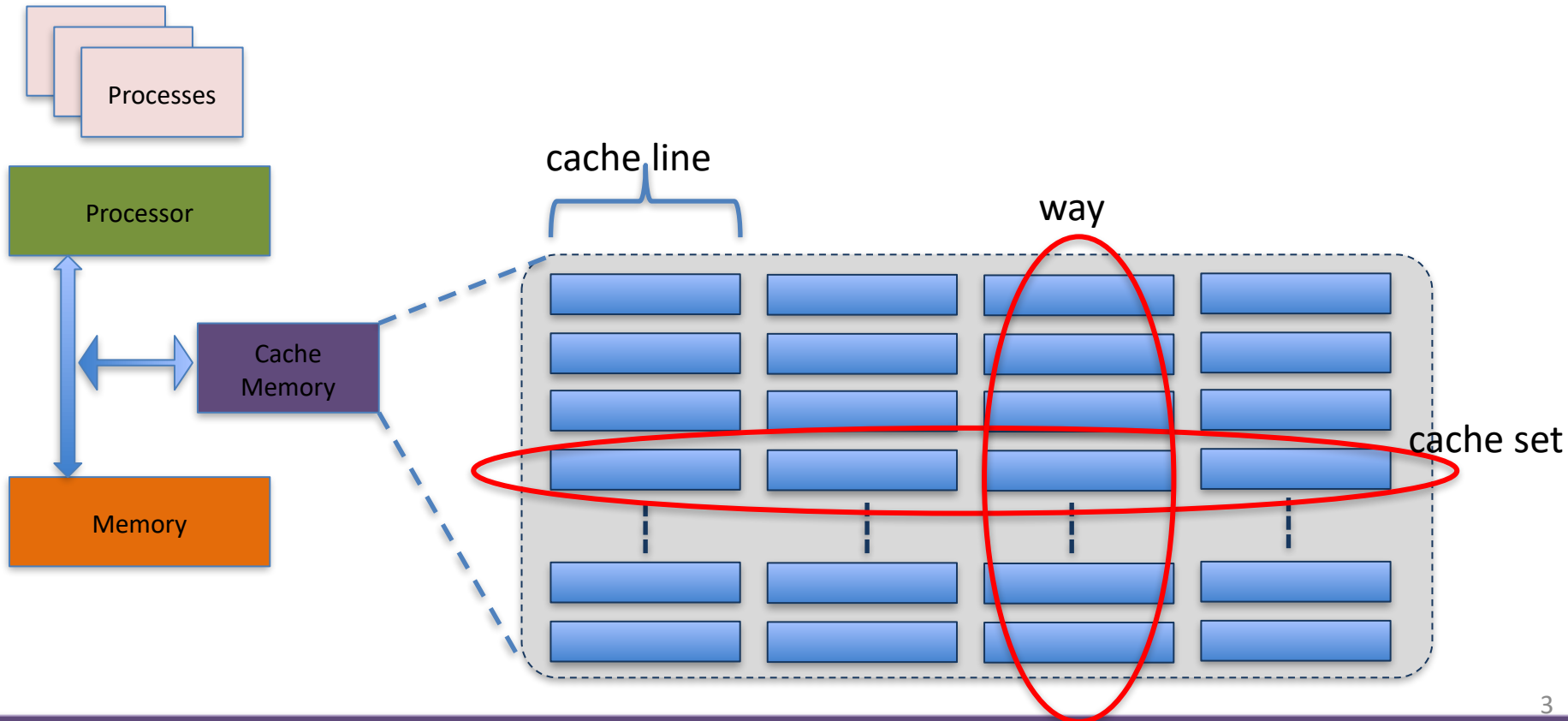
Chester Rebeiro

Indian Institute of Technology Madras

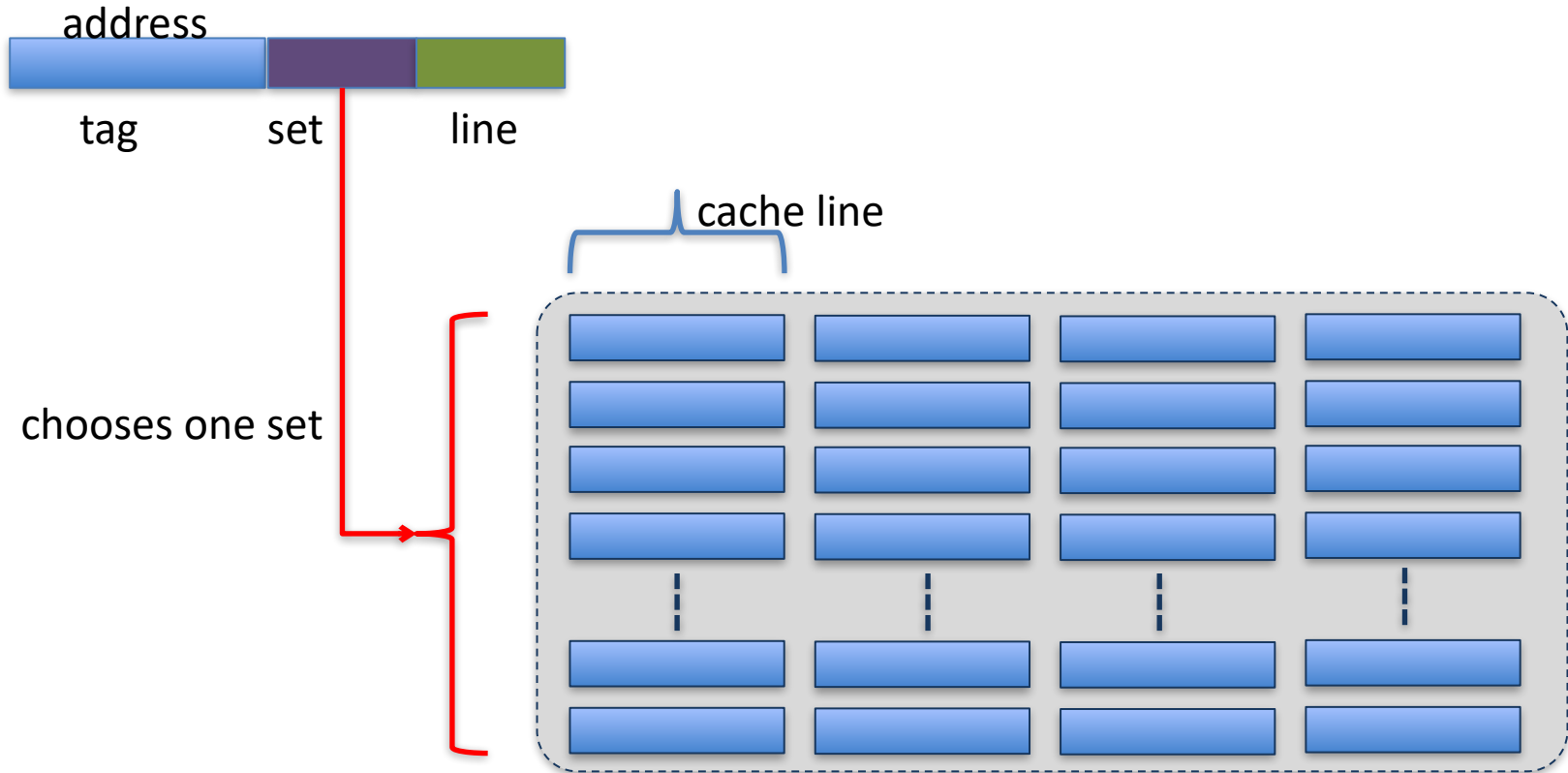
Cache Covert Channel



Cache Covert Channel



Cache Covert Channel



Cache Covert Channel



statistically
time A ~ time B

```
while(1){
```

```
load A1p2; load A2p2
```

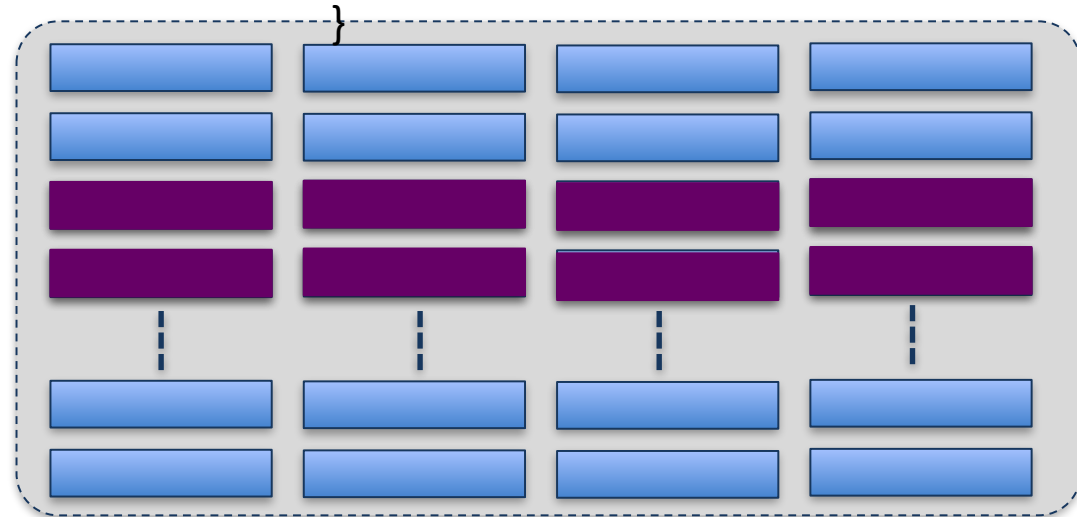
```
load A3p2; load A4p2
```

```
load B1p2; load B2p2
```

```
load B3p2; load B4p2
```

Process P2

A Set
B Set



Cache Covert Channel

Process P1

If (bit == 1)

load A_{p1}

Else

load B_{p1}



statistically
time A > time B

while(1){

load $A1_{p2}$; load $A2_{p2}$

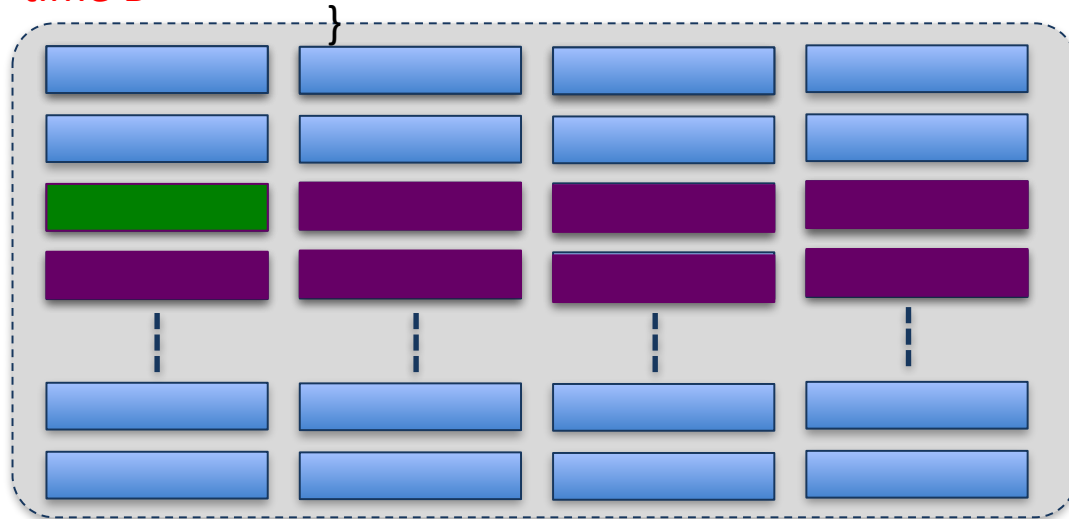
load $A3_{p2}$; load $A4_{p2}$

load $B1_{p2}$; load $B2_{p2}$

load $B3_{p2}$; load $B4_{p2}$

Process P2

A Set
B Set



Cache Covert Channel

Process P1

```
If (bit == 1)
    load Ap1
Else
    load Bp1
```



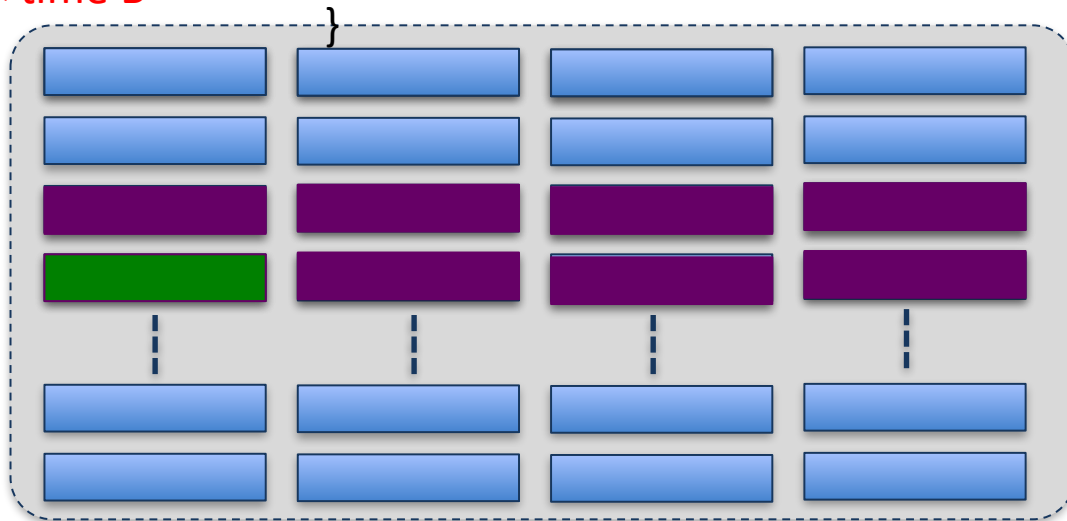
statistically
time A < time B

```
while(1){
```

```
    load A1p2; load A2p2
    load A3p2; load A4p2
    load B1p2; load B2p2
    load B3p2; load B4p2
```

Process P2

A Set
B Set



Cache Covert Channel

Process P1



statistically
time A < time B

while(1){

load A1_{p2}; load A2_{p2}

load A3_{p2}; load A4_{p2}

load B1_{p2}; load B2_{p2}

load B3_{p2}; load B4_{p2}

Process P2

bit = message

while(bit[i] != '\0')

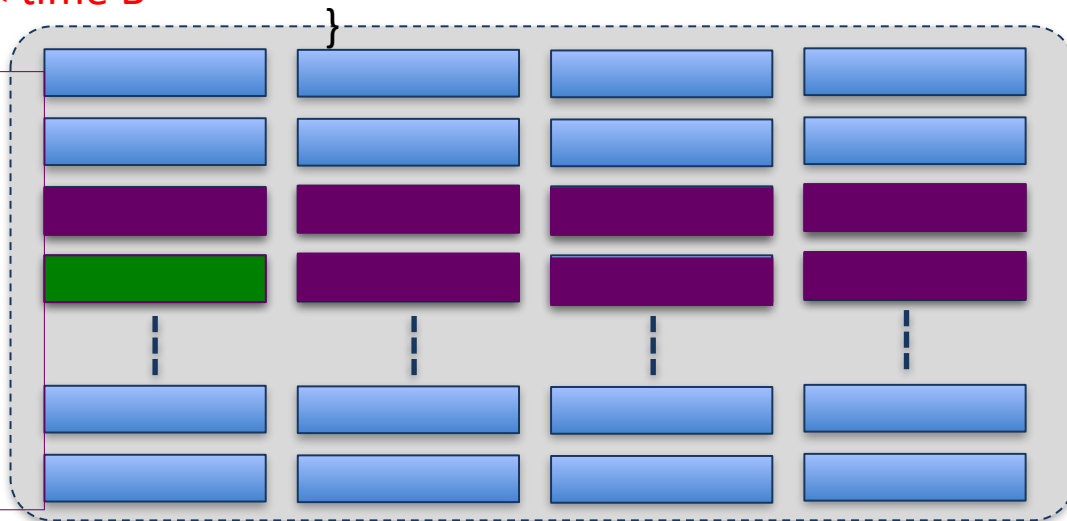
for(some number of iterations)

If (bit[i] == 1)

load A_{p1}

else

load B_{p1}



Covert Channels

- Identifying: Not easy because simple things like the existence of a file, time, etc. could be a source for a covert channel.
- Quantification: communication rate (bps)
- Elimination: Careful design, separation, characteristics of operation (eg. rate of opening / closing a file)