Indira Gandhi Delhi Technical University For Women
(Established by Govt. of Delhi vide Act 09 of 2012)
Kashmere Gate, Delhi-110006
Department of Electronics & Communication Engineering
TIME TABLE

(Room No. E-213) **Even Sem. January –May 2019/ B.Tech 4ᵗʰ Semester** **w.e.f: 17ᵗʰ January, 2019**

Time / Day	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Mon	AE-II	AE-II	DS	DS	DSC Lab (C) AE-II Lab (D)		Mini Project (D)	
Tue		DCS	DCS	DS		TLA	CS Lab (C)	
Wed	CS Lab (D)		CS		DCS	DCS	AE-II Lab (C)	
Thu	AE-II	AE-II	CS	CS	DS Lab (C) DCS Lab (D)		TLA	
Fri	TLA	TLA	DS Lab (D) Mini Project (C)			CS	DS	

Sub. Code	Lab/ Course Name	Venue for Lab	Faculty Name
BEC 252	Analog Electronics – II Lab (AE-II Lab)	E-315: Analog Lab	Dr. Nidhi Goel
BEC 254	Digital Circuits & Systems Lab (DCS Lab)	E 211: Microprocessor Lab	Dr. Richa Yadav
BEC 256	Communication Systems Lab (CS Lab)	E 201: Communication Lab	Mr. Kanchan Sharma
BCS 260	Data Structures Using C++ Lab (DS Lab)	DBMS Lab (Gp. C), Networking Lab (Gp. D)	Ms. Khyati Ahlawat
BEC 258	Mini Project	E 315 : Analog Lab	Ms. Greeshma Arya
BEC 202	Analog Electronics – II (AE-II)		Ms. Vidhi Bishnoi
BEC 204	Digital Circuits & Systems (DCS)		Dr. Richa Yadav
BEC 206	Communication Systems (CS)		Mr. Kanchan Sharma
BCS 208	Data Structures Using C++ (DS)		Ms. Khyati Ahlawat
BEC 210	Transmission Lines and Antennas (TLA)		Ms. Neha Singh

Dr. Nidhi Goel
(HOD, ECE)

Dr. Richa Yadav
(Time Table In-charge, ECE Dept)

(Room No. E-313)   **Even Sem. January –May 2019/ B.Tech 6ᵗʰ Semester**   **w.e.f: 17ᵗʰ January, 2019**

Time / Day	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Mon			ITC Lab (D) ME Lab (C)		EE	DCCN	DCCN	ME
Tue	VLSI Lab (C) DSP Lab (D)		VLSI	VLSI	ITC	ITC	DCCN Lab (D)	
Wed		DSP	EE	EE	ITC	ITC	ME	DCCN
Thu	DSP	DSP	VLSI Lab (D) ITC Lab (C)			VLSI	DCCN Lab (C)	
Fri		DSP	DCCN	VLSI	ME Lab (D) DPS Lab (C)		ME	ME

Sub. Code	Lab/ Course Name	Venue for Lab	Faculty Name
BEC 352	Microwave & Radar Engineering Lab (ME Lab)	E 201: Communication Lab	Md. Ejaz Lodhi
BEC 354	Information Theory & Coding Lab (ITC Lab)	E 109(A) : DSP & Research Lab	Ms. Greeshma Arya
BEC 356	VLSI Design Lab (VD Lab)	E 107: VLSI Design Lab	Dr. Vandana Niranjan/ Md. Ejaz Lodhi
BIT 358	Data Communication & Networking Lab (DCCN Lab)	E 109(A) : DSP & Research Lab	Dr. Chandra Prakash
BEC 360	Digital Signal Processing & its application Lab (DSP Lab)	E 109(A) : DSP & Research Lab	Dr. Akash Tayal
BEC 302	Microwave & Radar Engineering (ME)		Md. Ejaz Lodhi
BEC 304	Information Theory & Coding (ITC)		Ms. Greeshma Arya
BEC 306	VLSI Design (VD)		Dr . Vandana Niranjan
BIT 308	Data Communication & Networking (DCCN)		Dr. Chandra Prakash
BEC 310	Digital Signal Processing and its applications (DSP)		Dr. Akash Tayal
BAS 312	Engineering Economics (EE)		Prof. Ashwni Kumar

Dr. Nidhi Goel   **Dr. Richa Yadav**
(HOD, ECE)   **(Time Table In-charge, ECE Dept)**

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Kashmere Gate, Delhi-110006

F-AD-03

Department of Electronics & Communication Engineering
TIME TABLE

(Room No. IT-313)   **Even Sem. January –May 2019/ B.Tech 8th Semester**   **w.e.f: 17th January, 2019**

Time / Day	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Mon	MAJOR PROJECT							
Tue	MAJOR PROJECT		MC Lab (C) ESD Lab (D)			MC	MAJOR PROJECT	
Wed	ESD	ESD	ESD Lab (C)		MC	MC	MAJOR PROJECT	
Thu	ESD	ESD	OB		WSN	WSN	MC Lab (D)	
Fri		MC	OB	OB	WSN	WSN	MAJOR PROJECT	

Sub. Code	Lab/ Course Name	Venue for Lab	Faculty Name
BEC 454	Mobile Communication Lab (MC Lab)	E 201: Communication Lab	Mr. Kanchan Sharma
BCS 452	Embedded Systems Design Lab (ESD Lab)	E 109 (B): Embedded System Design Lab	Ms. Neha Singh
BAS 422	Organizational Behaviour (Elective 2) (OB)		Prof. Ashwni Kumar
BEC 404	Mobile Communication (MC)		Mr. Kanchan Sharma
BCS 402	Embedded Systems Design (ESD)		Ms. Neha Singh
BCS 412	Wireless Sensor Networks (Elective 1) (WSN)		Ms. Greeshma Arya
BEC 456	Major Project		Ms. Neha Singh

Dr. Nidhi Goel
(HOD, ECE)

Dr. Richa Yadav
(Time Table In-charge, ECE Dept)

Department of Electronics & Communication Engineering
TIME TABLE

(Room No. E-314A)   **Even Sem. January –May 2019/ M.Tech (VLSI Design) 2nd Semester**   **w.e.f: 17th January, 2019**

Time / Day	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Mon			DMC	DMC	DMC Lab (D)		MEMs	MEMs
Tue			VSD	MEMs	DMC Lab (C)		DSCI Lab (C)	
Wed		ICA	VSD Lab (C)		VSD	DSCI	DSCI Lab (D)	
Thu	ICA	ICA	VSD	Term Paper	VSD Lab (D)		DSCI	DSCI
Fri		ICA	VSD	Term Paper	DSCI	DMC	DMC	MEMs

Sub. Code	Lab / Course Name	Venue for Lab	Faculty Name
MVD 524	Device Modeling & Circuit simulation Lab (DMC Lab)	E 107: VLSI Design Lab	Dr. Vandana Niranjan
MVD 526	VLSI subsystem design Lab (VSD Lab)	E 107: VLSI Design Lab	Dr. Nidhi Goel
MVD 528	Lab based on elective(s) (DSCI Lab)	E 107: VLSI Design Lab	Dr. Shobha Sharma
MVD 502	Device Modeling & Circuit simulation (DMC)		Dr. Vandana Niranjan
MVD 504	VLSI subsystem design (VSD)		Dr. Nidhi Goel
MVD 506	Integrated Circuits for Analog Signal Processing (ICA)		Md. Ejaz Lodhi
MVD 510	Deep Submicron CMOS ICs (DSCI)		Dr. Shobha Sharma
MVD 530	Term Paper		Dr. Akash Tayal

Dr. Nidhi Goel   **Dr. Richa Yadav**
(HOD, ECE)   **(Time Table In-charge, ECE Dept)**

Department of Electronics & Communication Engineering
TIME TABLE

(Room No. E-314B)   **Even Sem. January –May 2019/ M.Tech (VLSI Design) 4th Semester**   **w.e.f: 17th January, 2019**

Time / Day	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Mon								
Tue			Seminar & Progress Report					
Wed			Seminar & Progress Report					
Thu								
Fri								

Sub. Code	Lab/Course Name		Faculty Name
MVD 604	Seminar & Progress Report	NUES	Dr. Akash Tayal

Dr. Nidhi Goel
(HOD, ECE)

Dr. Richa Yadav
(Time Table In-charge, ECE Dept