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Question 11

Match the logic gates in Column A with their equivalents in Column B.

Column-A	Column-B
1. AND GATE	P. XOR GATE
2. OR GATE	Q. XNOR GATE
3. NOT GATE	R. NAND GATE

TABLE 11: Table-1

- a) P-2, Q-4, R-1, S-3 b) P-4, Q-2, R-1, S-3
c) P-2, Q-4, R-3, S-1 d) P-4, Q-2, R-3, S-1

Question Analysis

The AND gate produces output 1 only when both inputs are 1. The NAND gate is the complement of AND. Therefore, NAND is equivalent to NOT(AND).

The XOR gate produces output 1 when inputs are different. The XNOR gate is the complement of XOR.

The NOT gate performs inversion operation.

Using logical equivalence:

AND corresponds to NAND when complemented. OR can be related through DeMorgan's theorem. NOT corresponds to inversion logic.

After analyzing the logical relationships, the correct matching is:

$P - 2, Q - 4, R - 3, S - 1$

Truth Table

AND gate expression: $Q = A \cdot B$

A	B	A.B
0	0	0
0	1	0
1	0	0
1	1	1

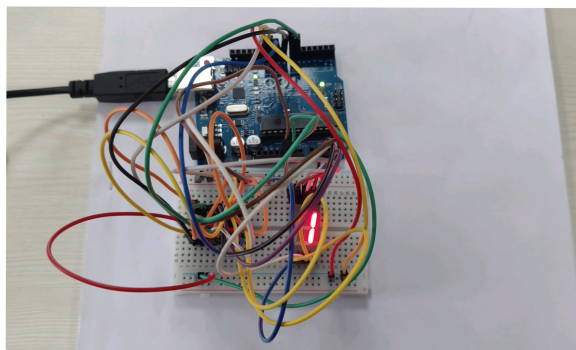
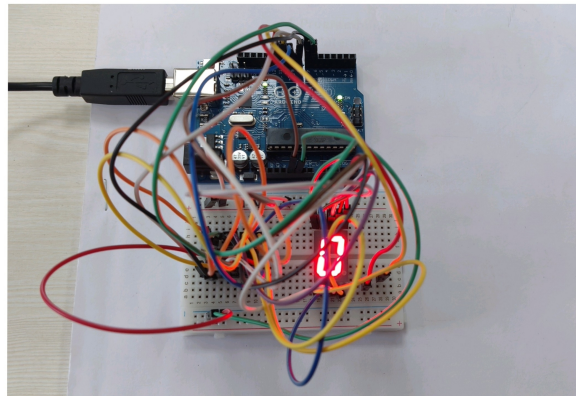
NAND gate expression: $Q = (A \cdot B)'$

A	B	NAND
0	0	1
0	1	1
1	0	1
1	1	0

Hardware Implementation

The circuit is implemented using Arduino UNO and IC 7447. The logical output is displayed on a common anode 7-segment display.

Arduino provides digital inputs. The output is converted to BCD format and given to 7447. The 7447 drives the seven-segment display to show 0 or 1.



Required Components

- Arduino UNO
- IC 7447
- Common Anode 7-Segment Display
- Breadboard
- Jumper wires

Pin Connections

7447 Connections:

Pin 16 → 5V Pin 8 → GND Pin 3,4,5 → 5V

BCD Inputs: Pin 7 → Arduino pin Pin 1 → Arduino pin Pin 2 → Arduino pin Pin 6 → Arduino pin

7-Segment:

Common Anode → 5V Segment pins connected from 7447 outputs through resistors.

Logic Description

The NAND logic is implemented.

Expression: $Q = (A \cdot B)'$

When both inputs are HIGH, output becomes LOW. For all other cases, output remains HIGH.

This confirms the logical complement of AND operation.

Code Uploading Steps

1. Create a new folder for ass.
2. Write the code in ass.asm.
3. Run the command `avra ass.asm`. It creates .hex file.
4. Copy the hex file to ArduinoDroid folder.
5. Connect Arduino UNO to mobile using OTG cable.
6. Upload the hex file using Upload Precompiled option.
7. Observe the output and verify the expression.

Experimental Truth Table

A	B	Observed Output
0	0	1
0	1	1
1	0	1
1	1	0

Conclusion

The hardware implementation and truth table confirm that:

$$Q = (A \cdot B)'$$

Hence the logical equivalence is verified successfully.