

VLSI 1

<https://drive.google.com/drive/folders/16U2HV1EVEYJ5vPsZHCcSskXoEf-Th-u?usp=sharing>

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<https://drive.google.com/drive/u/0/mobile/folders/1Mf-l48CtAcJcPpKspSil30lwrw5bAGzb>

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<https://drive.google.com/drive/folders/1Mf-l48CtAcJcPpKspSil30lwrw5bAGzb>

RTL Design Examples

https://www.linkedin.com/posts/cvnreddyseelam07-vlsi-ai_rtl-design-examples-ugcPost-7239133670542913537-CGNe?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/cvnreddyseelam07-vlsi-ai_100daysofrtlmaterial-ugcPost-7138148952624807936-IWPr?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/adithya-mallisetti-rtldesigner_100daysofrtlreference-ugcPost-7183501153819660288-OQ5D?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/jagruthi-k_50-days-of-rtl-ugcPost-7258821869032636416-vX6m?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/srinivas-dv-engineer_verilog-codeexamples-ugcPost-7170404250420699138-fAXh?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI 2

<https://drive.google.com/drive/folders/1RvYzDG-oULfhrOXqTP-gHZT9xDV4k4b8>

<https://drive.google.com/drive/folders/19ih8GyRtUFyCDT1J4ZgNEOyXvxGm0c62>

<https://bit.ly/4h571uB>

https://www.linkedin.com/posts/reshma-bhagat-624144235_interview-experience-ugcPost-7253669581636960256-rv0j/?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Road map to VLSI

https://www.linkedin.com/posts/kittupatel03_roadmap-to-get-into-vlsi-activity-7304160036254019584-qgXP?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

<https://lnkd.in/gagSBt8b>

https://www.linkedin.com/posts/values-company_values-getvaluesfromvalues-fpga-activity-7288153934299381760-KREv?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/prasanthi-chanda-484205245_mentor-guidance-training-activity-7282307495979560960-Uwaq?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/prov-logic_road-map-to-become-a-vlsi-engineer-ugcPost-7278411368083431425-GeUt?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/tadakamalla-gourav-design-verification_designverification-vlsi-freecourses-activity-7260153647009865729-1PHM?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Free Resources for VLSI

https://www.linkedin.com/posts/pooja-kumawat_vlsi-digital-digitalelectronics-activity-7190688939790209024-5k_0/

Digital VLSI

https://www.linkedin.com/posts/himanshu-shekhar08_day3-reposted-gate-activity-7302153249103458304-naa?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/sahaanish_digital-vlsi-roadmap-ugcPost-7241312422991384576-Bfb3?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Analog Design in VLSI

https://www.linkedin.com/posts/soumick-majumdar_vlsi-analogic-analogdesign-activity-7306153350847156224-1YFg?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/chaitanya-anand-2b2238a3_analogdesign-vlsi-semiconductorengineering-ugcPost-7283164221385711617-pnGk?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI Websites

https://www.linkedin.com/feed/update/urn:li:activity:7133766492415807488?updateEntityUrn=urn%3Ali%3Afs_feedUpdate%3A%28V2%2Curn%3Ali%3Aactivity%3A7133766492415807488%29

Analog Electronics

https://drive.google.com/drive/folders/0B_Od84JBf2dvYTRRWW0TWhoYVk?resourcekey=0-HBtTfbQXp-e3Zs83M29dnw

https://www.linkedin.com/posts/bapuji2214_cs-amplifier-resistive-load-ugcPost-7289345962945232897-wN3g?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/bapuji2214_single-stage-opamp-design-procedure-ugcPost-7288236219430313984-71-A?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/sai-manikanta-6022981a3_cmos-analog-ic-design-ugcPost-7151885134680522752-ifKD?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

APB Protocol

<https://drive.google.com/drive/folders/1S8YaRzf2qJrZMoW-GFRicXHxWWHw-g36>

https://www.linkedin.com/posts/prov-logic_amba-apb-protocol-ugcPost-7288101668733206531-4ANf?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

AXI Protocol

https://drive.google.com/drive/folders/1HJDDkA5WkliRioPOK0MgBjb_BUWuvlxq?usp=sharing

VLSI 3

https://drive.google.com/drive/folders/1n44XAh9dcT6hhLLOh8t9cje3F8Zdp5vp?usp=drive_link

https://drive.google.com/drive/folders/1rPB_luWV8lP-lBxneHYVrb_0df_wSKmX

https://drive.google.com/drive/folders/1tNNGST9tuAbHPgYXpdjdcjEajzb_GO4t

https://drive.google.com/drive/mobile/folders/1LoUFBgSX1cBz7q2km0Zrrr8sYBJBePyH/1KD7qpbMOiDI1RC11nbiaR2b4W-3fyozx?usp=share_link&sort=13&direction=a

VLSI placements 1

<https://drive.google.com/folderview?id=1EYE4SaTF3edNpLUXdyxSPZv9naXY-ODO>

Electronics 1

https://drive.google.com/drive/folders/1iPtKhjPkhLJANDfti92QwD9l3_Y9Vli-

Electronics 2

<https://drive.google.com/drive/folders/1LoUFBgSX1cBz7q2km0Zrrr8sYBJBePyH>

Electronics 3

<https://drive.google.com/drive/folders/1lryroPVTXDSOY7XjSJR1tguV9EvkMwhS>

Cadence virtuoso tool

<https://drive.google.com/file/d/1jexsrQzMbq7wXTU-VjBfRX9FXS03eNmww/view>

VLSI placements 2

https://drive.google.com/drive/folders/1p9fF1D_4W7Mr9SdnT0W-FBcucaDTU9Yi

<https://acrobat.adobe.com/id/urn:aaid:sc:AP:306f264e-ac8e-4b81-a82b-20b919512b46>

https://www.linkedin.com/posts/adithya-malliseti-rtldesigner_basictterms-ugcPost-7248661470186463232-ju7N?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/prasanthi-chanda-484205245_digitalinterview-questions-ugcPost-7137294206301048833-guE?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI Interview

https://www.linkedin.com/posts/its-ayush-gupta_vlsi-interview-imp-google-drive-activity-7003971622583865344-qutY/

https://www.linkedin.com/posts/its-ayush-gupta_interview-pdfs-google-drive-activity-7035552563013791744-QciV/?originalSubdomain=in

https://drive.google.com/drive/folders/1qC-pdHdJTGS7TcAbfuRDvTk_yq8EXKe3

VLSI Tools

https://www.linkedin.com/posts/kailash-prasad_vlsidesign-opensource-learning-activity-7047163831432658944-gBlo?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

<https://www.edaplayground.com/x/ZPVR>

<https://www.xilinx.com/support/download/index.html/content/xilinx/en/downloadNav/vivado-design-tools/2023-2.html>

VLSI Resources

https://youtube.com/@himanshuagarwal_si=zUb_5CWhtzbM1y4W

https://www.linkedin.com/posts/kumar-priyadarshi-b0a2a7a2_3-major-job-profiles-in-semiconductor-chip-activity-7245628888762990592-clBF?utm_source=share&utm_medium=member_android

https://www.linkedin.com/posts/alok-pathak-9128521bb_guidance-for-placement-in-the-digital-activity-7292796079677259776-Z0ZP?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/himanshu-atr-a59631167_vlsi-google-drive-activity-7288392548232970242-KBBY?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

<https://drive.google.com/drive/folders/1AVnK3ZwaofzTKP8ysESMftB1CmK6eRVW>

Digital VLSI Resources

https://www.linkedin.com/posts/himanshu-shekhar08_day3-reposted-gate-activity-7302153249103458304-naa?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI Companies

https://www.linkedin.com/posts/md-irshad-alam-953241270_top-50-vlsi-service-companies-ugcPost-7254327206829559808-XJ15?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Government jobs for VLSI

https://www.linkedin.com/posts/sai-manikanta-6022981a3_government-jobs-for-vlsi-design-engineers-activity-7253667222194135040-l6ac?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/sai-manikanta-6022981a3_in-india-the-semiconductor-industry-is-growing-activity-7251831991061368832-3Dqp?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

TCI scripting

<https://acrobat.adobe.com/id/urn:aaid:sc:AP:80c2164f-9f99-4b36-a950-4dd83beecb1e>

https://www.linkedin.com/posts/anurag-verma-nitian_tcl-handwritten-notes-by-anurag-verma-ugcPost-7247308641442172929%20qAMA/?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/naveenkumar-chalawadi-191622222_tcl-scripting-ugcPost-7239633286027714560-fSzY?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Verilog

https://www.linkedin.com/posts/planetskillzz_hdlbits-verilog-practice-activity-7132560757099020288-WlIz?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

<https://acrobat.adobe.com/id/urn:aaid:sc:AP:2ca8437d-07c1-4bdf-8c54-1508ba88df72>

https://www.linkedin.com/posts/kittupatel03_verilog-comprehensive-book-ugcPost-7301632992462987264-8SDM?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Embedded C

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UVM Fifo

<https://acrobat.adobe.com/id/urn:aaid:sc:AP:2a9625ff-f605-48b9-a802-943a991fb021>

<https://acrobat.adobe.com/id/urn:aaid:sc:AP:e457d52b-6773-4da8-ae15-60e4b1c17770>

UVM

https://www.linkedin.com/posts/srinivas-dv-engineer_uvmphases-ugcPost-7282665153018437635-Lltw/?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI Verification

<https://www.youtube.com/watch?v=aKiDJHQHGSU&list=PL44ol9iwigKq5MW17b3MxjPp1HvTlxFdoB>

System Verilog

https://www.linkedin.com/posts/vikram-madivalappa-9b947a140_system-verilog-for-frequecy-calculation-ugcPost-7281238202504331265-Qp9H?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/cvnreddyseelam07-vlsi-ai-%F0%9D%90%92%F0%9D%90%B2%F0%9D%90%AC%F0%9D%90%AD%F0%9D%90%9E%F0%9D%90%A6-%F0%9D%90%95%F0%9D%90%9E%F0%9D%90%AB%F0%9D%90%A2%F0%9D%90%A5%F0%9D%90%A8%F0%9D%90%A0-ugcPost-7243103205583155200-YA7q?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/prasanthi-chanda-484205245_systemverilog-interview-handbook-ugcPost-7197201286289637377-F91U?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Static timing Analysis

https://www.linkedin.com/posts/adithya-malliseti-rtl-designer_stanotes-ugcPost-7240910251468677120-KLiF?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/rajat-kumar-singh-vlsiforall_sta-hand-notes-download-the-vlsi-for-all-activity-7300026137940738048-XP8D?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

SOC Projects Practice

https://www.linkedin.com/posts/chaitanya-nagisetty-87176919a_verification-engineering-socdesign-opensource-activity-7303941731421433856-QIYh?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI Projects for practice

https://www.linkedin.com/posts/prasanthi-chanda-484205245_internship-batch-1-projects-ugcPost-7293807620061020161-Wxhr?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

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https://www.linkedin.com/posts/prasanthi-chanda-484205245_project-finalyear-btech-ugcPost-7286640032159866880-g0RW?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Physical Design in VLSI

https://www.linkedin.com/posts/chaitanya-kumar-mutyala-a05310110_mastering-physical-design-in-vlsi-top-interview-activity-7249357811921387520-wvSw?utm_source=share&utm_medium=member_android

https://www.linkedin.com/posts/vivek-arya-532558143_vlsi-freshers-vlsijobs-activity-7094562950232838144-806z?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/naveenkumar-chalawadi-191622222_physical-design-ugcPost-7252182305803272192-zZoT?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

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VLSI Recruiters

<https://docs.google.com/document/d/1dod1naiFdSyLMY4E8ygAQfDou0NQr3TEZXALzk5mBUc/edit?usp=sharing>

Operational Amplifiers

https://www.linkedin.com/posts/premkumar-k-5b4b57122_opamp-ugcPost-7303602771063148548-B5dj?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

Communication Protocols

<https://drive.google.com/drive/folders/1xBfDyqle3larYuWlrDSTafaZTnmCCSqn>

https://www.linkedin.com/posts/venkatavamsic_usart-spi-i2c-handwritten-notes-ugcPost-7293147143081496578-motk?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI for beginners

https://www.linkedin.com/posts/harshitgupta97_designverification-vlsijobs-systemverilog-activity-7221961290766852097-8sgt?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI books for beginners

https://www.linkedin.com/posts/prasanthi-chanda-484205245_mentor-guidance-training-ugcPost-7294658035455508481-F6p1?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

DFT Resources

https://www.linkedin.com/posts/sanaka-siva-naga-lakshmi-8b6527287_design-for-testability-notes-ugcPost-7290621126928457729-pMT5?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

https://www.linkedin.com/posts/dr-anu-asokan_open-source-tools-for-dft-self-learning-ugcPost-7271189003855376384-aiVP?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

UART

https://www.linkedin.com/posts/prasanthi-chanda-484205245_uart-protocol-ugcPost-7287022810290339841-lsrU?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

SPI

https://www.linkedin.com/posts/soumick-majumdar_introduction-to-serial-procotols-communication-ugcPost-7247669467353137152-uSDL?utm_source=share&utm_medium=member_android&rcm=ACoAADrxZ4YBmVvjR3TgZoimuJvt5cl-boHCsl8

VLSI placement materials

- TCS : <https://drive.google.com/open?id=1XN8w1Q9akk1lagpaSPDKVowIVCQJ1tV8>
- CTS : https://drive.google.com/open?id=1a-HWjvdfAb4t3xo0O_R8_kwlqGs6lpt-

- TEK SYSTEMS :

https://drive.google.com/drive/mobile/folders/1wsJFR1RNpzcDWcecYkx7Ke5EtvUa3b7i?usp=drive_open

- TECH MAHINDRA : <https://drive.google.com/open?id=1ltJ4CSu-6rSCOgjLKtFC6LlwfVej5NXI>

- INFOSYS : <https://drive.google.com/open?id=1A0so4BGzkynu4k9T0hl-c6yokMBIMQiF>

- MINDTREE :

<https://drive.google.com/open?id=1dkBNs61BGmaQ8bvMDMINO2ENDtCcNSdp>

- VALUE LABS :

<https://drive.google.com/open?id=1MC6LwK5zoTIDkmjZHgm2TxnHPGxGnrHJ>

- MPHASIS : <https://drive.google.com/open?id=1s4ExADXglicpTaNjLj5Et6yv4UaPqXaT>

- ZENQ : <https://drive.google.com/open?id=1FjZxLhhMSNhnkru1pZEGjD1-w8XJPTZQ>

- EPAM : https://drive.google.com/open?id=14kYwuvk3DiCp_c67Qhdm5j8MQ9ctPmRm

- GATE : <https://drive.google.com/open?id=1996QqgleETZQ2o7Lpomh8-fSl49tlckz>

- AMCAT : <https://drive.google.com/open?id=1vPCyQnc844BK9jsTKQ7WlzcGpx3r3J-d>

- ELITMUS : <https://drive.google.com/open?id=154gpJ7a43zDe2lwwwZPQ0uvEnU7SYF1>

- TALENT BATTLE :

https://drive.google.com/open?id=1wlUuyPIS41jKHTrA_1RyMX1hBNWJ0TFS

- Sample C Programs :

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- TR & HR Preparation : https://drive.google.com/open?id=1jkLdSlyv-zAx3a8G-EZ38eXQV_AGjci4

Digital Electronics

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Basic PLC Programming

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Python

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GATE ECE materials,PYQs

<https://practicepaper.in/gate-ec/topic-wise-practice-of-gate-ec-previous-year-papers>

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Embedded Systems Programming

<https://www.youtube.com/@etalvislearning>

Matlab resources

<https://www.lmssolution.net.in/file-share>

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Life Style

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CMOS

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 2. JavaScript :- <https://lnkd.in/dfSvFuhM>
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 4. Bootstrap 5:- <https://lnkd.in/dNZ6qdBh>
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