

M P Spurthi

Electronics and Communication Engineering

Student, PES University , Bangalore

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An enthusiastic and flexible person seeking employment which allows development and utilization of existing skills.

EDUCATION

- **Bachelor of Technology** (2020-present)
PES University
CGPA -7.83/10 (until sem 5)
- **Secondary Education** (2018 - 2020)
Jyoti Nivas Pre University College.
PCMB - 94.5
- **Primary Education** (2010-2018)
HAL Gnanajyoti School(ICSE)
Marks: 90.4

SKILLS

- System Verilog · Python · Java · C Programming · HTML(basics)
- Cadence · Intel Quartus · Matlab · GNS3 · CST Studio · EDA playground

PROJECTS

- Designing 4 x 4 SRAM memory array using Cadence - verified the read and write operations
- Layered testbench for a t-flipflop using intel Quartus
- IPv4 to IPv6 Migration and performance analysis using GNS3 - used manual tunneling technique to prove that IPv6 has lesser latency and better throughput when compared to IPv4
- Reconfigurable Intelligent Surface Assisted Multiuser MIMO Systems using Deep Reinforcement Learning(currently working)

EXPERIENCE

- **Hindustan Aeronautics Limited** 06/22 – 07/22 (1 month)
Internship Trainee

CERTIFICATION

- PESU IO Certification in Web Technology

ACHIEVEMENTS

- Earned MRD and Distinction certificates along with scholarships (being in top 20 percent of the University)

DECLARATION

- I hereby declare that the above mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above mentioned.