

This page contains a schematic diagram for connecting a C64 keyboard header to an MT8812-16 analog switch array, which is used to interface with a Seeduino XIAO USB keyboard project.

### SEED to MT8812-16

SEED	MT8812-16	Connection
1	38	DATA
2	R1	Q1-NMI
3	18	STROBE
4	2	AY2
5	25	AY1
6	24	AY0
7	23	AX2
8	22	AX1
9	5	AX0
10	4	AX3
11	3	RESET

\* NMI (Restore) must be connected to a transistor as shown.  
Although a 2N3904 is shown, any general purpose NPN transistor should work.

### C64 Keyboard Header to MT8812-16

C64	MT8812-16	Connection
5	1	PB3-Y3
6	17	PB6-Y6
7	19	PB5-Y5
8	21	PB4-Y4
9	15	PB7-Y7
10	39	PB2-Y2
11	37	PB1-Y1
12	35	PB0-Y0
13	33	PA0-X0
14	8	PA6-X6
15	28	PA5-X5
16	29	PA4-X4
17	30	PA3-X3
18	31	PA2-X2
19	32	PA1-X1
20	9	PA7-X7

Power to MT8812-16

POWER	PIN	LABEL
GND	16 & 20	VSS & VEE
+ 5 VDC	36 & 40	C/S & VDD

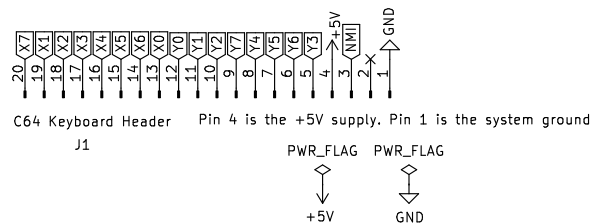
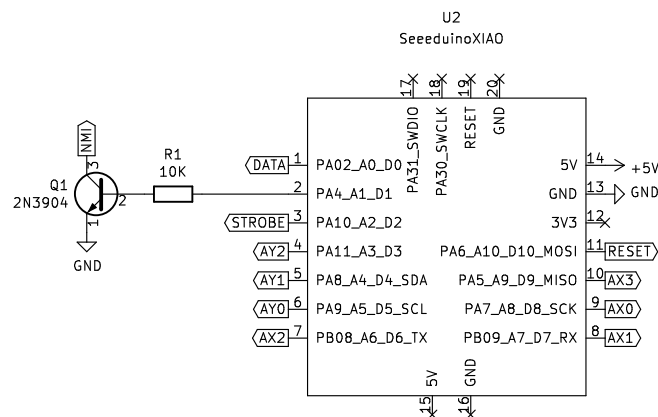
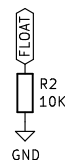
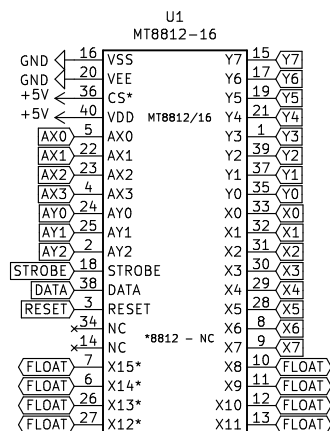
### Seeduino XIAO (USB keyboard)

#### MT8812-16 Analog Switch Array

Sheet: /  
File: C64 KEY SEED MT8812\_16.sch  
**Title:** C64 P52/USB keyboard project  
Size: A Date: 12/2021 Rev: 1.00  
KiCad E.D.A. eeschema (5.1.10)-1 Id: 1/1

SEED	MT8812-16	Connection
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11	37	PB1-Y1
12	35	PB0-Y0
13	33	PA0-X0
14	8	PA6-X6
15	28	PA5-X5
16	29	PA4-X4
17	30	PA3-X3
18	31	PA2-X2
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11	3	RESET

C64 Keyboard Header to MT8812-16

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6	17	PB6-Y6
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Power to MT8812-16

POWER	PIN	LABEL
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+ 5 VDC	36 & 40	C/S & VDD

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U1  
MT8812-16

U2  
SeedeunoXIAO

Q1  
2N3904

R1  
10K

R2  
10K

J1  
C64 Keyboard Header

Pin 4 is the +5V supply. Pin 1 is the system ground

PWR\_FLAG  
+5V

PWR\_FLAG  
GND

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POWER	PIN	LABEL
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+ 5 VDC	36 & 40	C/S & VDD

**Seeeduino XIAO (USB keyboard)**  
**MT8812-16 Analog Switch Array**

Sheet: /  
File: C64 KEY SEED MT8812\_16.sch  
**Title: C64 PS2/USB keyboard project**

Size: A	Date: 12/2021	Rev: 1.00
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