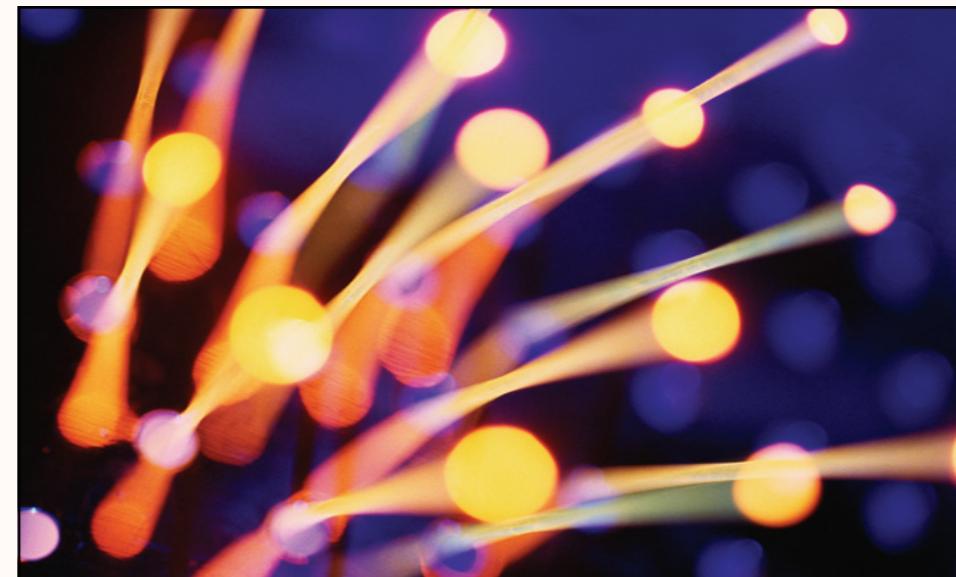


CSCI 381/780: Parallel and Distributed Computing

Why Parallel (& Distributed) Computing?

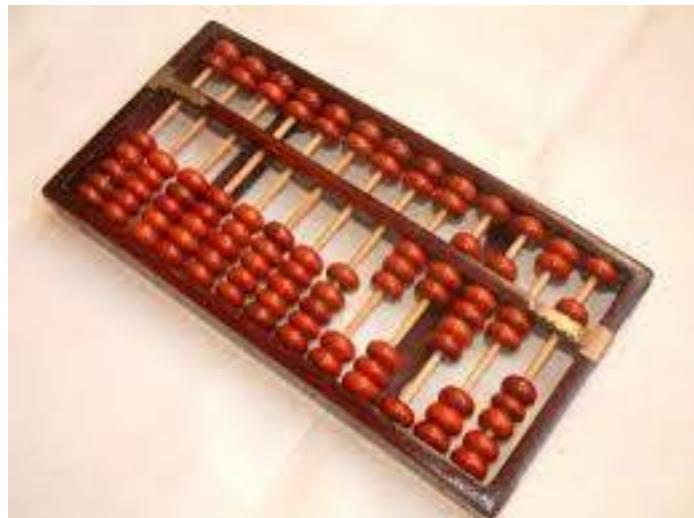


Jun Li

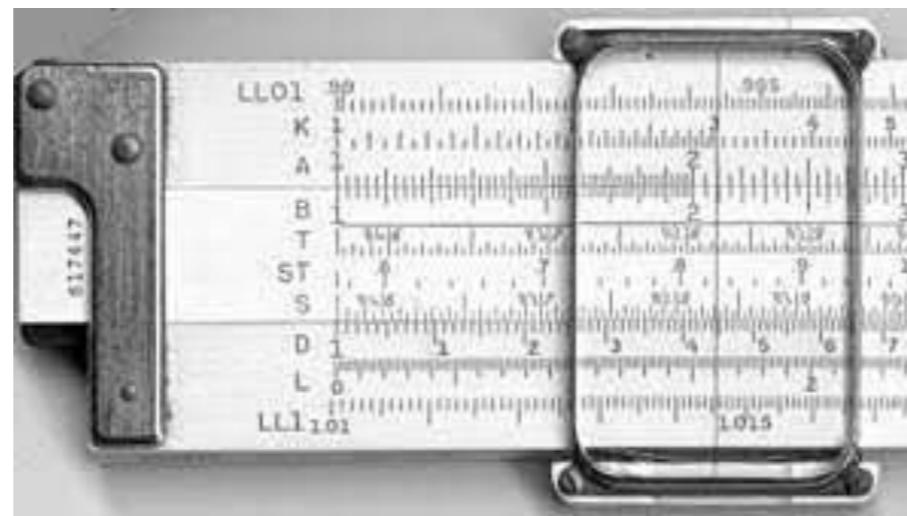
Queens College & Graduate Center

jun.li@qc.cuny.edu

Mechanical “Computer”



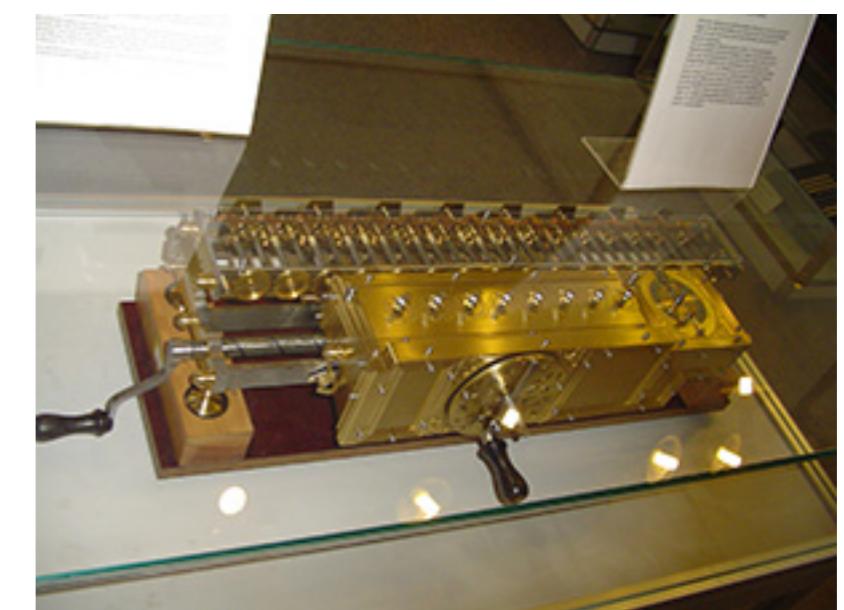
abacus



slide rule



Pascal calculator

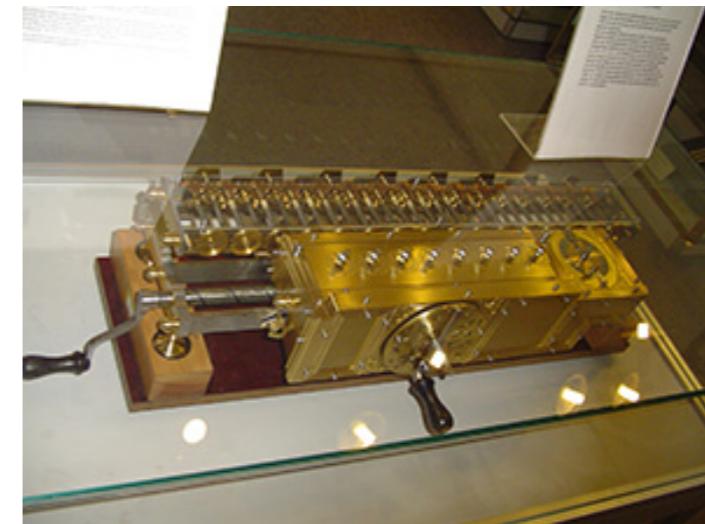


Leibniz's Calculating Machine

Wheel/Key-based Calculator



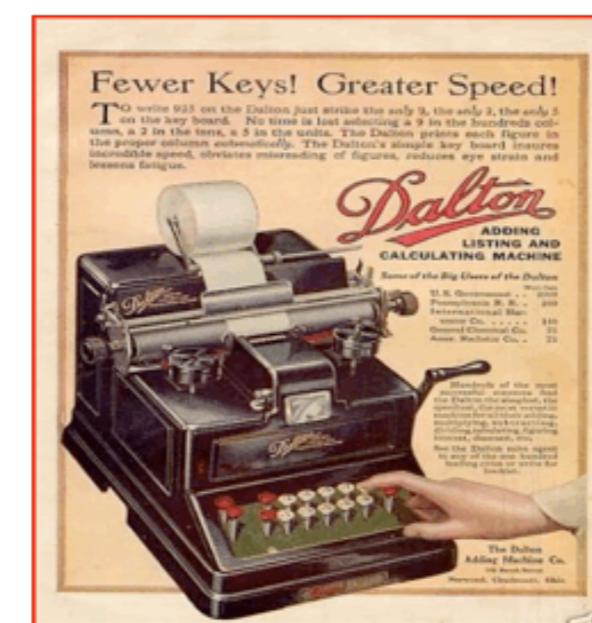
Pascal calculator (1652)



Leibniz's Calculating Machine (1694)

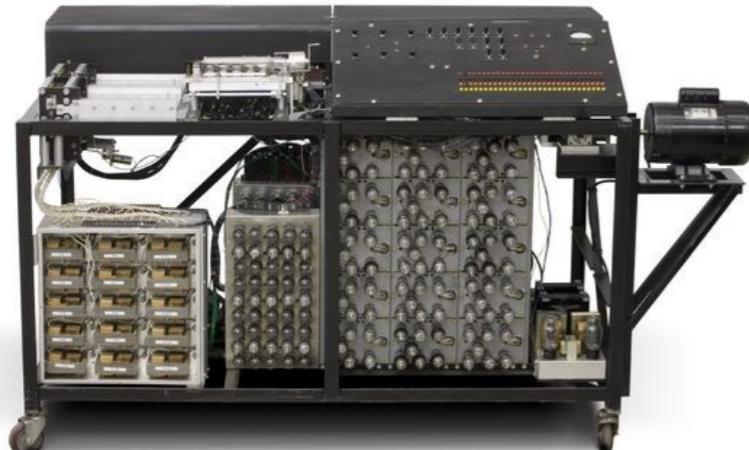


Comptometer (1887)

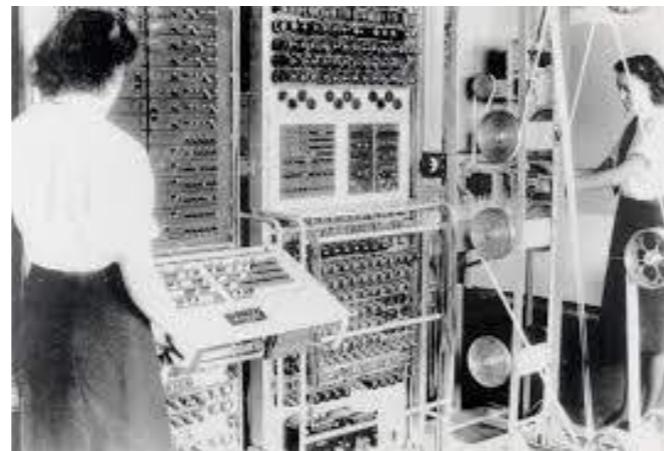


Dalton's Adding Machine (1903)

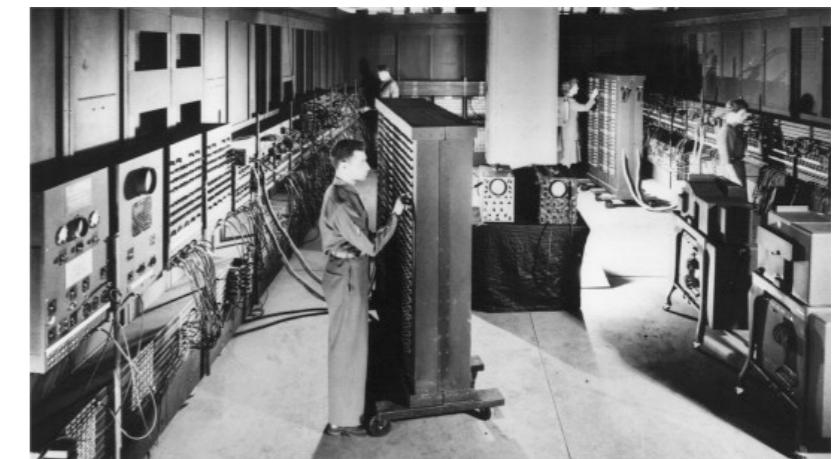
Electronic Computer



Atanasoff-Berry Computer
(1942)



Colossus (1943)



ENIAC (1946)

ENIAC can perform 1900 additions per second, but ENIAC calculated a trajectory in 30 seconds that took a human 20 hours (allowing one ENIAC hour to displace 2,400 human hours)



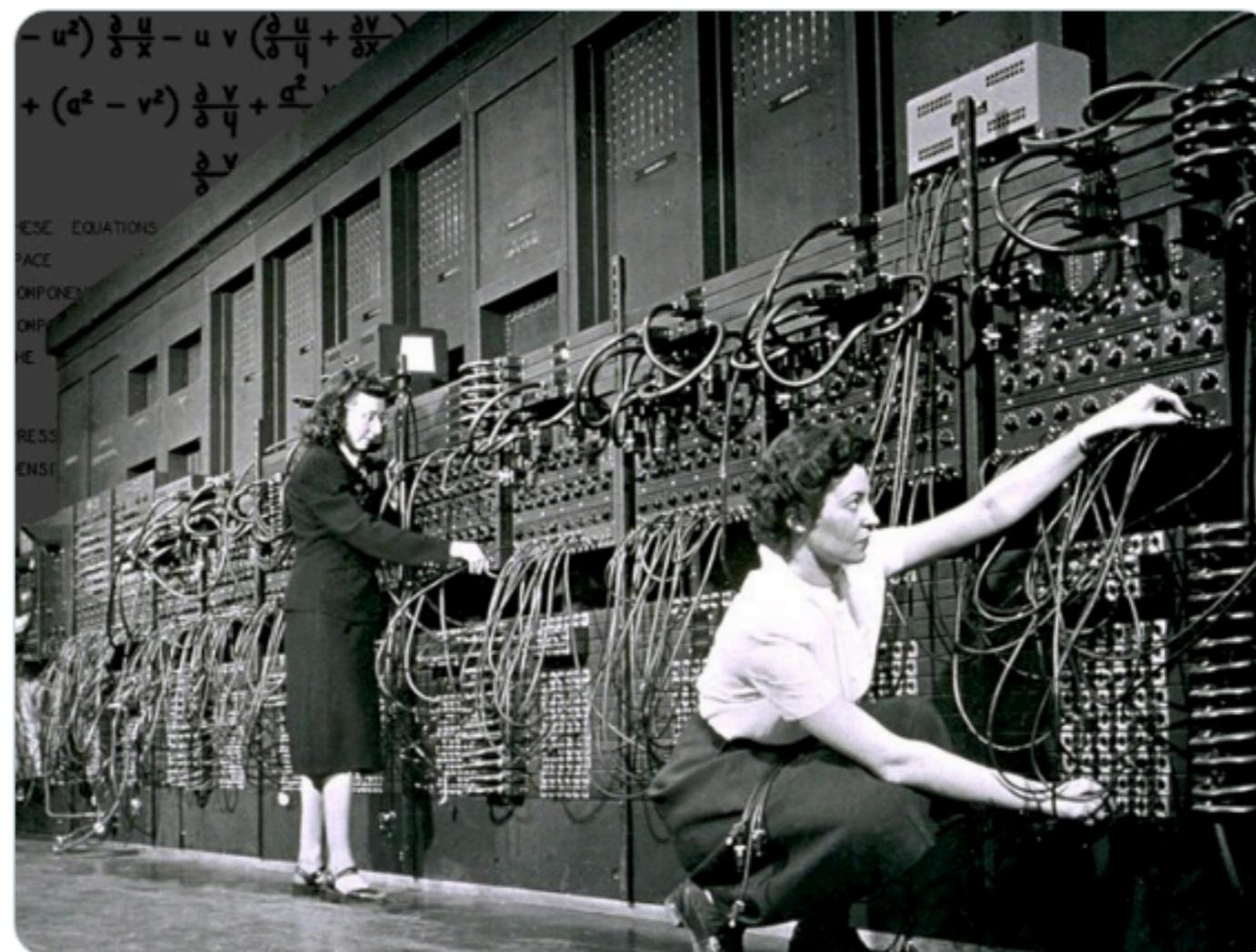
MIT CSAIL
@MIT_CSAIL

...

Today's the 75th anniversary of ENIAC, widely considered to be the first digital computer, coded by six female programmers. bit.ly/1i95x4o
@DigitalTrends

#EniacDay

(photo: Corbis/Getty Images)



11:53 AM · Feb 15, 2021 · TweetDeck

From Cubes to Transistors

- ▶ The first-generation computers are made with vacuum tubes.



vacuum computer



transistor computer

Property of Museum of History & Industry, Seattle

Commercialization



IBM System/360 (1964)

Mainframe



DEC PDP-8 (1965)

Mini-computer



Cray-1 (1976)

Supercomputer

Supercomputers

Rank	System	Cores	Rmax (TFlop/s)	Rpeak (TFlop/s)	Power (kW)
1	Supercomputer Fugaku - Supercomputer Fugaku, A64FX 48C 2.2GHz, Tofu interconnect D, Fujitsu RIKEN Center for Computational Science Japan	7,630,848	442,010.0	537,212.0	29,899
2	Summit - IBM Power System AC922, IBM POWER9 22C 3.07GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband, IBM DOE/SC/Oak Ridge National Laboratory United States	2,414,592	148,600.0	200,794.9	10,096
3	Sierra - IBM Power System AC922, IBM POWER9 22C 3.1GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband, IBM / NVIDIA / Mellanox DOE/NNSA/LLNL United States	1,572,480	94,640.0	125,712.0	7,438

LINPACK benchmark :

Rmax = maximal performance obtained

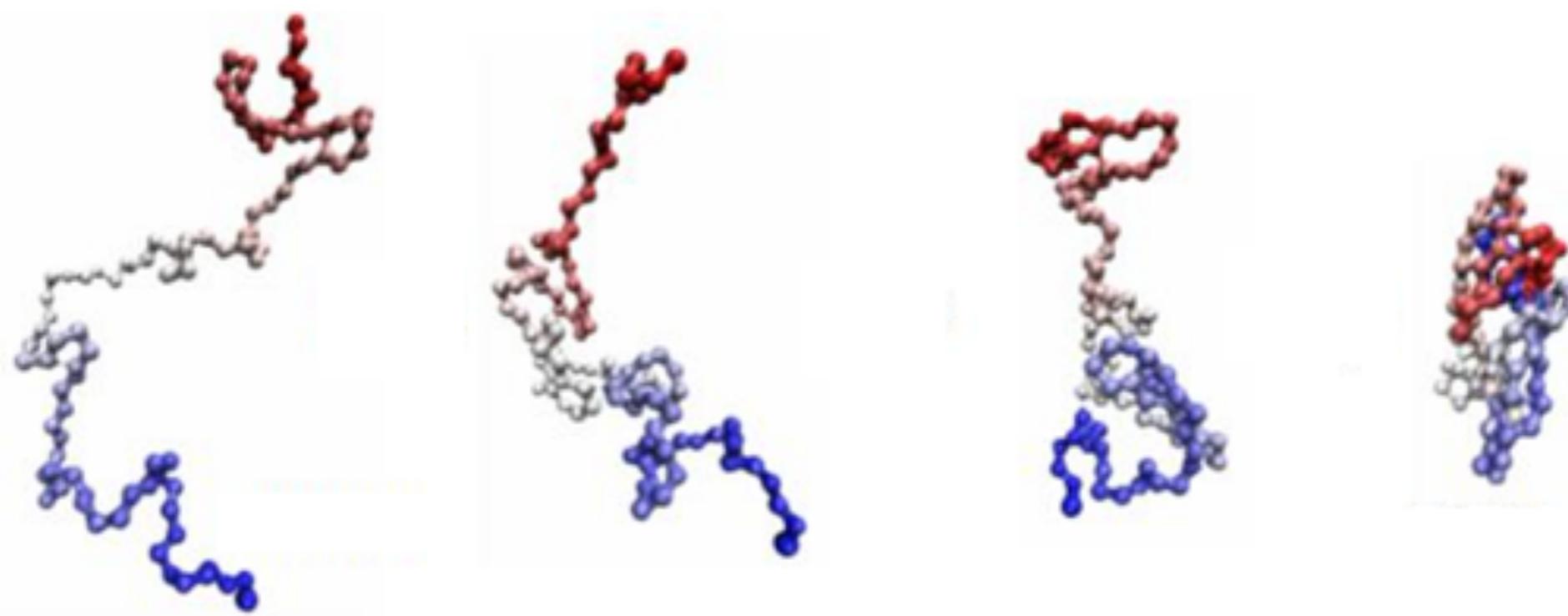
Rpeak = theoretical maximal performance

A 1 teraFLOPS (TFLOPS) computer system is capable of performing one trillion (10^{12}) floating-point operations per second

Why we need ever-increasing performance

- ▶ Computational power is increasing, but so are our computation problems and needs.
- ▶ Problems we never dreamed of have been solved because of past increases, such as decoding the human genome.
- ▶ More complex problems are still waiting to be solved.

Protein folding



Highly accurate protein structure prediction with AlphaFold

John Jumper , Richard Evans, ... Demis Hassabis 

+ Show authors

Nature 596, 583–589 (2021) | [Cite this article](#)

525k Accesses | 714 Citations | 2962 Altmetric | [Metrics](#)

Abstract

Proteins are essential to life, and understanding their structure can facilitate a mechanistic understanding of their function. Through an enormous experimental effort^{1,2,3,4}, the structures of around 100,000 unique proteins have been determined⁵, but this represents a small fraction of the billions of known protein sequences^{6,7}. Structural coverage is bottlenecked by the months to years of painstaking effort required to determine a single protein structure. Accurate computational approaches are needed to address this gap and to enable large-scale structural bioinformatics. Predicting the three-dimensional structure that a protein will adopt based solely on its amino acid sequence—the structure prediction component of the ‘protein folding problem’⁸—has been an important open research problem for more than 50 years⁹. Despite recent progress^{10,11,12,13,14}, existing methods fall far short of atomic accuracy, especially when no homologous structure is available. Here we provide the first computational method that can regularly predict protein structures with atomic accuracy even in cases in which no similar structure is known. We validated an entirely redesigned version of our neural network-based model, AlphaFold, in the challenging 14th Critical Assessment of protein Structure Prediction (CASP14)¹⁵, demonstrating accuracy competitive with experimental structures in a majority of cases and greatly outperforming other methods. Underpinning the latest version of AlphaFold is a novel machine learning approach that incorporates physical and biological knowledge about protein structure, leveraging multi-sequence alignments, into the design of the deep learning algorithm.

Drug discovery



Russell Kaplan
@russelljkaplan

1/ A friend runs a biotech startup designing drugs to fight cancer.

make a protein that binds to just one receptor. Binding to just one receptor might be a growth, but binds faster.

5:02 AM · Jan 23, 2022



Russell Kaplan
@russelljkaplan

...

2/ If they make a protein that binds to just one receptor. Binding to just one receptor might be a growth, but binds faster.

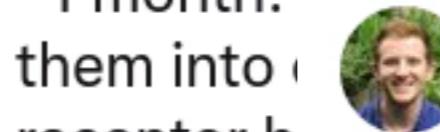
5:02 AM · Jan 23, 2022



Russell Kaplan
@russelljkaplan

...

3/ Before AlphaFold, finding such a protein would take ~1 month: they need to put them into a receptor b DNA.



Russell Kaplan
@russelljkaplan

...

4/ AlphaFold unlocked a different approach: it found the 3D structure of the existing protein & receptors, which was unknown. With the structure + another ML model, they saw how binding affinities would change with different mutations. This led to an ideal candidate in 8 hours.

5:02 AM · Jan 23, 2022 · Twitter Web App

NREL Acquires Next-Generation High Performance Computing System

Kestrel Will Take Flight and Bring the United States Closer to a Clean Energy Future

Dec. 1, 2021



As the dedicated HPC system for EERE, Kestrel will play a critical role in computing across the research portfolio, advancing research in computational materials, continuum mechanics, and large-scale simulation and planning for future energy systems. Rapidly advancing applications and technologies in artificial intelligence (AI) and machine learning are fostering innovation and expansion of research into new directions for computing. These workflows drive complementary physics and data-driven approaches by fusing simulation with new sensor data sources. Kestrel's heterogeneous architecture—which includes both CPU-only and GPU-accelerated nodes—is designed to enable these emerging workflows, providing EERE and industry partners with the ability to tackle the energy challenges for moving into a renewable and sustainable future.

Climate modeling

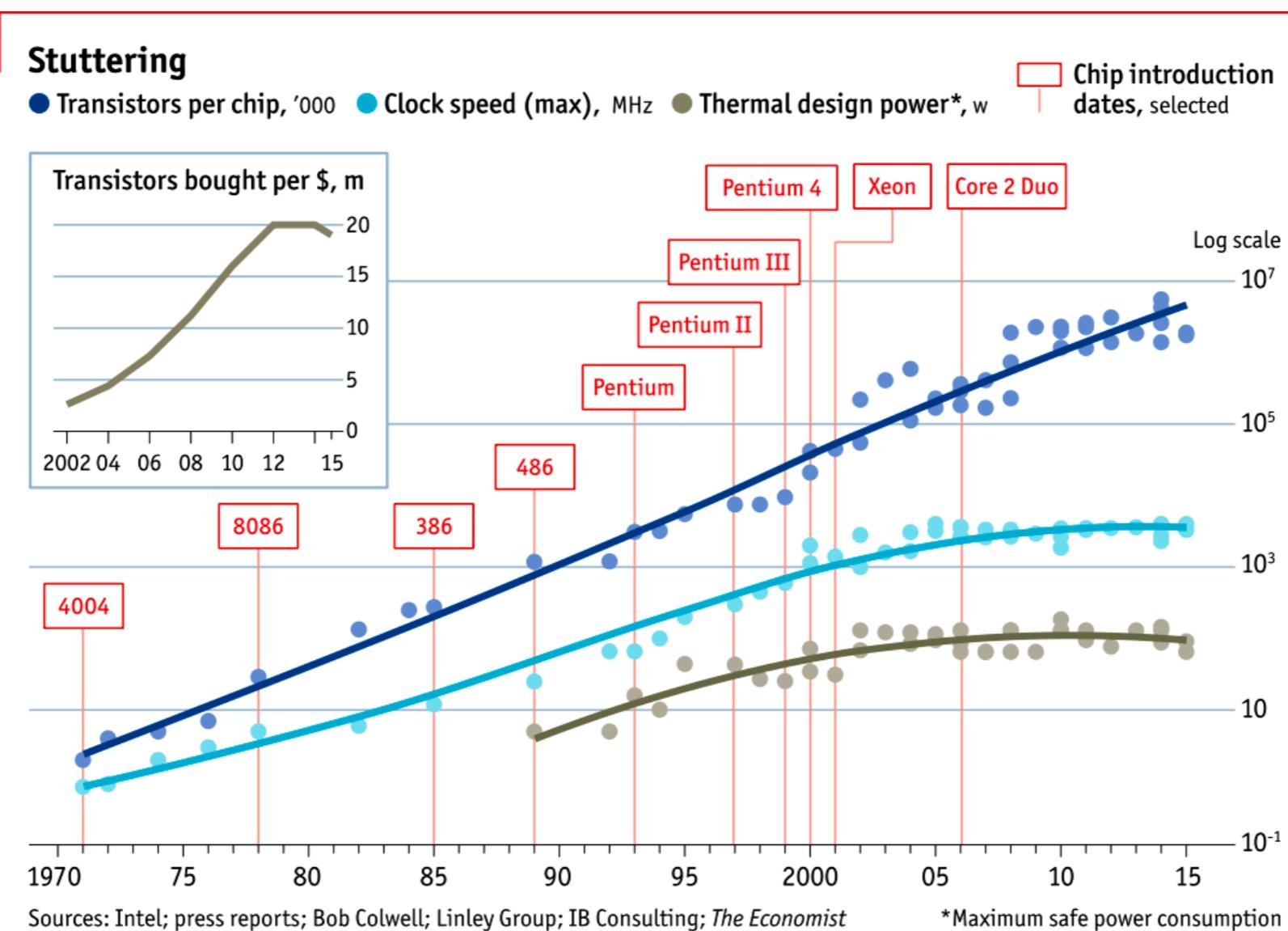


Data analysis



Moore's Law

- From 1986 to 2002, microprocessors were speeding like a rocket, increasing in performance an average of 50% per year.



- Since then, it's dropped to about a 20% increase per year.

Why we're building parallel systems

- ▶ Up to now, performance increases have been attributable to the increasing density of transistors.
- ▶ But there are inherent problems.



discrete transistors



integrated circuits

A little physics lesson

- ▶ **Smaller transistors = faster processors.**
- ▶ **Faster processors = increased power consumption.**
- ▶ **Increased power consumption = increased heat.**
- ▶ **Increased heat = unreliable processors.**

Power consumption

- ▶ The dominant technology for integrated circuits is called CMOS (complementary metal oxide semiconductor). For CMOS, the primary source of energy consumption is so-called dynamic energy:
 - Energy is consumed when transistors switch states from 0 to 1 and vice versa.

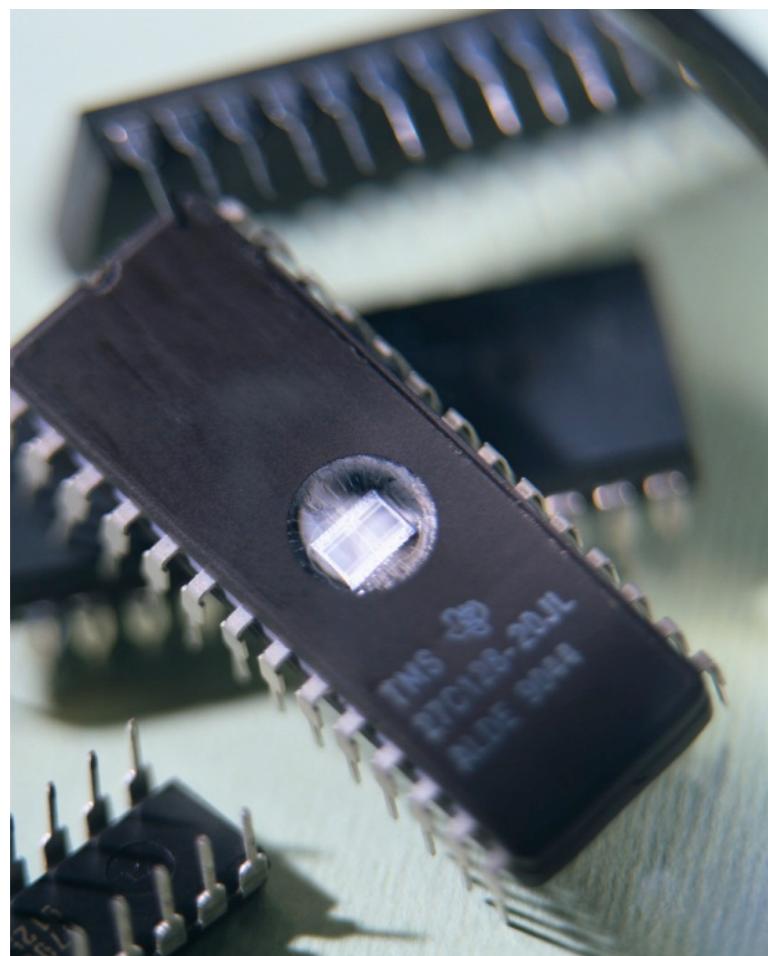
$$Energy \propto 1/2 \times \text{Capacitive load} \times \text{Voltage}^2$$

$$Power \propto 1/2 \times \text{Capacitive load} \times \text{Voltage}^2 \times \text{Frequency switched}$$

- ▶ Further lowering of the voltage appears to make the transistors too leaky.

Solution

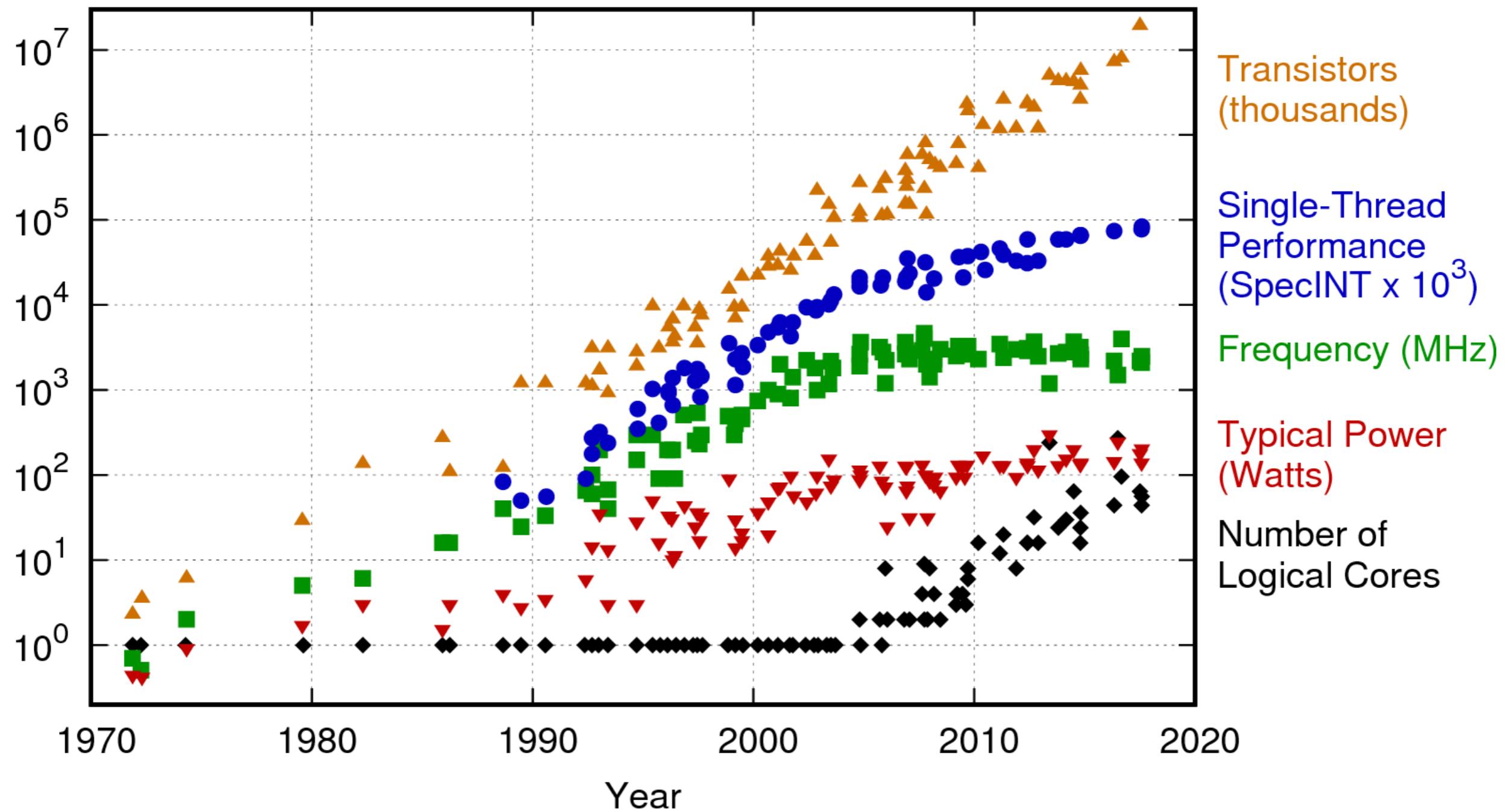
- ▶ Move away from single-core systems to multicore processors.
- ▶ “core” = central processing unit (CPU)



Introducing parallelism!!!

Multiprocessor

42 Years of Microprocessor Trend Data



Original data up to the year 2010 collected and plotted by M. Horowitz, F. Labonte, O. Shacham, K. Olukotun, L. Hammond, and C. Batten
New plot and data collected for 2010-2017 by K. Rupp

Multiprocessor

- ▶ Rather than continuing to decrease the response time of a single program running on the single processor, as of 2006 all desktop and server companies are shipping microprocessors with multiple processors ("cores") per chip, where the benefit is often more on throughput than on response time.
 - For programmers to get significant improvement in response time, they need to rewrite their programs to take advantage of multiple processors.