

Department of Electrical and Computer Engineering

Subject: Linear Integrated Circuit Design

LAB # 2

	• Iviuiiaiiii	nad Haseeb	Reg. No	200718	Section:	BEEE-7A
Objective: _S	Schematic	and Simulation	on Environ	ment on Cac	lence Virtuoso)
<u>]</u>	Build a cir	cuit schematic	and simula	ite it		
LAB ASSESS	MENT:					
Attribu	ıtes	Excellent (5)	Good (4)	Average (3)	Satisfactory (2)	Unsatisfactor (1)
Ability to Co Experiment	nduct					
Ability to ass the results	imilate					
Effective use equipment ar follows the la rules	nd					
Гotal Marks: _ LAB REPOR?				Obtained Ma	arks :	
Attribu	ıtes	Excellent (5)	Good (4)	Average (3)	Satisfactory (2)	Unsatisfactor (1)
Data present	ation					
Data present						

Date:	Signature:
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Title:

• Schematic and Simulation Environment on Cadence Virtuoso

Equipment:

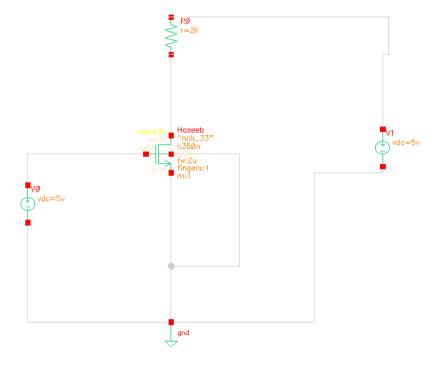
- Personal Computer
- VMware software
- Cadence Virtuoso

Introduction

In this lab we will build a circuit on Cadence virtuoso and simulate and get results in the form of a graph. We will also understand how to label and get values from any plot that is generated.

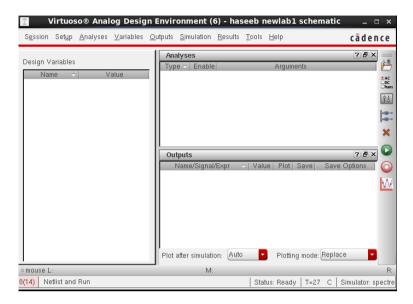
Procedure

Once circuit is built click on "**check and save**" and if nothing pops up that means there are no errors in the wire connections of the circuit. The circuit that is made on Cadence is given below

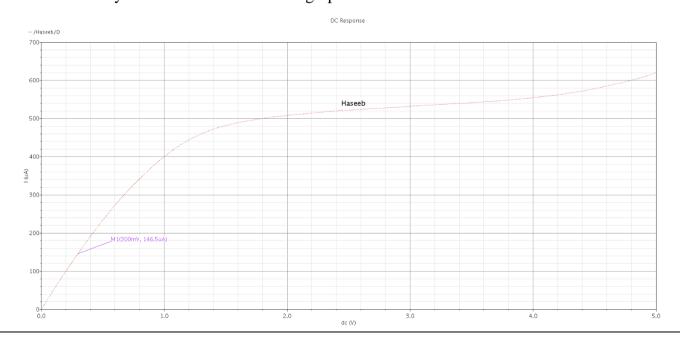


The steps to simulate are as follows:

• Click on launch and then select ADE L.



- Right click under design variable and select "copy from cellview".
- Assign a value then right click on the analysis window and choose the type of analysis needed for example DC or transient.
- Select design perimeter and range and to select the value on the y axis click on output and select 'to be plotted' and select the point on the schematic.
- Finally click on run to obtain the graph



CONC:	<u>LUSION</u>					
n this la	b will learned to arameters and the abel the graph with	variable that we	e need to plot	on the x and y a	xis.We also lear	rned