


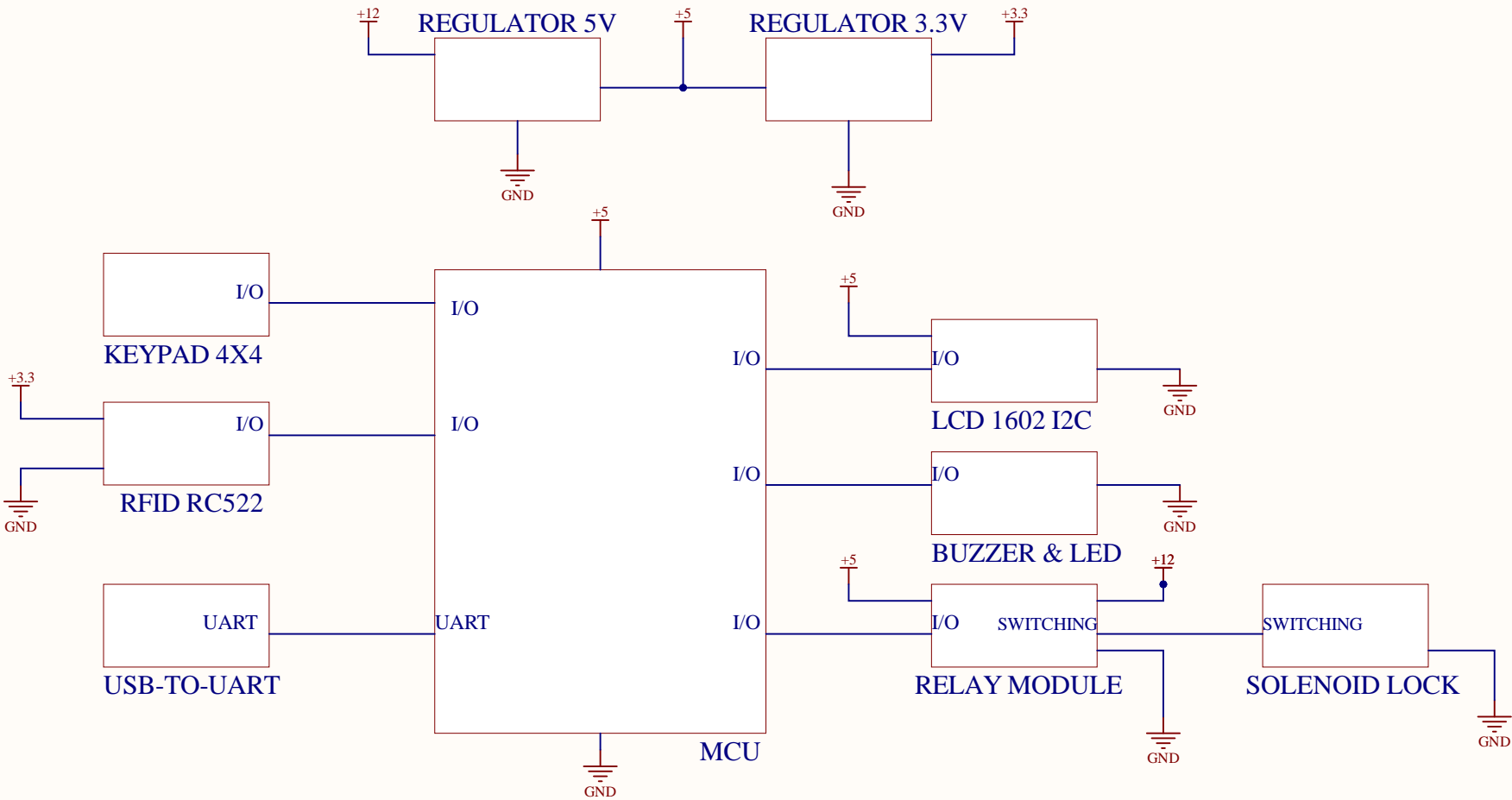
RELEASED OCTOBER-2025

Page .....	Index .....
1	COVER PAGE
2	BLOCK DIAGRAM
3	POWER SUPPLY
4	MICROCONTROLLER & OUTPUT DISPLAY
5	USER INPUT & ACTUATOR

Project: <i>IOT DOORLOCK USING ESP32</i>		
Page Contents: <i>[01] - INDEXPAGE.SchDoc</i>		
Revision: <b>v1</b>	Design: <b>N&amp;K</b>	Date: <b>5/10/2025</b>
Size: <b>A4</b>	Document Number: <b>1</b>	Sheet <b>1</b> of <b>5</b>



# BLOCK DIAGRAM



Project: ***IOT DOORLOCK USING ESP32***

Page Contents: ***[02] - BLOCKDIAGRAM.SchDoc***

Revision: **v1**

Design: **N&K**

Date: **5/10/2025**

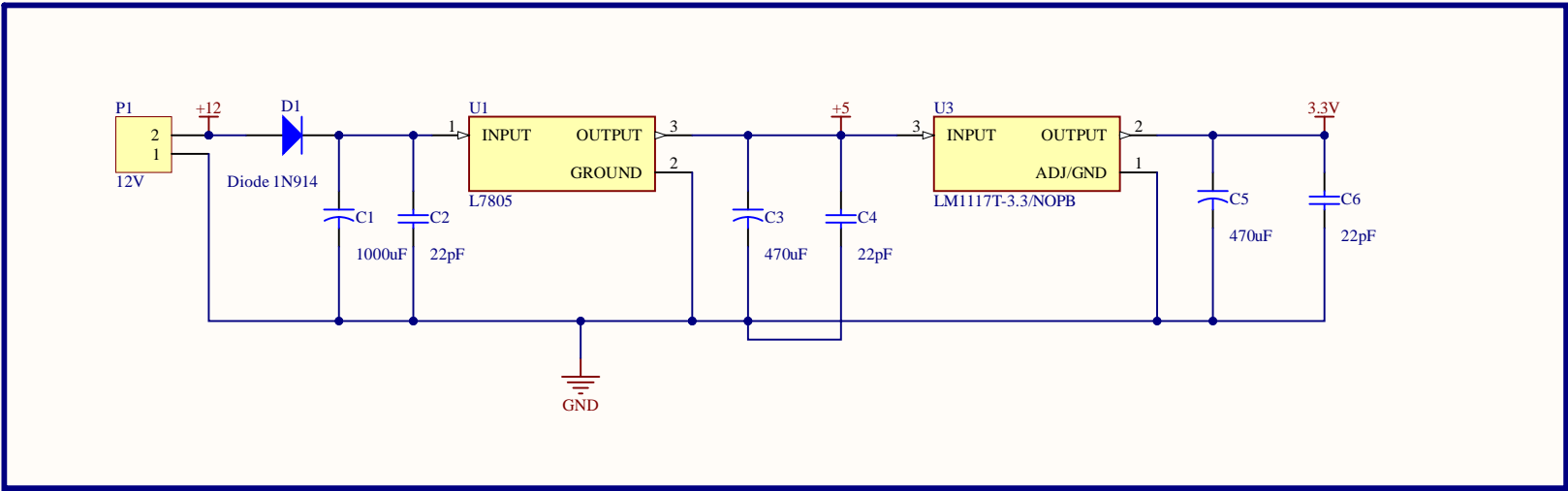
Size: **A4**

Document Number: **2**

Sheet **2** of **5**



# POWER SUPPLY



Project: ***IOT DOORLOCK USING ESP32***

Page Contents: ***[03] - POWERSUPPLY.SchDoc***

Revision: **v1**

Design: **N&K**

Date: **5/10/2025**

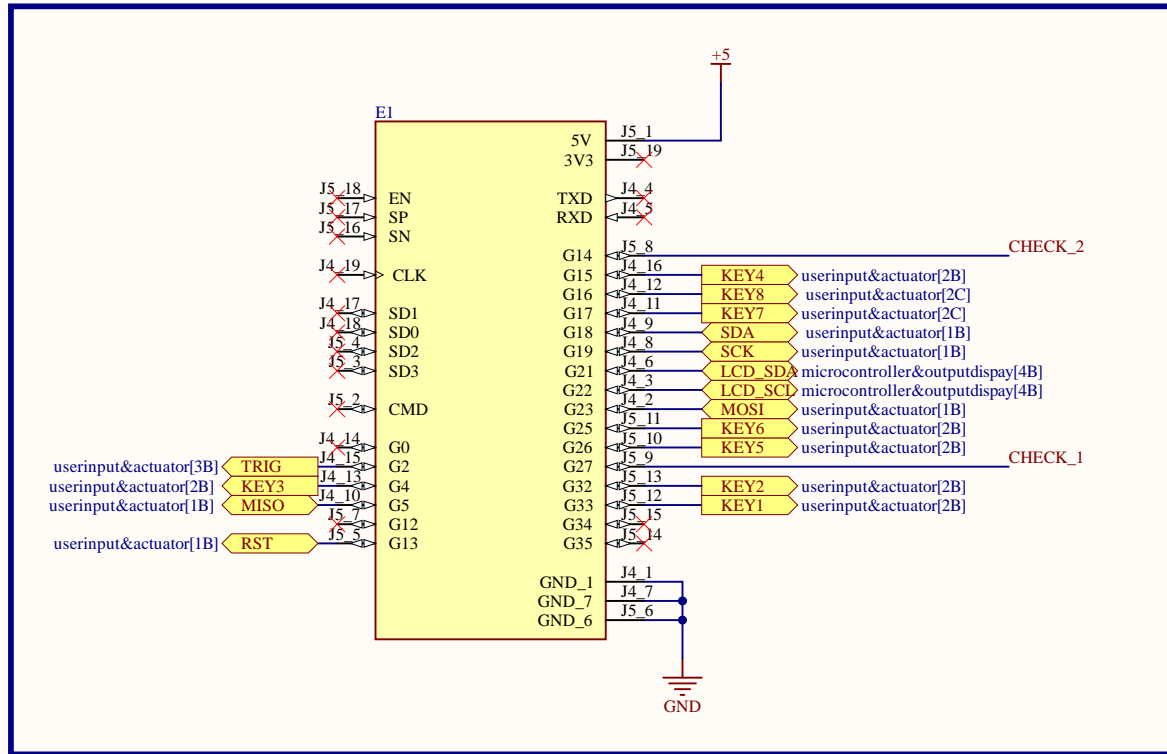
Size: **A4**

Document Number: **3**

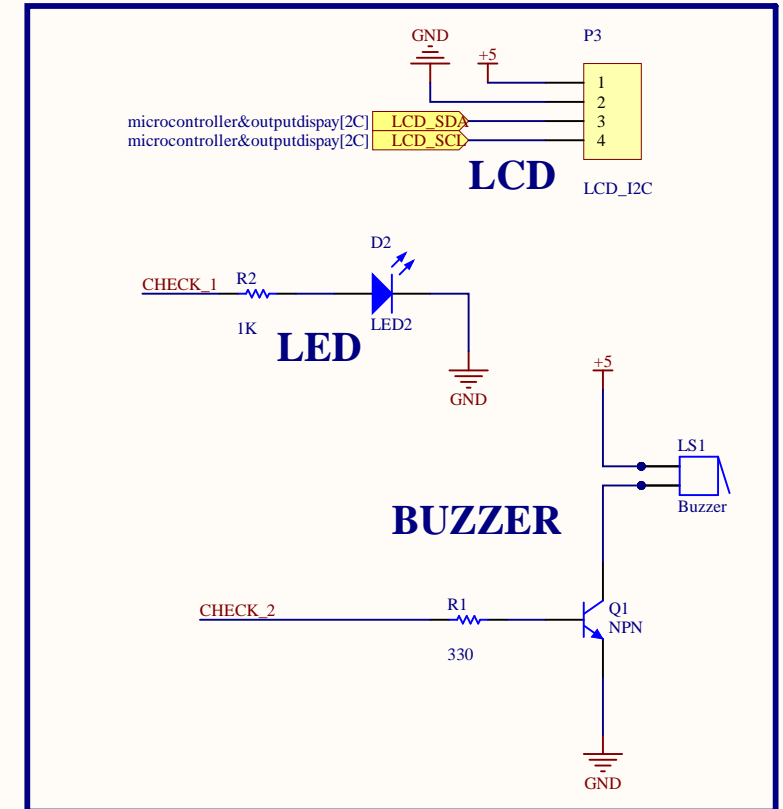
Sheet **3** of **5**



# MICROCONTROLLER



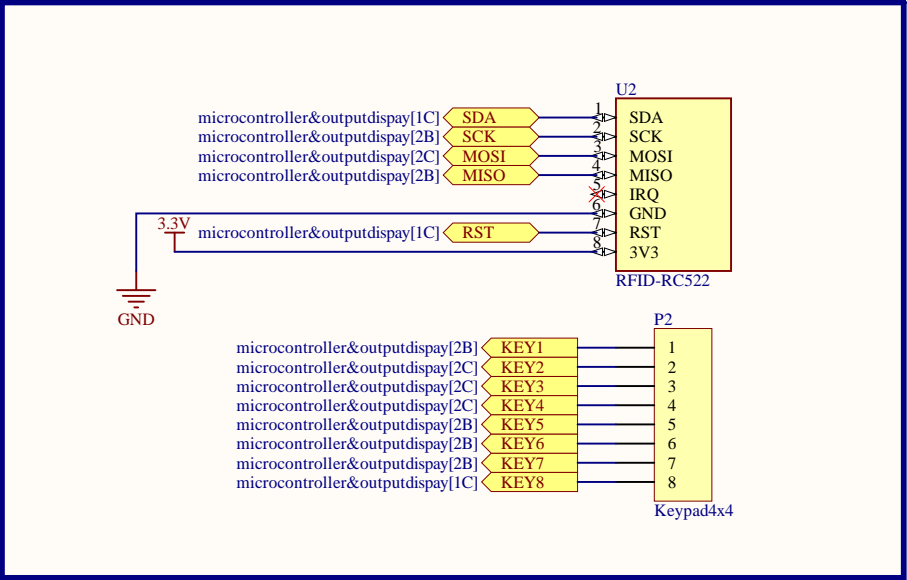
# OUTPUT DISPLAY



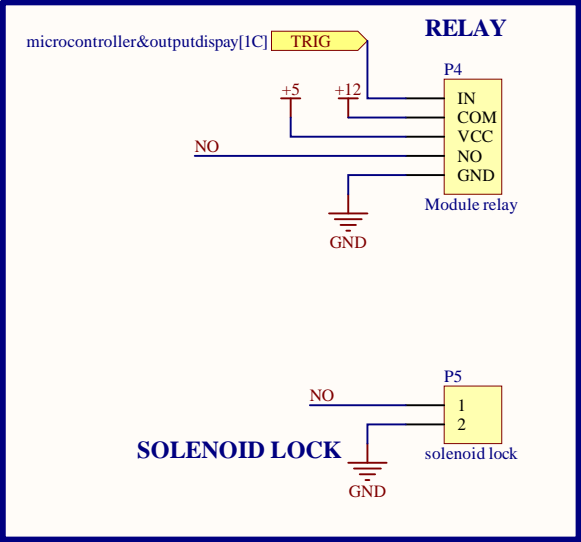
Project: <b>IOT DOORLOCK USING ESP32</b>		
Page Contents: <b>[04] - MICROCONTROLLER &amp; OUTPUT DISPLAY.SchDoc</b>		
Revision: <b>v1</b>	Design: <b>N&amp;K</b>	Date: <b>5/10/2025</b>
Size: <b>A4</b>	Document Number: <b>4</b>	Sheet <b>4</b> of <b>5</b>



# USER INPUT



# ACTUATOR



Project: **IOT DOORLOCK USING ESP32**

Page Contents: **[05] - USERINPUT &ACTUATOR.SchDoc**

Revision: **v1** Design: **N&K** Date: **5/10/2025**

Size: **A4** Document Number: **5** Sheet **5** of **5**

