

Digital Systems Lab

CE2120

LAB 10

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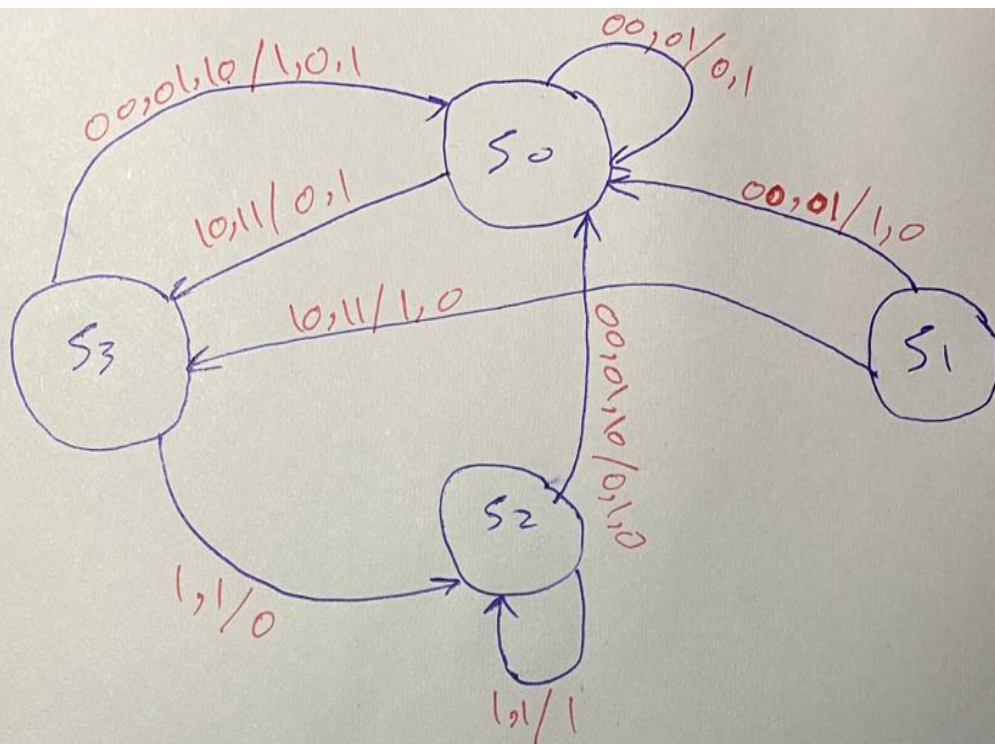
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Question 1

a. The circuit is mealy machine since it depends on the f.fs output states as well as the external inputs.

b.

Q_1	Q_2	X	Y	Q_1^+	Q_2^+	F
0	0	0	0	0	0	0
0	0	0	1	0	0	1
0	0	1	0	1	1	0
0	0	1	1	1	1	1
0	1	0	0	0	0	1
0	1	0	1	0	0	0
0	1	1	0	1	1	1
0	1	1	1	1	1	0
1	0	0	0	0	0	0
1	0	0	1	0	0	1
1	0	1	0	0	0	0
1	0	1	1	1	0	1
1	1	0	0	0	0	1
1	1	0	1	0	0	0
1	1	1	0	0	0	1
1	1	1	1	1	0	0



Question 2

