				Serializable						
Event	EventQueue	0	Zobals		Statistics	PhysicalMemory	y	Simi	Object	]
	TickEvent					Statistics				BaseTLB
						•				•
	DCacheCompletionEvent					Statistics				Basic Link
	LdWritebackfivent					Statistics				Basic Link
	BurriorEvent					Statistics				BProdUnit
	TickEvent					Statistics				BranchPredictor
	ConsumerEvent					Statistics				RubyCache
	CountedDesinEvent					Statistics				CacheMemory
						Julius				
	CountedExisEvent									ClockDomain
	Cpullwent									ClockDomain
	Traptivent									ClockedObject
	PinishTranslationEvent									ClockedObject
	EndQuiesceEvent									CPA
	DoneEvent									DirectedGenerator
	TuEvent									DirectoryMemory
	EventWrapper< T, F >									RubyDirectoryMemory
	EventWtapper : DescCache,&DescCache: fetchComplete >									DiskImage
										•
	EventWrapper < DescCache_&DescCache::fetchDescriptors I >									DiskImage
-	EventWrapper< DescCache,&DescCache::wbComplete >									EtherDump
	EventWrapper - DescCache,&DescCache::writeback1 >									EtherObject
	Activate/Threadflivent									FaultModel
	DeaflocateContextEvent									FashModel
	TickEvent									FUDesc
										•
	DenaDoneTwent									FUDesc
	DCacheCompletionEvent									OpDesc
	CPUEscox									FUPool
	TickEvent									FUPool
	PUCompletion									GenericTimer
	Counterfivent									IdeDisk
	LocalSimLoopExist/vent									IntrControl
	•									•
	WritebackEvent									IntrControl
	TrapEvent									KvmVM
	RTCEsonz									OpDesc
										•
	RTCTickEvent									Platform
	MonCartEvent									Platform
	TickEvent									Prefetcher
	CPOEvent									ProbeLinenerObject
	CPOEvent									Process
	TickEvent									Process
	TickEvent: Impl >									GenericTimer
	Store Completion Except									Root
	WrisebackEvent									Root
										•
	Posthstřvent									DirectedGenerator
	PythonEvent								<b>—</b>	SimpleDisk
	ResourceDvent									SimpleDisk
	ResPoolEvent									SouthBridge
	DirectedStartEvent								-	SouthBridge
	RubyEvent									Terminal
	CheckStantDvent									Terminal
	SequencerWakeupEvent								<b>—</b>	InstTracer
	Ipelivent									Vactaput
	TickEvent									VoltageDomain
<u> </u>	Intelligent									VoltageDomain
	PostVIntEvent									WireBuffer
									1	