

PCStateBase

```
graph BT; DelaySlotUPCState[DelaySlotUPCState< MachInst >] --> DelaySlotPCState[DelaySlotPCState< MachInst >]; DelaySlotPCState --> SimplePCState[SimplePCState< MachInst >]; SimplePCState --> PCStateBase[PCStateBase]
```

SimplePCState< MachInst >

DelaySlotPCState< MachInst >

DelaySlotUPCState< MachInst >