

A

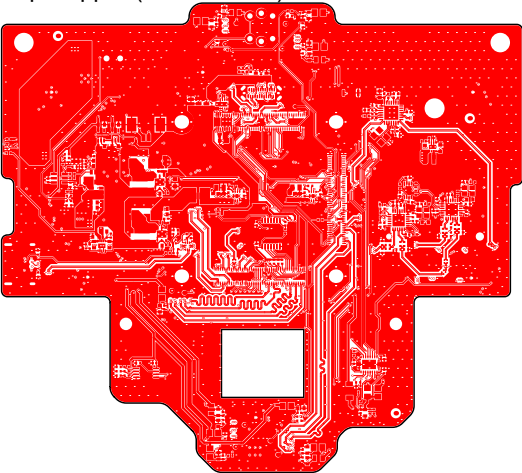
B

C

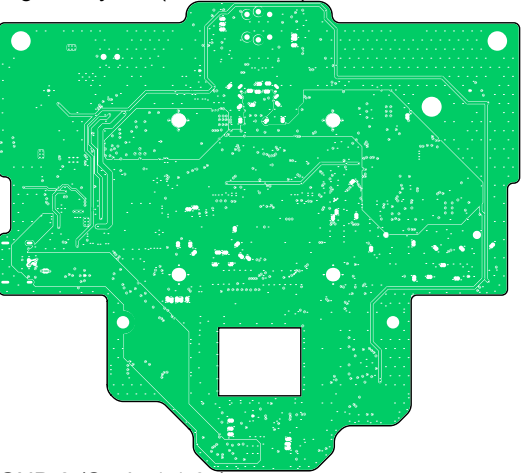
D

E

Top Copper (Scale 1:1.67)



Signal Layer 1 (Scale 1:1.67)



FABRICATION NOTES:

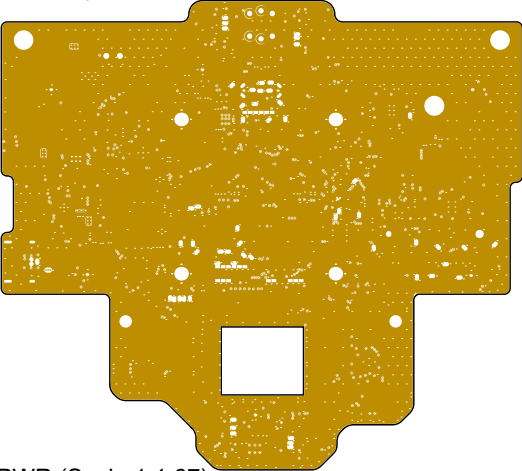
Fabricate per IPC-6011 & IPC-6012 CLASS 2
Inspect per IPC-A-600 CLASS 2
Test per IPC-TM-650

- * PCB has 6 copper layers
- * Copper thicknesses are finished and include base foil plus Cu plating on plated layers.
- * PCB thickness: 63mil +/- 3mil
- * Min. trace width/clearance: 0.089mm/0.1mm
- * Min. hole drill/ring: 8mil/16mil
- * Soldermask gang relief is allowed for pads in same footprint, if footprint is NSMD.
- * Silkscreen, non-conductive epoxy ink, color: white
- * Solder mask color: green
- * Remove slikscreen as needed to prevent ink on any exposed copper
- * Surface finish: ENIG
- * Hole dimensions are finished size, +/-3mil
- * Linear board dimension tolerance: +/-10mil
- * Bow, twist, warp not to exceed 0.75% of greatest diagonal span
- * PCB shall be UL Recognized printed wiring board (ZPMV2), minimum flammability rating 94V-0
- * PCB shall be marked with fabricator company or trade name, UL mark, and date code using legend ink on secondary side
- * All PCBs shall be electrically tested for opens and shorts per gerber. Test marking shall be marked on secondard side.

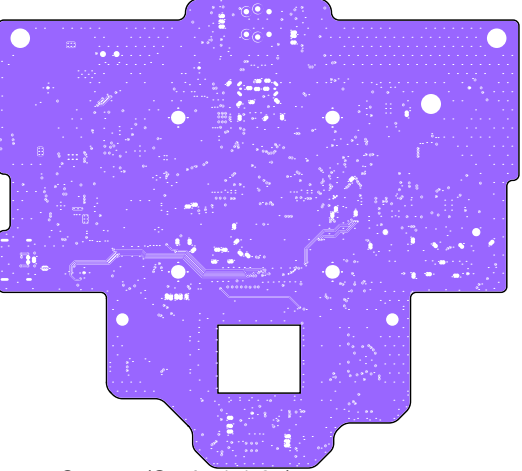
Fabricator shall panelize the PCB using mouse bites and tab routing. V-scoring not allowed.

Controlled impedance differential pairs shall be within +/-10% for 100ohm targets, and +/-10% for 90ohm targets. See Sheet 3 for transmission line details and location of 90ohm differential pairs.

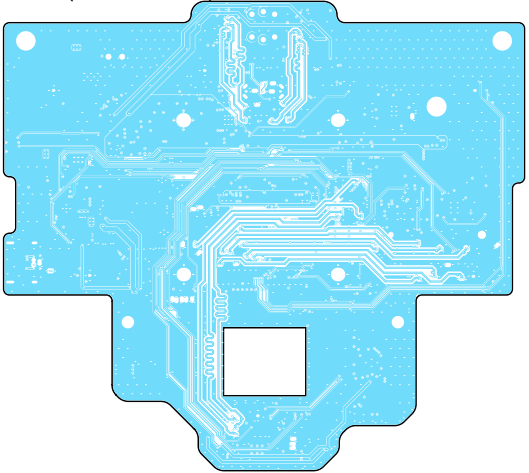
GND 1 (Scale 1:1.67)



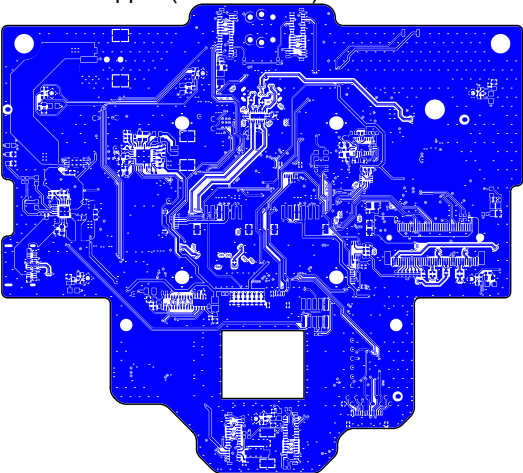
GND 2 (Scale 1:1.67)



PWR (Scale 1:1.67)



Bottom Copper (Scale 1:1.67)



Title: **DM3370**

Number: DXXXX Revision: R5M2E5

Date: 24/05/2023 Sheet: 1 of 3

Drawn by: David Malovrh

LUXonjs

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A

B

C

D

E

Layer Stack Legend

| Layer | Thickness | Type | Gerber | Df | Dk |
|----------------|-------------------|-------------|--------|----|------|
| Top Overlay | | Legend | GTO | | |
| Top Mask | 0.59mil(0.015mm) | Solder Mask | GTS | | 3,8 |
| Top Copper | 1.38mil(0.035mm) | Signal | GTL | | |
| | 3.94mil(0.100mm) | Dielectric | | | 4,05 |
| GND 1 | 0.71mil(0.018mm) | Signal | G1 | | |
| | 22.24mil(0.565mm) | Dielectric | | | 4,5 |
| PWR | 0.71mil(0.018mm) | Signal | G2 | | |
| | 5.00mil(0.127mm) | Dielectric | | | 4,25 |
| Signal Layer 1 | 0.71mil(0.018mm) | Signal | G3 | | |
| | 22.24mil(0.565mm) | Dielectric | | | 4,5 |
| GND 2 | 0.71mil(0.018mm) | Signal | G4 | | |
| | 3.94mil(0.100mm) | Dielectric | | | 4,05 |
| Bottom Copper | 1.38mil(0.035mm) | Signal | GBL | | |
| Bottom Mask | 0.59mil(0.015mm) | Solder Mask | GBS | | 3,8 |
| Bottom Overlay | | Legend | GBO | | |

Drill Table

Total thickness: 64.13mil(1.629mm)

| Symbol | Count | Hole Size | Plated | Hole Type | Via / Pad | Pad Shape |
|--------|------------|--------------------|------------|-----------|-----------|-----------|
| ☆ | 21 | 7.87mil(0.200mm) | Plated | Round | (Mixed) | (Mixed) |
| ⊕ | 1739 | 8.00mil(0.203mm) | Plated | Round | (Mixed) | (Mixed) |
| ✕ | 16 | 10.00mil(0.254mm) | Plated | Round | Via | |
| A | 6 | 11.81mil(0.300mm) | Plated | Round | Via | |
| ☆ | 6 | 19.69mil(0.500mm) | Non-Plated | Round | Pad | Rounded |
| □ | 2 | 19.69mil(0.500mm) | Plated | Slot | Pad | Rounded |
| ✱ | 4 | 23.62mil(0.600mm) | Plated | Slot | Pad | Rounded |
| ○ | 4 | 27.56mil(0.700mm) | Plated | Slot | Pad | Rounded |
| ☆ | 3 | 43.31mil(1.100mm) | Non-Plated | Round | Pad | Rounded |
| ⊕ | 6 | 47.24mil(1.200mm) | Plated | Round | Pad | Rounded |
| ◇ | 1 | 62.99mil(1.600mm) | Non-Plated | Round | Pad | Rounded |
| D | 2 | 108.27mil(2.750mm) | Plated | Round | Pad | Rounded |
| ✕ | 4 | 118.11mil(3.000mm) | Plated | Round | Pad | Rounded |
| C | 2 | 169.29mil(4.300mm) | Plated | Round | Pad | Rounded |
| ▽ | 1 | 173.23mil(4.400mm) | Plated | Round | Pad | Rounded |
| | 1817 Total | | | | | |

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Sheet: 2 of 3

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A

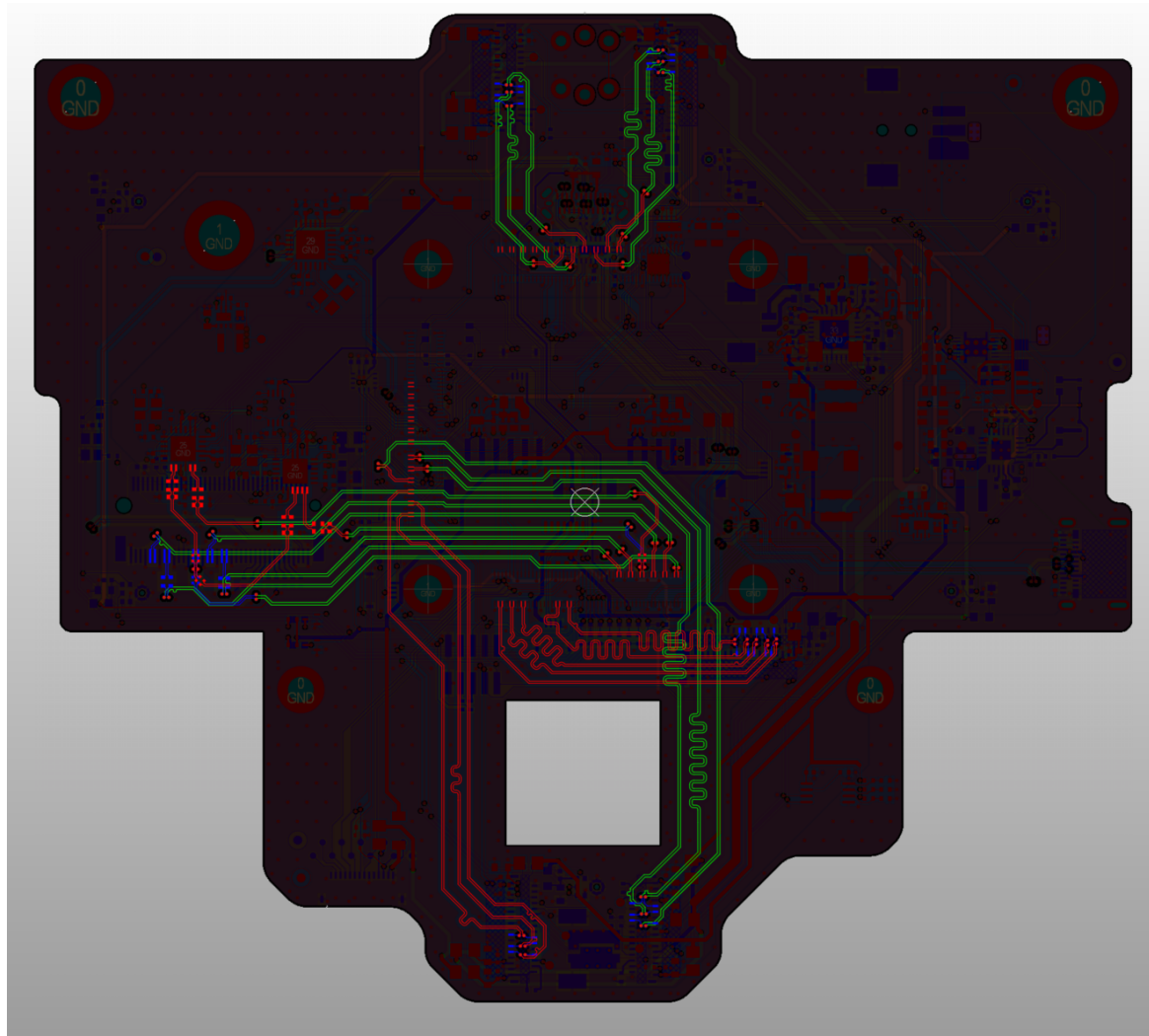
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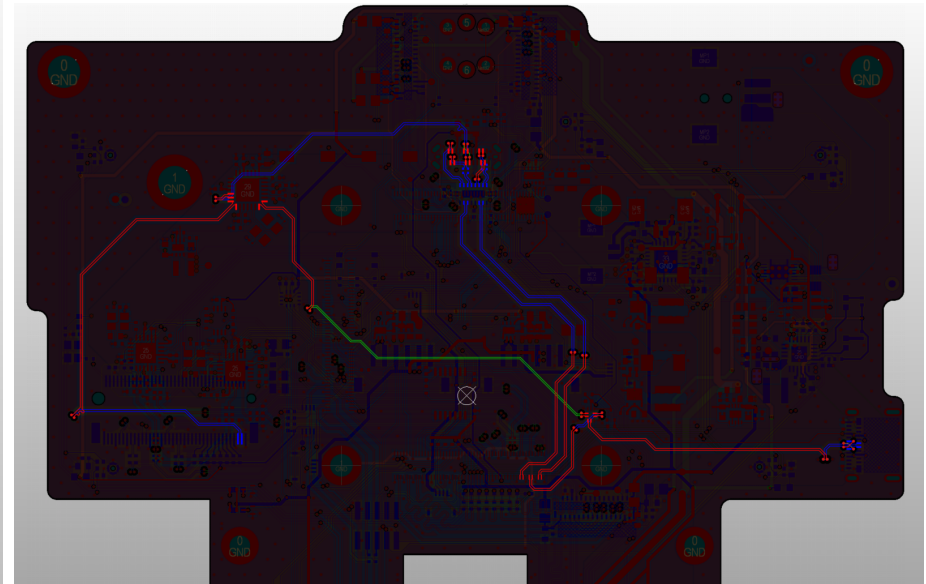
D

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100 OHM (+/-10%) DIFF PAIRS



90 OHM (+/-10%) DIFF PAIRS



Transmission Line Structure Table

| Target Impedance | Trace layer | Reference layers | Lower Trace Width | Upper Trace Width | Gap |
|------------------|---------------|----------------------|-------------------|-------------------|------------------|
| 100 | Top Copper | GND 1 | 3.98mil(0.101mm) | 3.98mil(0.101mm) | 5.00mil(0.127mm) |
| 90 | Top Copper | GND 1 | 5.51mil(0.140mm) | 5.51mil(0.140mm) | 5.00mil(0.127mm) |
| 50 | Top Copper | GND 1 | 5.55mil(0.141mm) | 5.55mil(0.141mm) | |
| 90 | PWR | GND 1,Signal Layer 1 | 4.02mil(0.102mm) | 4.02mil(0.102mm) | 4.09mil(0.104mm) |
| 100 | Bottom Copper | GND 2 | 3.98mil(0.101mm) | 3.98mil(0.101mm) | 5.00mil(0.127mm) |
| 90 | Bottom Copper | GND 2 | 5.51mil(0.140mm) | 5.51mil(0.140mm) | 5.00mil(0.127mm) |

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