

A

B

C

D

E

FABRICATION NOTES:

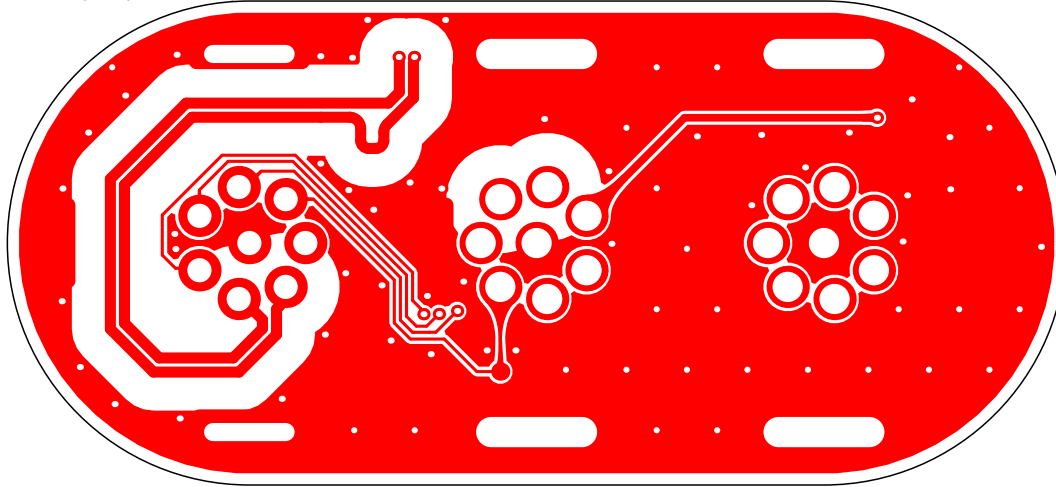
Fabricate per IPC-6011 & IPC-6012 CLASS 2
Inspect per IPC-A-600 CLASS 2
Test per IPC-TM-650

- * PCB has 4 copper layers
- * Copper thicknesses are finished and include base foil plus Cu plating on plated layers.
- * PCB thickness: 63mil +/- 3mil
- * Min. trace width/clearance: 0.1mm/0.1mm
- * Min. hole drill/ring: 8mil/16mil
- * Soldermask gang relief is allowed for pads in same footprint, if footprint is NSMD.
- * Silkscreen, non-conductive epoxy ink, color: white
- * Solder mask color: black
- * Remove silkscreen as needed to prevent ink on any exposed copper
- * Surface finish: ENIG
- * Hole dimensions are finished size, +/-3mil
- * Linear board dimension tolerance: +/-10mil
- * Bow, twist, warp not to exceed 0.75% of greatest diagonal span
- * PCB shall be UL Recognized printed wiring board (ZPMV2), minimum flammability rating 94V-0
- * PCB shall be marked with fabricator company or trade name, UL mark, and date code using legend ink on secondary side
- * All PCBs shall be electrically tested for opens and shorts per gerber. Test marking shall be marked on secondard side.

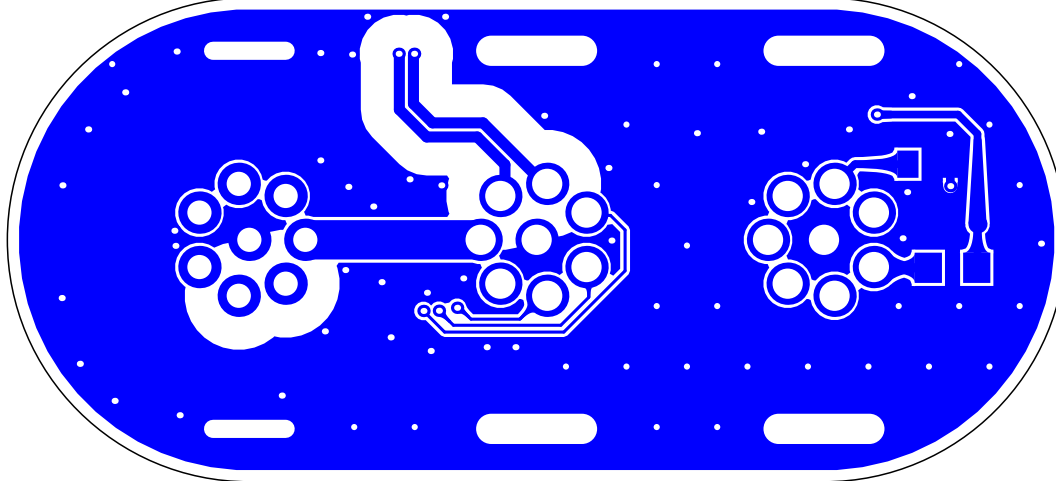
Fabricator shall panelize the PCB using mouse bites and tab routing. V-scoring not allowed.

Controlled impedance differential pairs shall be within +/-10% for 90ohm targets. See Sheet 3 for transmission line details and location of 90ohm differential pairs.

Top Layer (Scale 4:1)



Bottom Layer (Scale 4:1)

Title: **PA6940**

Number: D0000999

Revision: R1M0
E1

Date: 04/07/2023

Sheet: 1 of 3

Drawn by: Primoz Ambrozic

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Layer Stack Legend

| Layer | Thickness | Type | Gerber | Df | Dk |
|------------------------------------|-------------------|-------------|--------|------|-----|
| Top Overlay | | Legend | GTO | | |
| Top Solder | 1.00mil(0.025mm) | Solder Mask | GTS | 0.03 | 4 |
| Top Layer | 1.38mil(0.035mm) | Signal | GTL | | |
| | 59.06mil(1.500mm) | Dielectric | | 0.02 | 4.3 |
| Bottom Layer | 1.38mil(0.035mm) | Signal | GBL | | |
| Bottom Solder | 1.00mil(0.025mm) | Solder Mask | GBS | 0.03 | 4 |
| Bottom Overlay | | Legend | GBO | | |
| Total thickness: 63.81mil(1.621mm) | | | | | |

Drill Table

| Symbol | Count | Hole Size | Plated | Hole Tolerance |
|-----------|-------|-------------------|--------|----------------|
| ◇ | 29 | 7.87mil(0.200mm) | Plated | |
| ☆ | 46 | 8.00mil(0.203mm) | Plated | |
| □ | 2 | 23.62mil(0.600mm) | Plated | |
| ▣ | 8 | 31.50mil(0.800mm) | Plated | |
| ○ | 20 | 39.37mil(1.000mm) | Plated | |
| 105 Total | | | | |

Title: PA6940

Number: D0000999 Revision: R1M0E1

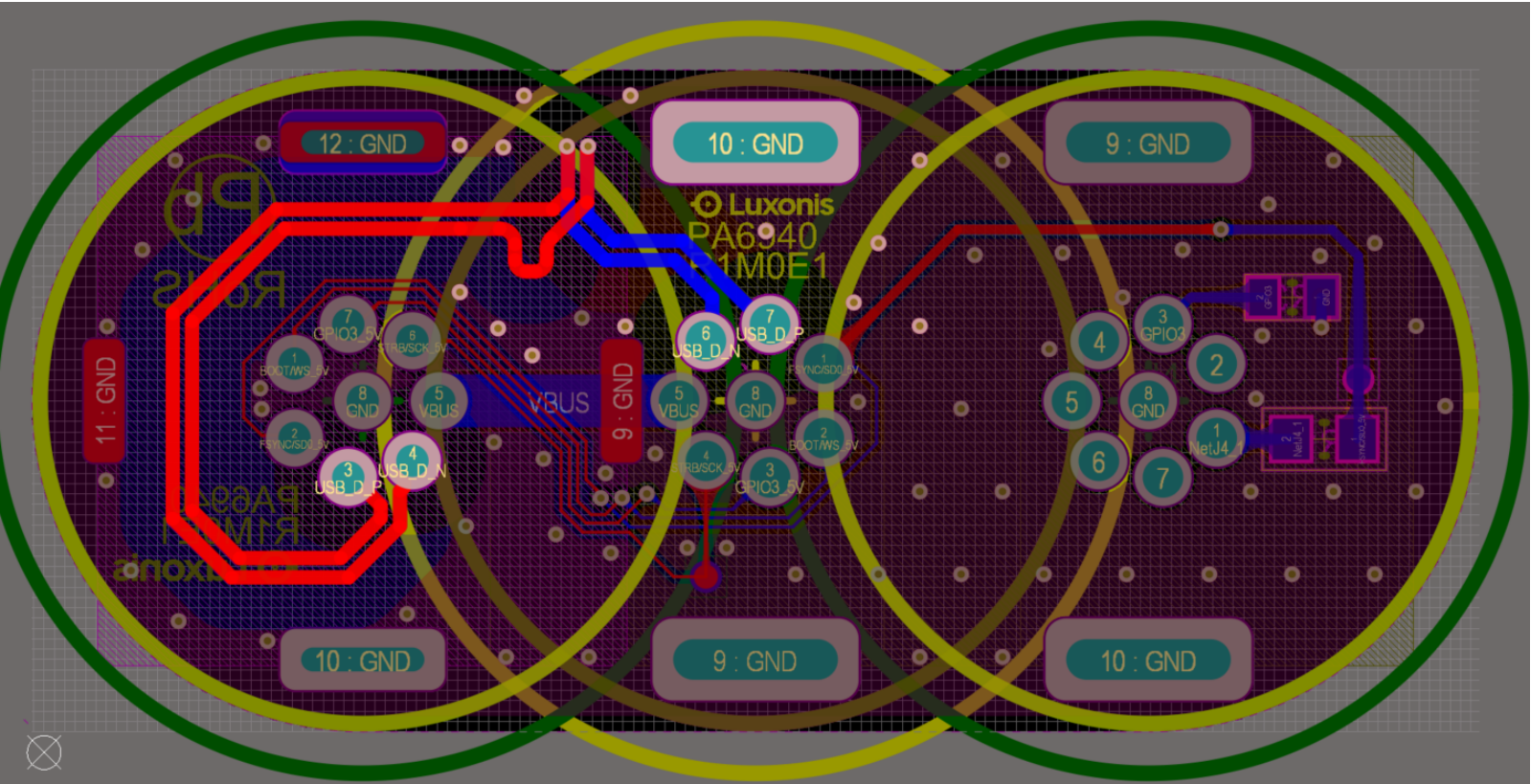
Date: 04/07/2023 Sheet: 2 of 3

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90 OHM (+/-10%) DIFF PAIRS

Transmission Line Structure Table

| Impedance Id | Target Impedance | Calculated Impedance | Trace layer | Wide Trace Width | Gap | Reference layers |
|--------------|------------------|----------------------|--------------|------------------|--------|------------------|
| 1 | 90 | 90.03 | Top Layer | 0.37mm | 0.10mm | Bottom Layer |
| 2 | 90 | 90.03 | Bottom Layer | 0.37mm | 0.10mm | Top Layer |

| | |
|---------------------------|-------------------------|
| Title: PA6940 | Revision: R1M0E1 |
| Number: D0000999 | |
| Date: 04/07/2023 | Sheet: 3 of 3 |
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