cycle	reset	pc	instr	branch	srca	srcb	ALUout	zero	pcsrc	Write data	Mem. write	Read data
1	1	00	addi $2, $0, 5 20020005	0	0	5	5	0	0	0	0	X
2	0	04	addi $3, $0, 12 2003000c	0	0	C	C	0	0	0	0	X
3	0	08	addi $7, $3, −9 2067fff7	0	C	-9	3	0	0	0	0	x
4	0	0C	or $4, $7, $2 00e22025	0	3	5	7	0	0	0	0	x
5	0	10	and $5, $3, $4 00642824	0	C	7	4	0	0	0	0	X
6	0	14	add $5, $5, $4 00a42820	0	4	7	B	0	0	0	0	x
7	0	18	beq $5, $7, end 10a7000a	0	B	3	8	0	0	0	0	X
8	0	1C	slt $4, $3, $4 0064202a	0	C	7	5	1	0	0	0	X
9	0	20	beq $4, $0,around 10800001	1	0	0	0	1	1	0	0	x
10	0	28	slt $4, $7, $2 00e2202a	0	3	5	-2	0	0	0	0	x
11	0	2C	add $7, $4, $5 00853820	0	1	B	C	0	0	0	0	X
12	0	30	sub $7, $7, $2 00e23822	0	C	5	7	0	0	0	0	x
13	0	34	sw $7, 68($3) ac670044	0	C	68	74	0	0	1	1	0
14	0	38	lw $2, 80($0) 8c020050	0	0	80	80	0	0	0	0	1
15	0	3C	j end 08000011	0	X	X	X	0	1	0	0	0
16	1	44	sw $2, 84($0) ac020054	0	0	84	84	0	0	1	1	0