

Assignment 2

Multi-Mode-Counter

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Code: 1900802

Section : 2

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Verification

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CODE

```
Ln#
1 module multi_mode_counter(
2
3     input        clk        ,
4     input        rst_n      ,
5     input        init       ,
6     input [3:0]  load_value ,
7     input [1:0]  mode_control ,
8
9     output reg    GAMEOVER   ,
10    output reg [1:0] WHO
11
12 );
13
14 |
15 //----- DECLARATIONS -----
16
17 typedef enum reg[2:0] {INITIAL = 3'b000,COUNT = 3'b001,WINNER_FLAG = 3'b010 , LOSER_FLAG = 3'b011 , COMPLETE = 3'b100} state;
18 state current_state,next_state;
19
20
21 reg WINNER;
22 reg LOSER;
23
24
25 reg unsigned[3:0] counter;
26 reg [3:0] winner_counter,loser_counter;
```

E:/counter_project/counter.sv - Default

```
Ln#
26 reg [3:0] winner_counter,loser_counter;
27
28
29
30 assign WINNER = (counter == 4'd15 && current_state == 3'b001)? 1'b1 : 0;
31 assign LOSER = (counter == 4'd00 && current_state == 3'b001)? 1'b1 : 0;
32
33 //-----
34
35 always @(posedge clk or negedge rst_n) begin
36
37     if(~rst_n) begin
38         current_state <= INITIAL;
39     end
40
41     else begin
42         current_state <= next_state;
43     end
44 end
45
46 //-----
47
48
49 always @(*)begin
50
51     case (current_state)
```

E:/counter_project/counter.sv - Default

```
Ln#
49 always @(*)begin
50
51     case (current_state)
52
53         INITIAL: begin
54             next_state = COUNT;
55         end
56
57         COUNT : begin
58
59             if(winner_counter == 4'b1111 || loser_counter == 4'b1111)
60                 next_state = COMPLETE;
61             else if(WINNER)
62                 next_state = WINNER_FLAG;
63             else if(LOSER)
64                 next_state = LOSER_FLAG;
65             else
66                 next_state = COUNT;
67
68         end
69
70         WINNER_FLAG: next_state = COUNT;
71
72         LOSER_FLAG: next state = COUNT;
```

```

E:/counter_project/counter.sv - Default *
Ln#
74         LOSER_FLAG: next_state = COUNT;
75
76         COMPLETE : next_state = INITIAL;
77
78
79         endcase
80
81     end
82
83
84     //-----
85
86     always @(posedge clk)begin
87
88         case(current_state)
89
90             INITIAL : begin
91
92                 counter <= 4'b1;
93                 GAMEOVER <= 0;
94                 WHO <= 0;
95                 winner_counter <=0;
96                 loser_counter <= 0;
97                 end
98             |
99
100         COUNT : begin
101
102             if(init)begin

```

```

E:/counter_project/counter.sv - Default *
Ln#
99
100     COUNT : begin
101
102         if(init)begin
103             counter <= load_value;
104             end
105
106
107         else if (mode_control == 2'b00)begin
108             counter <= counter + 1 ;
109             end
110
111
112         else if(mode_control == 2'b01)begin
113             counter <= counter + 2;
114             end
115
116
117         else if(mode_control == 2'b10)begin
118             counter <= counter - 1;
119             end
120
121
122         else if(mode_control == 2'b11)begin
123             counter <= counter - 2;
124             end
125
126         end
127

```

Ln#	
126	end
127	
128	
129	
130	WINNER_FLAG: begin
131	
132	winner_counter = winner_counter + 1;
133	end
134	
135	
136	LOSER_FLAG: begin
137	
138	loser_counter = loser_counter + 1;
139	end
140	
141	
142	COMPLETE : begin
143	GAMEOVER = 1'b1;
144	if(winner_counter == 4'b1111)
145	WHO = 2'b10;
146	else
147	WHO = 2'b01;
148	end
149	
150	endcase
151	
152	end
153	
154	endmodule

TestBench

```

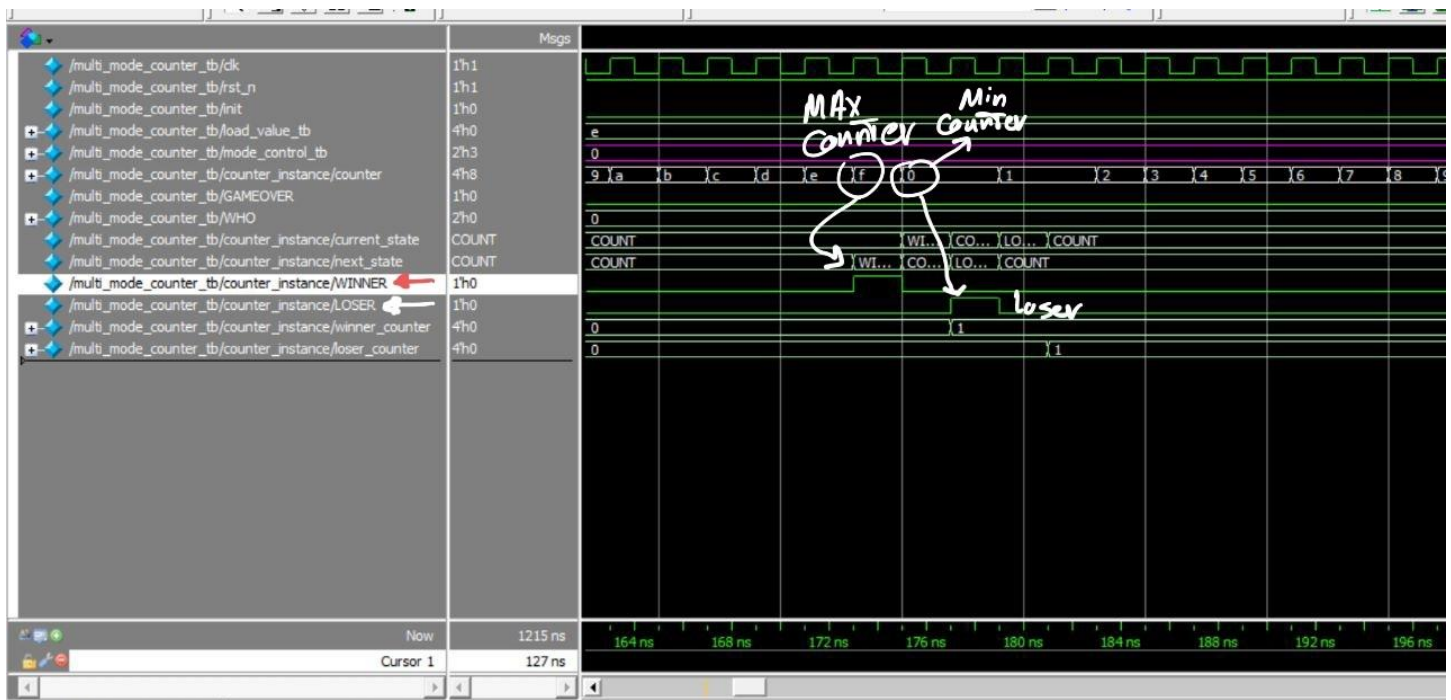
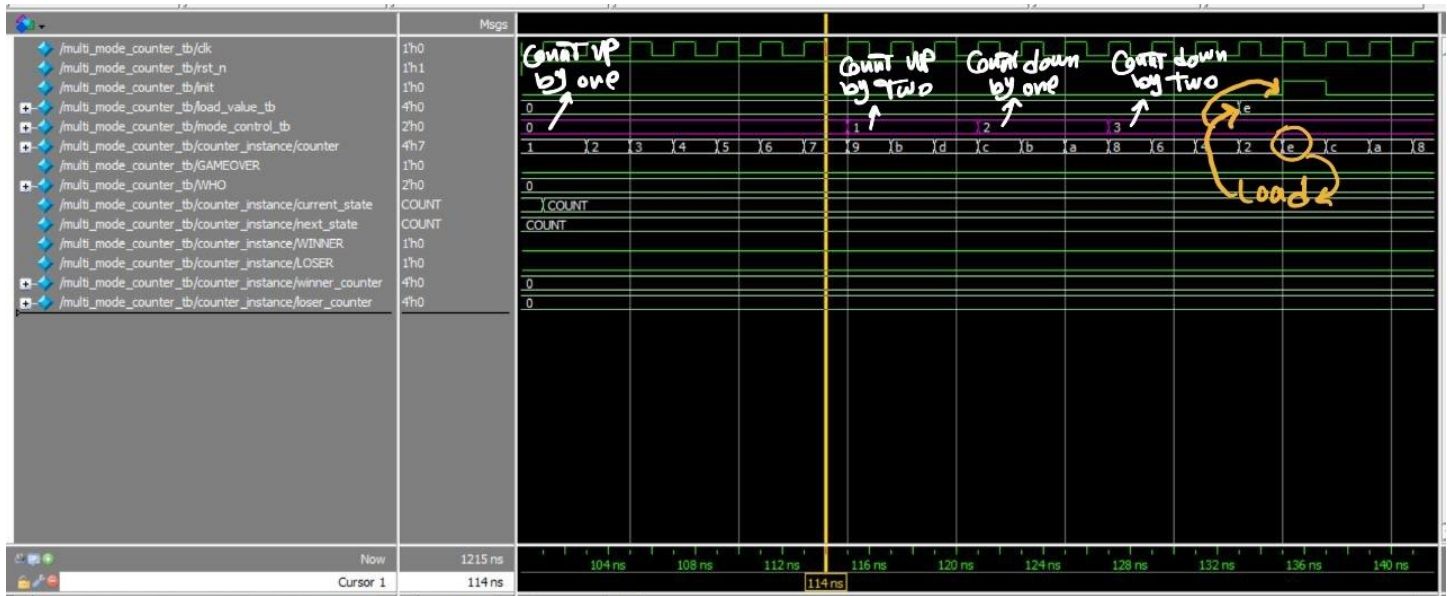
1  module multi_mode_counter_tb();
2
3      reg        clk,rst_n,init ;
4      logic [3:0] load_value_tb ;
5      logic [1:0] mode_control_tb;
6
7      wire  GAMEOVER;
8      wire  [1:0] WHO;
9
10
11  multi_mode_counter  counter_instance(.clk(clk),.rst_n(rst_n),.load_value(load_value_tb),.mode_control(mode_control_tb),
12                                     .init(init),.GAMEOVER(GAMEOVER) ,.WHO(WHO));
13
14
15  initial begin
16      clk = 0;
17      forever begin
18          #1 clk = ~clk;
19      end
20  end
21
22
23  initial begin
24      init = 0;
25      load_value_tb = 0;
26      mode_control_tb = 0;
27      rst_n = 0;
28      #100;

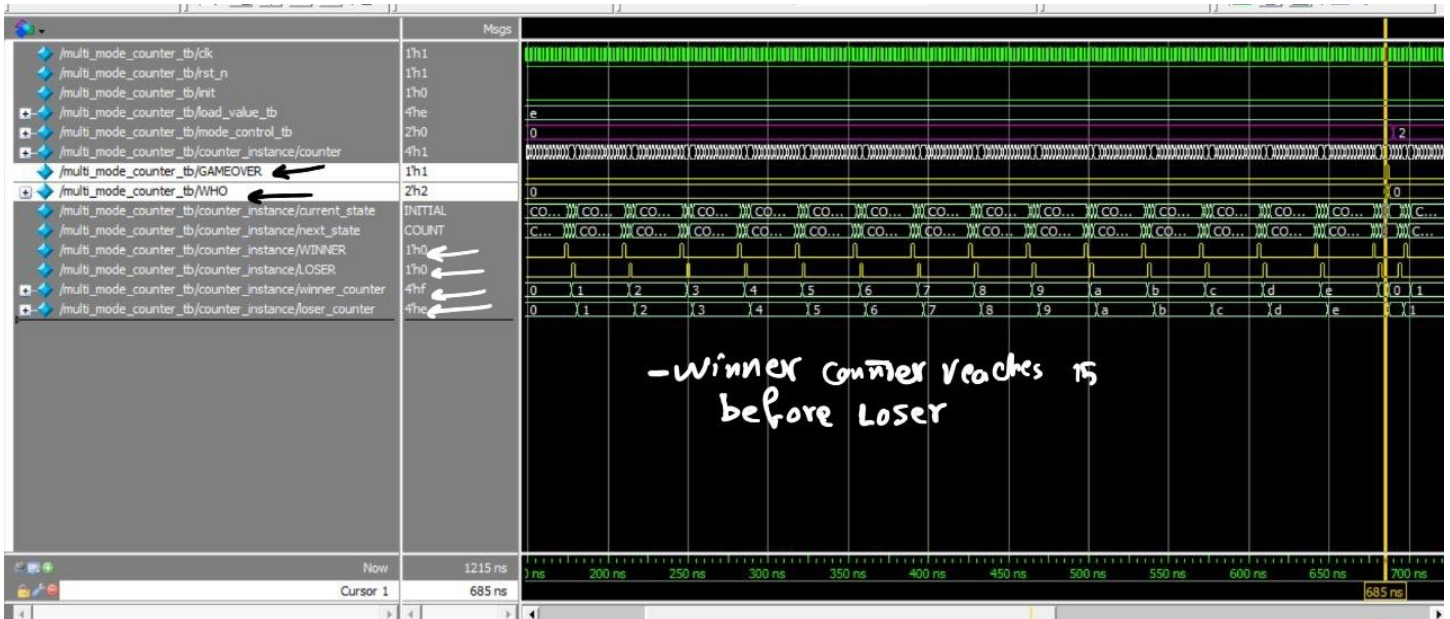
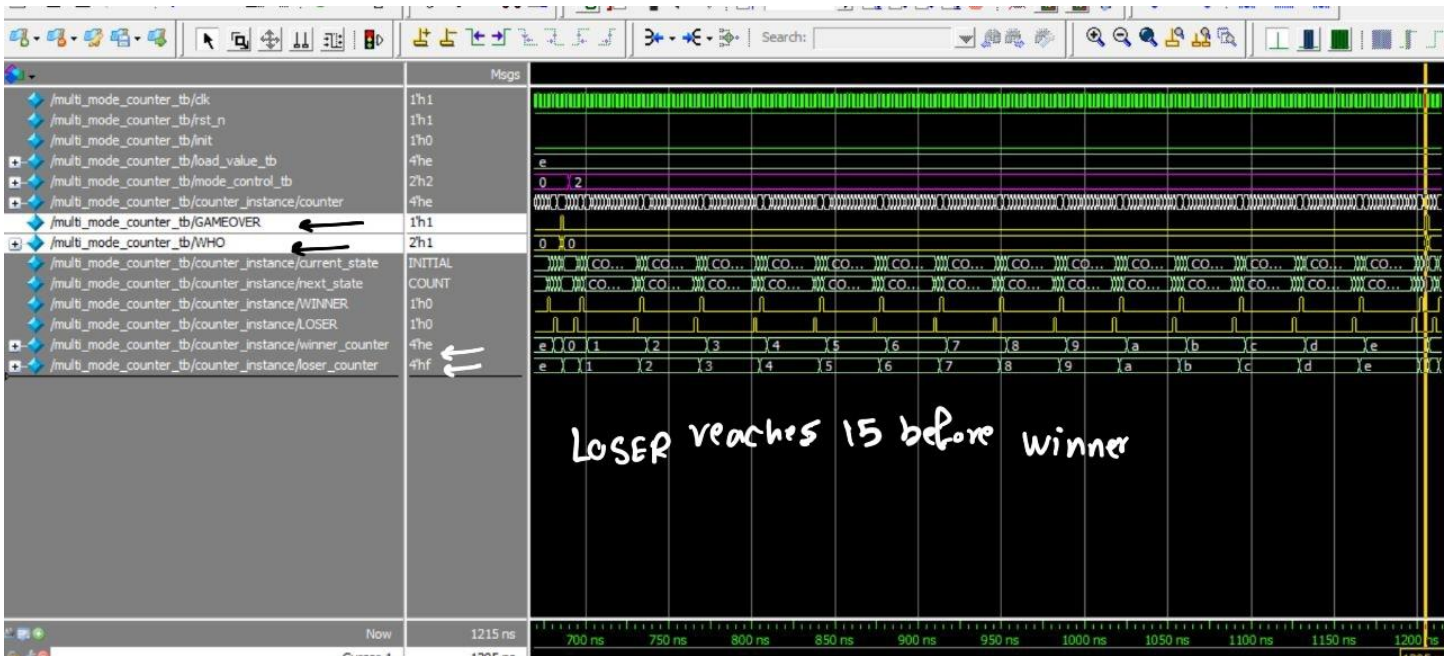
```

Ln#	
27	rst_n = 0;
28	#100;
29	rst_n = 1;
30	#5;
31	
32	mode_control_tb = 2'b0;
33	#10;
34	mode_control_tb = 2'b01;
35	#6;
36	mode_control_tb = 2'b10;
37	#6;
38	mode_control_tb = 2'b11;
39	#6;
40	load_value_tb = 4'd14;
41	#2;
42	init=1;
43	#2;
44	init = 0;
45	#2;
46	mode_control_tb = 2'b11;
47	#4;
48	rst_n = 0;
49	#2;
50	rst_n = 1;
51	mode_control_tb = 2'b0;
52	end
53	
54	initial begin

Ln#	
41	#2;
42	init=1;
43	#2;
44	init = 0;
45	#2;
46	mode_control_tb = 2'b11;
47	#4;
48	rst_n = 0;
49	#2;
50	rst_n = 1;
51	mode_control_tb = 2'b0;
52	end
53	
54	initial begin
55	#690;
56	mode_control_tb = 2;
57	end
58	
59	
60	initial begin
61	#1215;
62	\$stop;
63	end
64	
65	
66	endmodule
67	

WaveForms





GitHub Link : https://github.com/HassanKhaled11/Multi_Mode_Counter.git