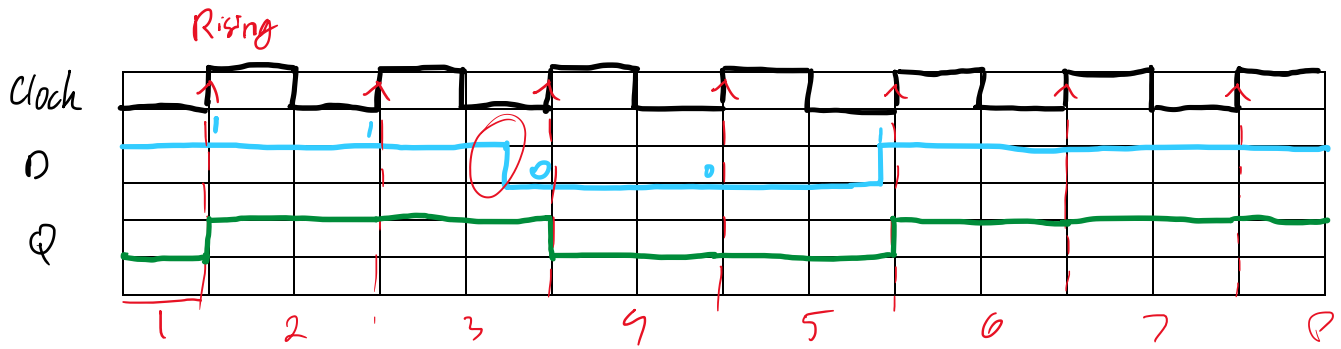


Clock : RET

FF : D-FF

5 : 0101



D	Q
0	0
1	1

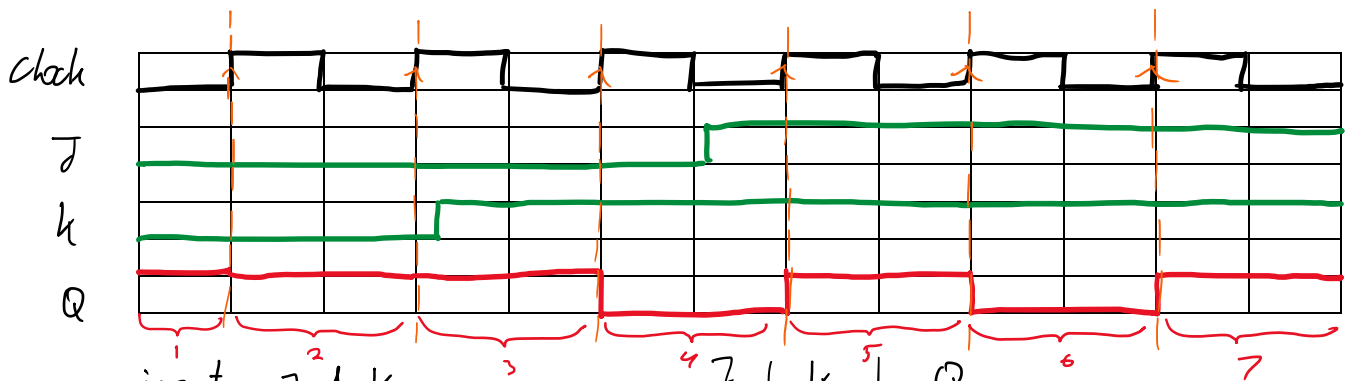
D = input  
Q = output

1000

Clock : RET

FF : JK-FF

1 → 0001



input : J & K

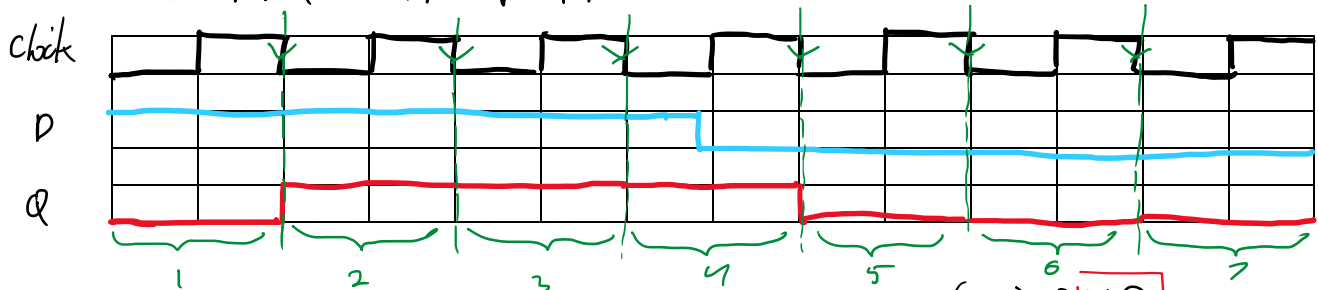
Output : Q

J	K	Q
0	0	$Q_t$
0	1	0
1	0	1
1	1	$\bar{Q}_t$

Falling

Clock : FET

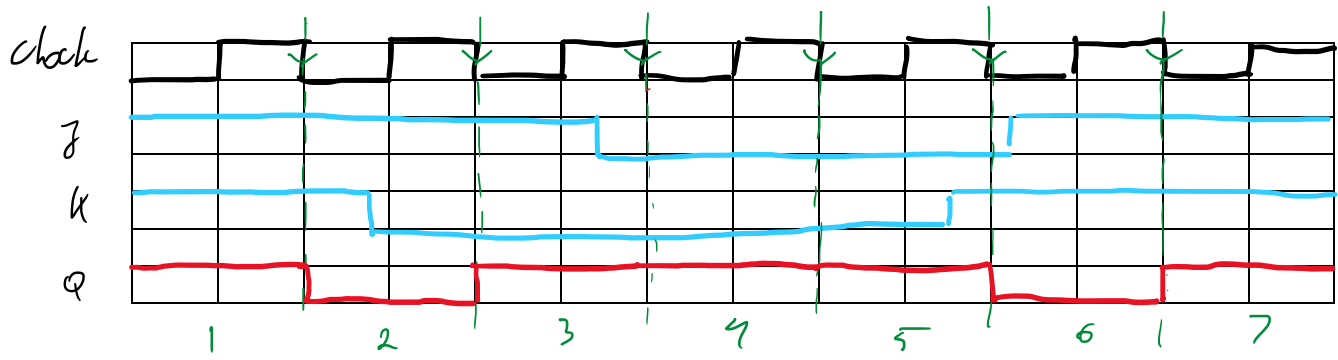
FF = D-FF



6 ⇒ 0110

clock: FET FF: JK-FF

$\bar{S} = 0101$



clock: RET FF: T-FF

