1	PLC	
1.1 PI	PLC	
1.1	1.1 PLC	
1.1	1.2 PLC	
1.2 PI	PLC	
1.2	2.1	
1.2	2.2	
1.3 C		
1.3		
1.4 Pl		
	4.1 PLC	
1.4	4.2 PLC	
2	MACTED IZ	
2	MASTER-K	
2.1	1.1 DV C	
	1.1 PLC	
	1.2 PLC 1.3 MASTER-K	
2.1	1.5 WIAGIER-K	
2.3		
2.3	3.1 ?	
2.3		
2.3		
3	MASTER-K	
3.1		
3.2		

4		46
4.1	()	46
4.1.1 LOAD	, LOAD NOT, OUT	46
4.1.2 AND,	AND NOT	48
4.1.3 OR, O	R NOT	49
4.2		50
4.2.1 END		50
4.3		51
4.3.1 NOP		51
4.3.2 KGL-	WIN	53
4.4		64
4.4.1 AND I	LOAD	64
4.4.2 OR LC	OAD	66
4.4.3 MPUS	H, MLOAD, MPOP	67
4.4.4 KGL-	WIN	71
4.5		74
4.5.1 NOT		74
4.6		75
4.6.1 MCS,N	MCSCLR	75
4.7		78
4.7.1 D		78
4.7.1 D NOT	Γ	83
4.8		84
4.8.1 SET		84
4.8.2 RST		85
4.9 ,		89
4.9.1 SET S		89
4.9.2 OUT S	S	92
4.10		97
4.10.1 ON d	elay(TON)	97
4.10.2 OFF	delay(TOFF)	100
4.10.3	(TMR)	105
4.10.4	(TMON)	107
4.10.5	(TRTG)	109
4.11		114
4.11.1 UP C	OUNTER(CTU)	114
4.11.2 DOW	VN COUNTER(CTD)	117
4 11 3 IJP D	OWN COUNTER(CTUD)	118

4.11.3 RING COUNTER(CTR)	120
5	130
5.1 ?	130
5.2 PLC	131
1:	1
2:	2
3:	4

10

4: Handy Loader

Code

1 PLC

1.1 PLC

1.1.1 PLC

PLC(Programmable Logic Controller) , , , , , LSI,

가 가가

(NEMA: National Electrical Manufactrurers Association)

"

1.1.2 PLC

PLC

FMS(Flexible Manufacturing System) PLC

,

1-1 PLC

1-1 PLC

	,
,	, ,
,	,
	, ,
	, , .
	, , .
	, ,
	,

1.2 PLC

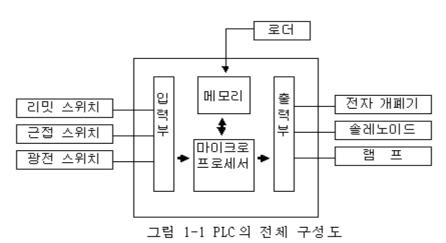
1.2.1

(1)

PLC (microprocessor) (CPU),

, PLC

1-1 PLC



(2) PLC CPU

PLC

. 2

•

(3) PLC CPU

1

IC ROM(Read Only Memory) RAM(Random Access Memory)

ROM , . ,

RAM 가 가

•

RAM (Battery back-up)

2

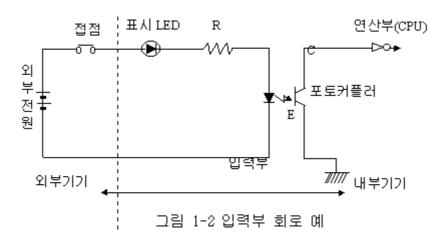
PLC , , 3 가

. 가

RAM , ROM ROM 가 가 RAM PLC PLC , PLC ROM (4) PLC · . PLC DC+5 PLC V (TTL) PLC 가 (Interface) PLC · 1 가 CPU 2 [(Photocoupler)] 3 .(LED) 4 Panel 1-2 1-2 I/O () ()

가)
CPU ...
DC24[V], AC110[V] , (A/D)
(High Speed Counter) .

1-2



)

. , SSR(Solid State Relay)

, (D/A) ,

.

1-3 .

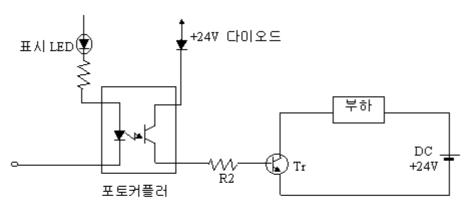


그림 1-3 트랜지스터의 출력부 회로

1-3

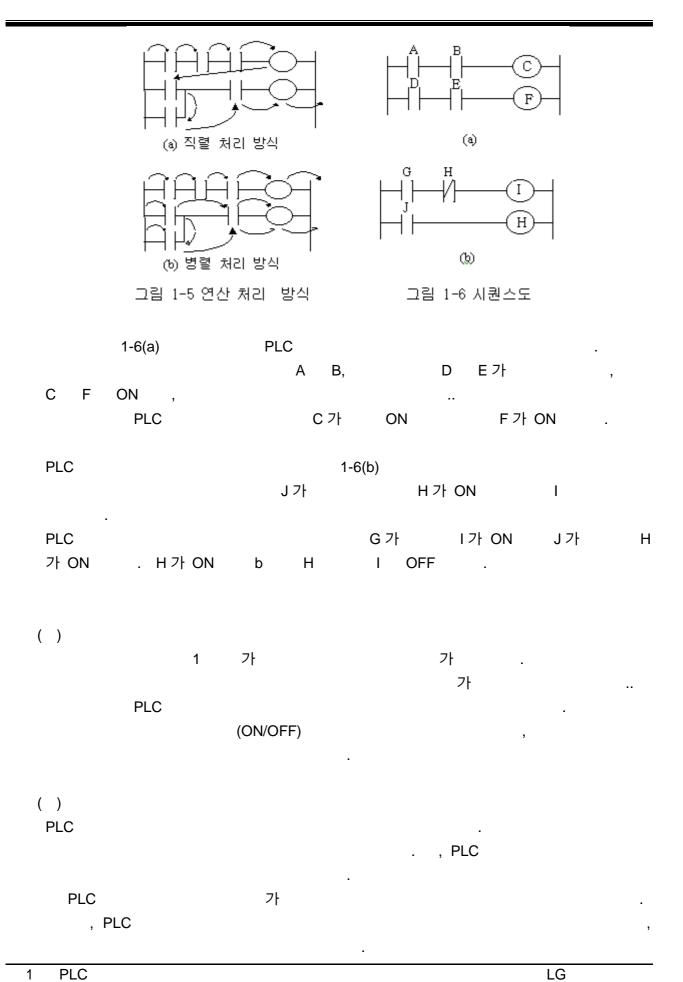
	()
(DC)	
(AC)	SSR

1-3

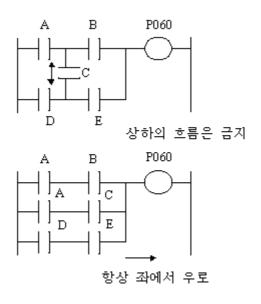
1-3

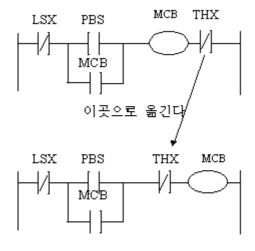
가 SSR

1.2.2 (1)				
	(Hardwired Logic)		·	
	. ,	()	가 , 가	,
	(Hardware)		가	
		(Softwired Logic)	, PLC	
(2) PLC ,	PLC LSI			
,	•	가		
PLC				
(가) PLC " PLC	33 66 33	가	1-5	
PLC	가	가	·	



PLC 1-7 .





1-7 PLC

1.3 CPU

1.3.1

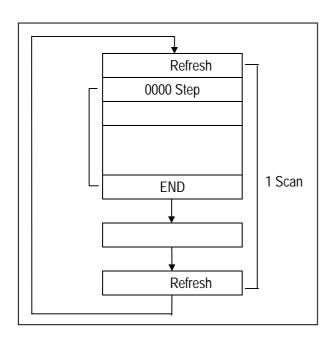
REMARK

1 Scan :

0 Step

Unit

END



Refresh

Refresh

Refresh

(1) Refresh: Unit Data Read Data (P) Memory Refresh: Data Memory (P) Data (2) Unit (IORF): (3) Refresh (4) OUT Sequence Program Data Memory (P) **END** OFF Refresh ON

Data Read

, Timer, Counter

Data Memory

Data Memory

(P) Data

1 PLC LG

Unit

1.4 PLC

PLC

PLC

1.4.1 PLC

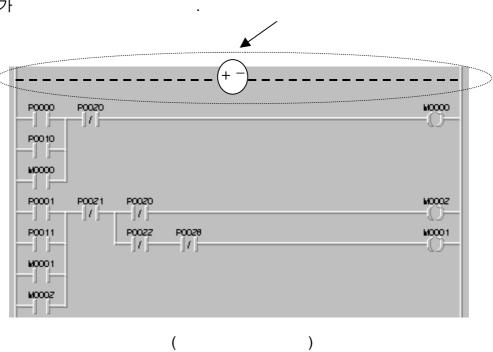
(Mnemonic), (Ladder), SFC(Sequential Function Chart) (Mnemonic), (Ladder) MASTER-K PLC 2 가 (Conversion) 가 (Mnemonic) (1)

(Handy Loader)

LOAD P000 AND NOT P001 OR P002 OUT P020

(2) (Ladder):

가



1.4.2 PLC

(1) PLC ()

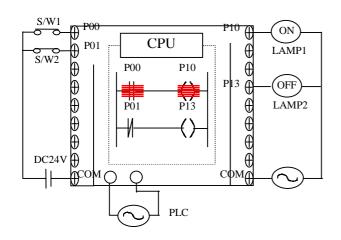
		PLC	
А	100	⊢	(Open) N.O. (Normally Open) PLC: , ON/OFF
В	-ala- -o_o-	<u> </u>	(Closed) N.C. (Normally Closed) PLC: , ON/OFF
С			a, b PLC
		→ →	
		—: Н	PLC

2)		
(Point): 8, 6	PLC	8 ,
6		
PLC		
(Step): PLC	А, В,	1
	가	
CPU).	: 30k step, :sec/Step
(Scan Time) :	1	
가	가 .	
WDT(Watch Dog Timer) :	CPU	
(WDT)		
200ms		
(Parameter) :	PLC	,

3) PLC PLC PLC (1) Loader (KGL-WIN, PLC CPU Handy Loader) COM DC24V 가 (LAMP) COM (DC DC .) **PLC PLC** OFF S/W1 OFF a P00 CPU LAMP1 S/W1 OFF P10 P00 (Disconnect) P10 OFF (ON LAMP2 S/W2 가 OFF P01 b S/W2 **OFF** ON (Connect) P13 DC24V

: ()

PLC



 S/W1
 ON
 a
 P00

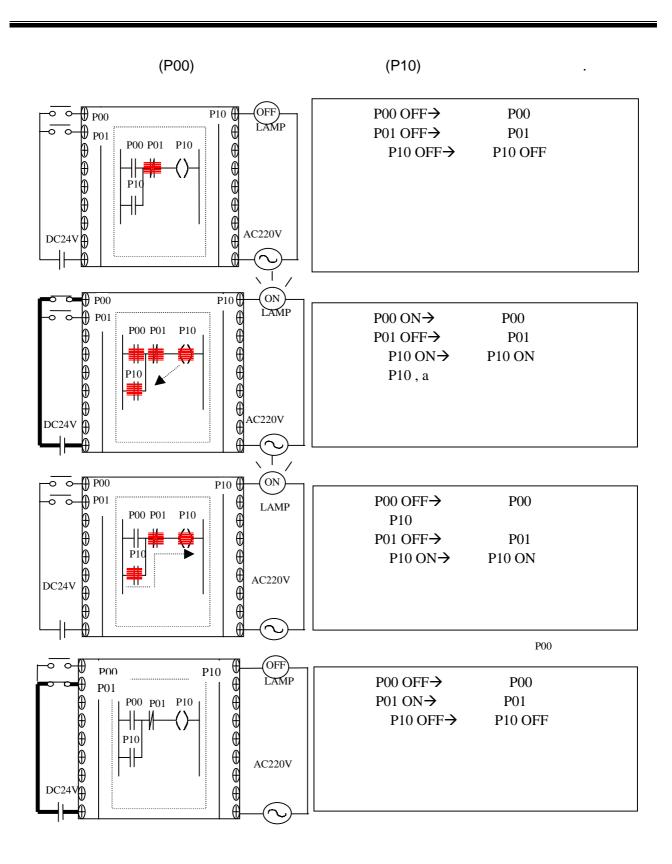
 S/W1
 ON
 ,

 (Connect)
 P10
 ON
 .

 S/W2
 7\tau ON
 b
 P01

 S/W2
 ON
 ,
 ,

 (Disconnect)
 P13
 OFF
 .



2 MASTER-K

2.1

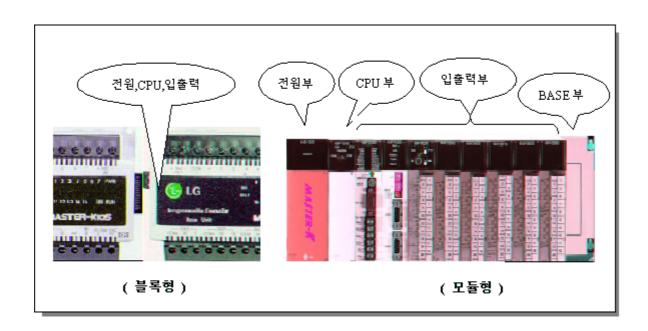
2.1.1 PLC

PLC (BASE), (SMPS), CPU , (DI,DO, ,)

TYPE

K10S1, K10S, K30S, K60S, K80S 가

TYPE

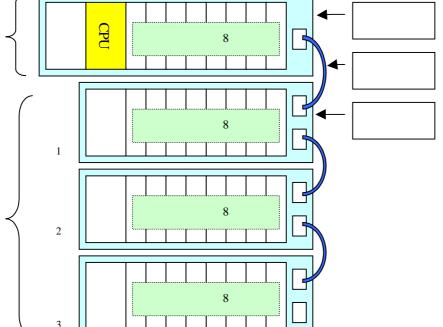


2 MASTER-K LG

2.1.2 PLC

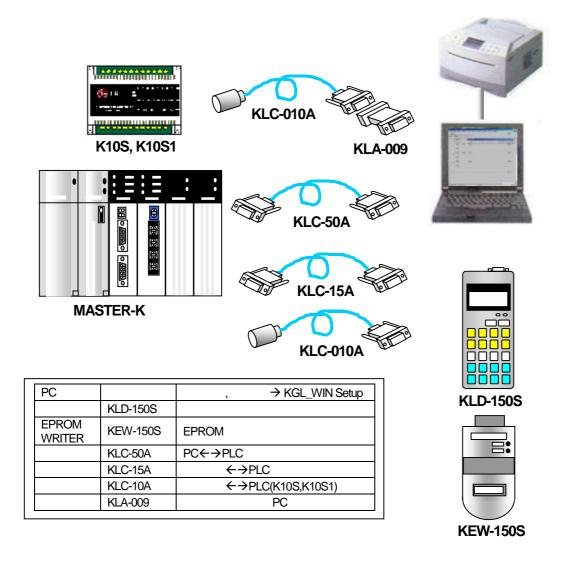
1024 . 32 (K300S, 1) 1024 1/4 . 1024 가 , 가 가 K1000S 256 (8 X 32) 8 32 가 , 가 . 가 32 8

CPU 가 , CPU, Connector 1 CPU Connector 2



2 MASTER-K LG

2.1.3 MASTER-K

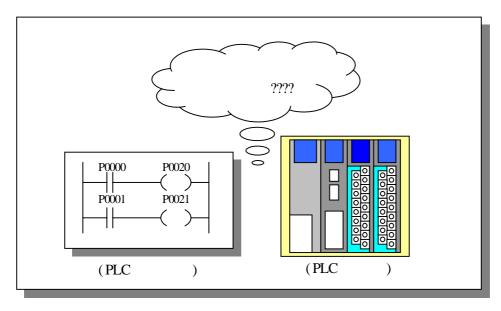


16

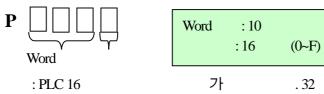
2.2

PLC , PLC PLC

, PLC 가 가



PLC (P) (Device) 'P' .



2 7}
P000 P002
P001 P003 P004 P005 P006 P008 P010 P012
P0WER CPU
POWER CPU
P000 P002
P001 P003 P004 P005 P007 P009 P011 P013

TOWLK	CI U	32	32	16				32	32	
		1	2	3	4	(5)	6	7	8	J

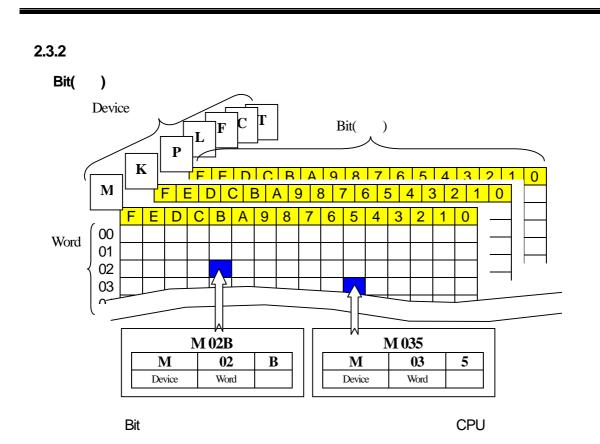
(HSC), 16,32,64 . P11F: P11 F (16) 2.3 2.3.1 ? PLC P ON/OFF, Data PLC (Bit) 1) 가 $\mathbf{M}: PLC$ (1) 가 가 . a, b 가 M (2)): PLC **K** (Data 가 Data (3) F: PLC PLC (4) Data Register **D**: 16Bit(1Word)) 32Bit(2Word) 가 가 (5) **T**: (6) \mathbf{C} :

2 MASTER-K LG

Register: #D

L,

(7)



:FFFF)

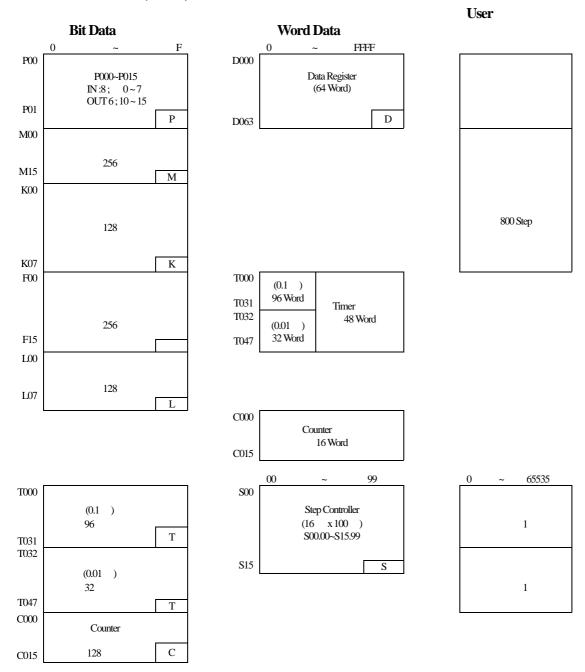
	F	Е	D	С	В	Α	9	8	7	6	5	4	3	2	1	0
M01	0	0	0	1	0	0	1	0	0	0	1	1	0	1	0	0
HEX		•	1			2			3			4				

BLD,BAND,BOR D Bit 가 .(K80S, K200S, 300S, 1000S)

2 MASTER-K LG

2.3.3

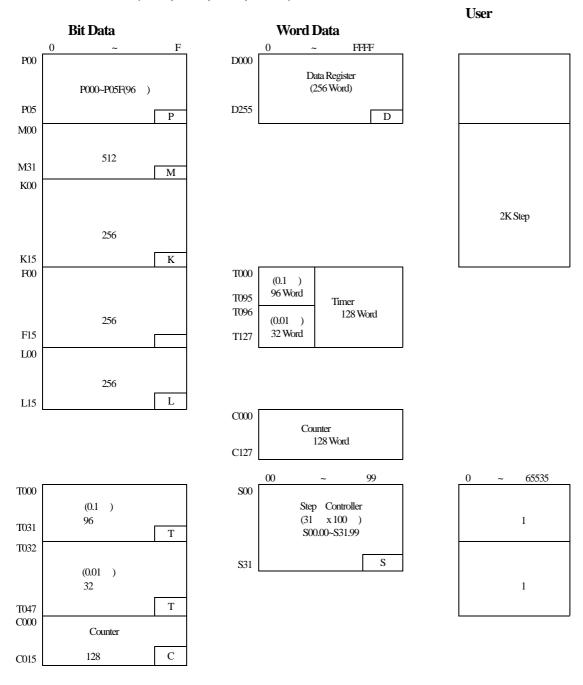
MASTER-KS (K10S1)



(가 가)

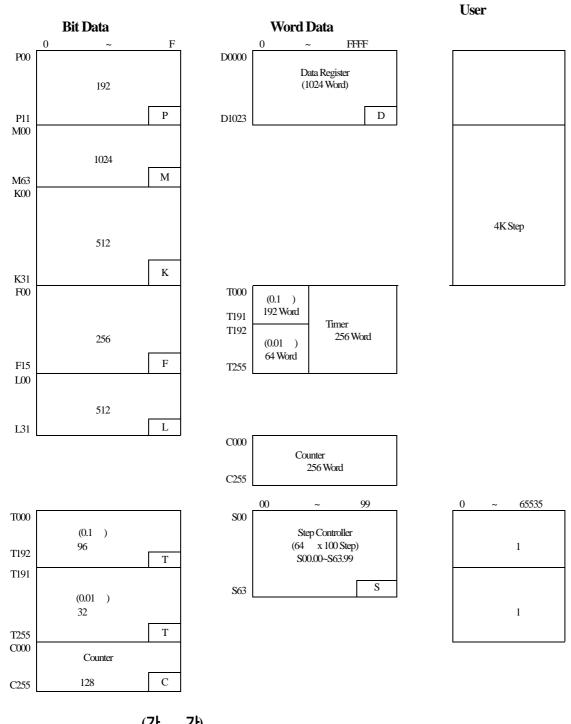
K	L	T C		D	S	
K000~K07F	L000~L07F	0.1 :T024~T031 0.01 :T044~T047	C012~C015	D048~D063	S12~S15	

MASTER-K S (K10S, K30S, K60S, K100S)



	17)	7 f)			
K	L	T	С	D	S
K000~K15F	L000~L15F	0.1 :T072~T095 0.01 :T120~T127	C096~C127	D192~D255	S24~S31

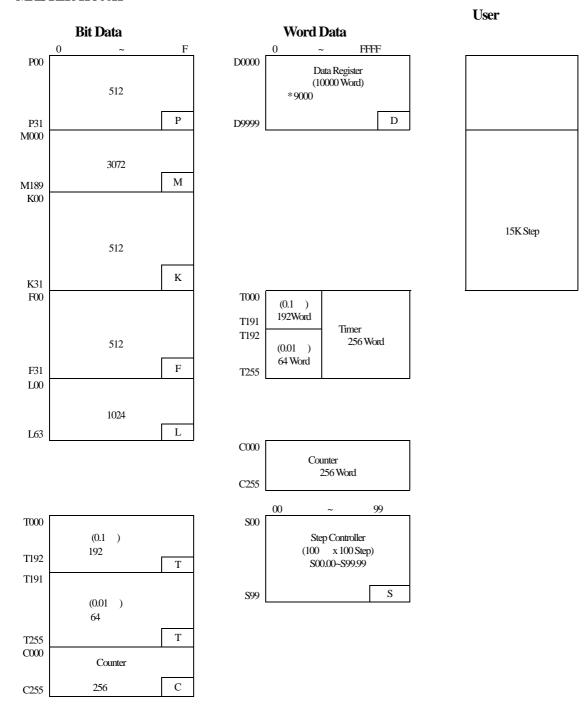
MASTER-K 200H



(71 -	/ f)	
L	T	С

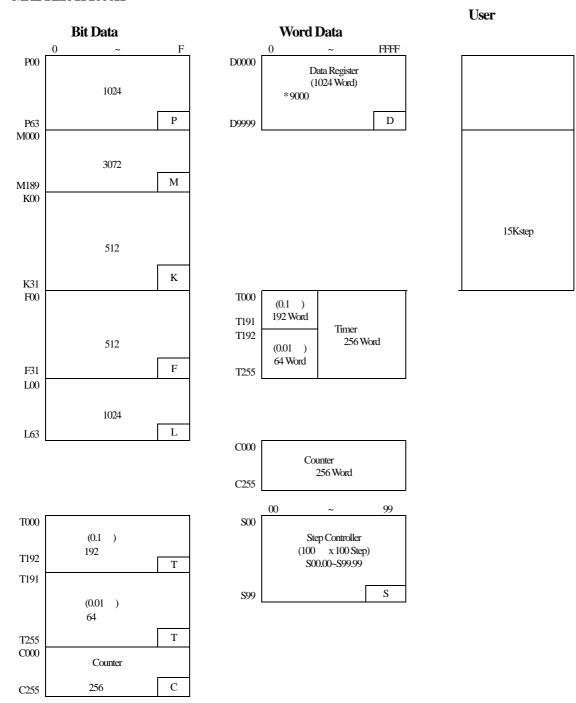
	` •	•/			
K	L	T	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D0768~D1023	S48~S63

MASTER-K 500H



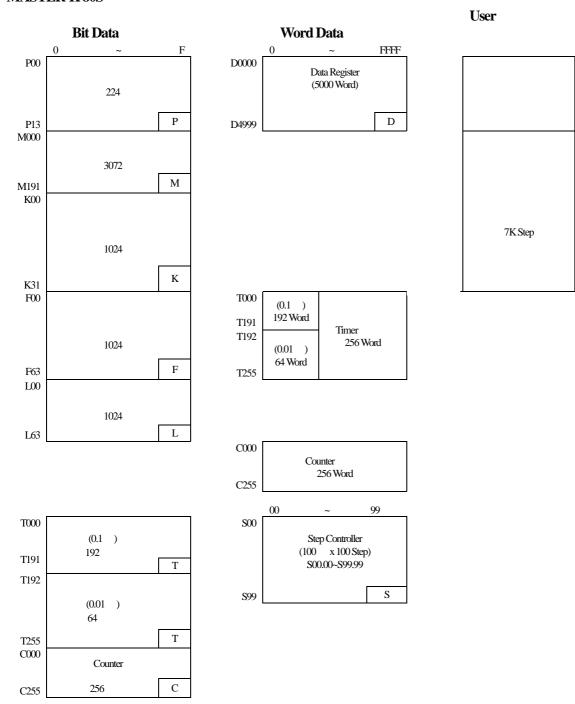
K	M,L	T	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D6000~D8999	S80~S99

MASTER-K 1000H



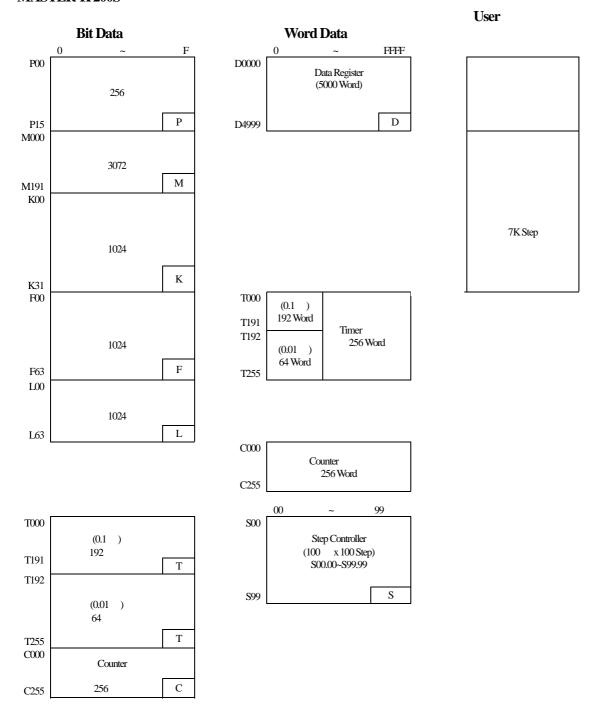
K	M,L	Т	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D6000~D8999	S80~S99

MASTER-K 80S



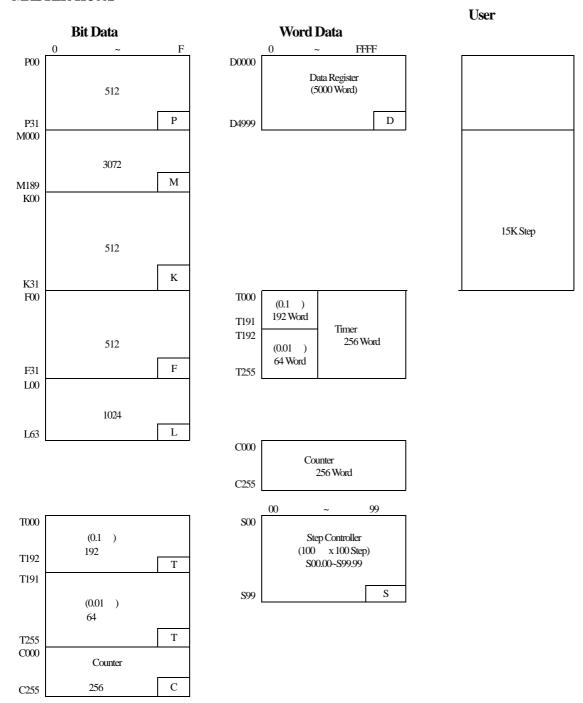
K	M,L	T	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D3500~D4500	S80~S99

MASTER-K 200S



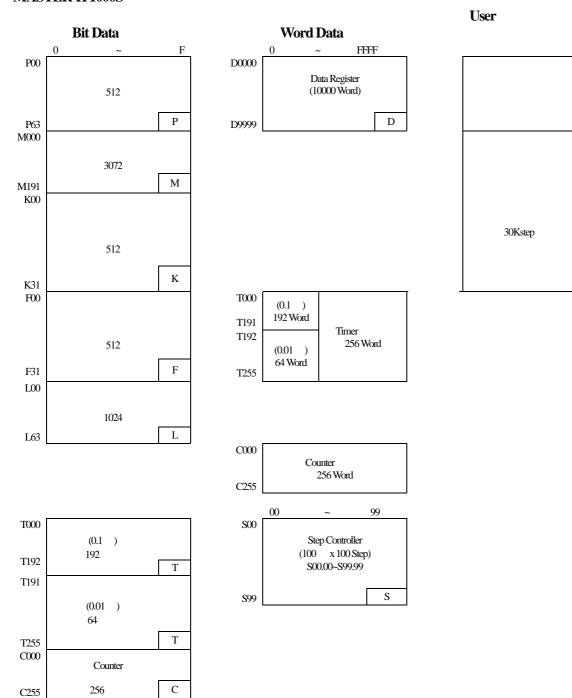
K	M,L	T	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D3500~D4500	S80~S99

MASTER-K 300S



K	M,L	Т	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D3500~D4500	S80~S99

MASTER-K 1000S



K	M,L	T	С	D	S
K000~K31F	가	0.1 :T144~T191 0.01 :T240~T255	C192~C255	D6000~D8999	S80~S99

3 MASTER-K

3.1

	Function No.		
LOAD	-		а
LOAD NOT	-	 	b
AND	-		а
AND NOT	-		b
OR	-		а
OR NOT	-	L	b

	Function No.		
AND LOAD	-	A B	А, В
OR LOAD	-	T A I B I B I B I B I B I B I B I B I B I	А, В
MPUSH	005	MPUSH — ()—	Push
MLOAD	006	MLOAD — ()—	Load
MPOP	007	MPOP — ()—	Pop

	Function No.		
NOT	-		NOT

	Function No.		
MCS	010	MCS n	Set (n : 0 ~ 7)
MCSCLR	011	MCSCLR n	Clear (n : 0 ~ 7)

	Function No.		
D	017		1 Pulse
D NOT	018	D NOT D	1 Pulse
SET	-	SET D]	On (Set)
RST	-	RST D	Off (Reset)
OUT	-	<u> </u>	

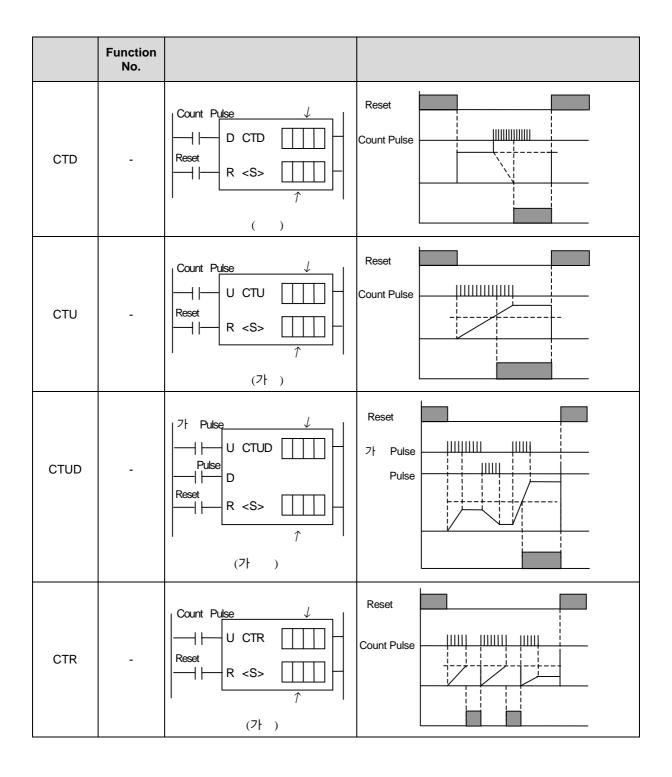
1

	Function NO.				
SET S	-	SET SXX.XX	()	
OUT S	•	(Sxx.xx)	()	

	Function No.		
END	001	END	 Program

	Function No.	
NOP	000	(No Operation),

	Function No.		
TON		H-[TON]	On Delay (7)) t=
TOFF	-	HH_TOFF	Off Delay
TMR	•	H-TMR TTMR	$t = \frac{(7 + 1)}{(7 + 1)}$
TMON	1	H-TMON	Monostable t=
TRTG	-	HH_TRTG	Retriggerable ()



3.2

	Function No.		
MOV	080	MOV S D	
MOVP	081	MOVP S D	Move
DMOV	082	DMOV S D	s — D
DMOVP	083	— DMOVP S D	
CMOV	084	CMOV S D	Complement Move
CMOVP	085	BCMOVP S D	S 1 0 1 0 ··· 1 0 1
DCMOV	086	DCMOV S D	$ \downarrow \\ \bigcirc \boxed{0} \boxed{1} \boxed{0} \boxed{1} \cdots \boxed{0} \boxed{1} \boxed{0} $
DCMOVP	087	DCMOVP S D	
GMOV GMOVP	090 091	GMOVP S D Z	Group Move S
FMOV FMOVP	092 093	FMOVP S D Z	File Move S Z Z
BMOV BMOVP	100	BMOV S D CW BMOVP S D CW	Move S → D

	Function No.		
BCD	060	BCD S D	
BCDP	061	BCDP S D	BIN BCD
DBCD	062	DBCD S D	s
DBCDP	063	DBCDP S D	BCD
BIN	064	BIN S	
BINP	065	BINP S D	BCD BIN S (D)
DBIN	066	DBIN S D	S BIN
DBINP	067	DBINP S D	

	Function No.			
CMP	050	CMP S ₁	S ₂	
CMPP	051	—CMPP S1	S2	S1 S2
DCMP	052	DCMP S ₁	S ₂	()
DCMPP	053	DCMPP S ₁	S ₂	
TCMP	054	TCMP S ₁	S ₂	
TCMPP	055	TCMPP S ₁	S ₂	Table Compare
DTCMP	056	DTCMP S1	S2	
DTCMPP	057	DTCMPP S ₁	S ₂	
LOAD= LOADD=	028 029	= S1	S ₂	
LOAD> LOADD>	038 039	> S ₁	S ₂	
LOAD< LOADD<	048 049	< S1	S ₂	S1 S2 Result Bit (BR)
LOAD>= LOADD>=	058 059	>= S1	S ₂	(Signed)
LOAD<= LOADD<=	068 069	<= S1	S ₂	MASTER-K 80S
LOAD<> LOADD<>	078 079	<> S1	S ₂	W. OTENTOOO

	Function No.						
AND= ANDD=	094 095	=	S1	S ₂			
AND> ANDD>	096 097	>	S ₁	S ₂			
AND< ANDD<	098 099	<	S ₁	S2	S ₁	S ₂	BR AND
AND>= ANDD>=	106 107	>=	S ₁	S ₂		Result Bit(BR) (Signed)	
AND<= ANDD<=	108 109	<=	S ₁	S ₂			
AND<> ANDD<>	118 119	<>	S ₁	S ₂			
OR= ORD=	188 189	=	S1	S ₂			
OR> ORD>	196 197	<u> </u>	S ₁	S ₂			
OR< ORD<	198 199	<	S ₁	S_2	S ₁	S ₂ Result Bit(BR)	BR OR
OR>= ORD>=	216 217	└ >=	S ₁	S ₂		(Signed)	
OR<= ORD<=	218 219	<=	S ₁	S ₂			
OR<> ORD< >	228 229	<>	S1	S ₂			

	Function No.			
INC	020	— INC		
INCP	021	—INCP		Increment \bigcirc + 1 \rightarrow \bigcirc
DINC	022	— DINC		
DINCP	023	— DINCP		
DEC	024	— DEC	①]—	
DECP	025	— DECP	①]	Decrement \bigcirc $-1 \rightarrow \bigcirc$
DDEC	026	— DDEC	①]—	
DDECP	027	— DDECP		

35

	Function No.			
ROL	030	— ROL	$\bigcirc] \longrightarrow$	
ROLP	031	—ROLP		CY TO
DROL	032	— DROL		
DROLP	033	— DROLP		
ROR	034	— ROR		
RORP	035	— RORP		©CY
DROR	036	— DROR		
DRORP	037	— DRORP		
RCL	040	— RCL	$\bigcirc \bigcirc \bigcirc \bigcirc$	Carry Flag
RCLP	041	—RCLP	$\bigcirc] \longrightarrow$	CY ← □
DRCL	042	— DRCL	$\bigcirc] \longrightarrow$	
DRCLP	043	— DRCLP		
RCR	044	— RCR		Carry Flag
RCRP	045	— RCRP		© CY
DRCR	046	— DRCR		
DRCRP	047	— DRCRP		

	Function No.		
BSFT BSFTP	074 075	BSFT S E BSFTP S E	Shift
WSFT WSFTP	070 071	WSFT S E	Shift
SR	237	SR D N	Shift

	Function No.		
XCHG XCHGP DXCHG DXCHGP	102 103 104 105	XCHG D ₁ D ₂	D₁ ◆ D₂

BIN

	Function No.		
ADD	110	ADD S1 S2	
ADDP	111	ADDP S ₁ S ₂ D	Binary Add
DADD	112	DADD S ₁ S ₂ D	S ₁ + S ₂
DADDP	113	— DADDP S ₁ S ₂ D	
SUB	114	SUB S1 S2 D	
SUBP	115	SUBP S ₁ S ₂	Binary Subtract
DSUB	116	DSUB S ₁ S ₂	S1 - S2
DSUBP	117	DSUBP S ₁ S ₂ D	
MUL	120	MUL S ₁ S ₂ D	Binary Multiply
MULP	121	MULP S ₁ S ₂	Birtary Mulupry
DMUL	122	— DMUL S₁ S₂ D]—	S1*S2
DMULP	123	DMULP S ₁ S ₂ D	① +1()
DIV	124	DIV S1 S2	
DIVP	125	DIVP S1 S2	Binary Divide
DDIV	126	DDIV S1 S2	$S_1 \div S_2 \longrightarrow \widehat{\mathbb{D}}$ ()
DDIVP	127	DDIVP S1 S2 D	① +1()

	Function No.	
MULS	072	$- \boxed{\text{MULS} S_1 S_2 \boxed{\hspace{1cm}} \qquad \qquad } \qquad \boxed{\hspace{1cm}} \qquad \qquad \qquad \boxed{\hspace{1cm}} \qquad \qquad \qquad \qquad \qquad \qquad \boxed{\hspace{1cm}} \qquad $
MULSP	073	$- \boxed{MULSP S1 S2 } \boxed$
DMULS	076	DMULS S ₁ S ₂ D (signed)
DMULSP	077	— DMULSP S ₁ S ₂ D
DIVS	088	$- \boxed{\text{DIVS} \text{S1} \text{S2} \boxed{\text{D}} } \qquad \boxed{\text{S1} * \text{S2}} - \boxed{\text{D}} \qquad (\)$
DIVSP	089	$- \boxed{DIVSP S_1 S_2 \textcircled{D} } + 1 ()$
DDIVS	128	DDIVS S ₁ S ₂ D (signed)
DDIVSP	129	DDIVSP S ₁ S ₂ D

BCD

	Function No.		
ADDB	130	ADDB S1 S2 D	
ADDBP	131	ADDBP S ₁ S ₂	BCD Add
DADDB	132	DADDB S1 S2 D	S₁ + S₂
DADDBP	133	— DADDBP S1 S2 D	
SUBB	134	SUBB S1 S2 D	
SUBBP	135	SUBBP S1 S2 D	BCD Subtract
DSUBB	136	DSUBB S ₁ S ₂	S1 - S2
DSUBBP	137	DSUBBP S ₁ S ₂ D	
MULB	140	MULB S1 S2 D	BCD Multiply
MULBP	141	MULBP S ₁ S ₂	.
DMULB	142	— DMULB S₁ S₂ D]—	S ₁ * S ₂
DMULBP	143	— DMULBP S₁ S₂ D →	
DIVB	144	DIVB S1 S2	
DIVBP	145	DIVBP S ₁ S ₂ D	BCD Divide
DDIVB	146	DDIVB S1 S2 D	S₁÷ S₂
DDIVBP	147	DDIVBP S ₁ S ₂ D	① +1()

	Function No.		
WAND	150	WAND S1 S2	
WANDP	151	WANDP S ₁ S ₂	Word AND
DWAND	152	DWAND S ₁ S ₂	S ₁ AND S ₂
DWANDP	153	— DWANDP S ₁ S ₂ D	

	Function No.		
WOR	154	WOR S1 S2 D	
WORP	155	WORP S ₁ S ₂	Word OR
DWOR	156	DWOR S ₁ S ₂	S₁ OR S₂
DWORP	157	DWORP S ₁ S ₂ D	
WXOR	160	WXOR S1 S2	
WXORP	161	WXORP S ₁ S ₂	Word Exclusive OR
DWXOR	162	DWXOR S ₁ S ₂	S₁ XOR S₂ → 🕩 🛈
DWXORP	163	DWXORPS1 S2 D	
WXNR	164	WXNR S1 S2	
WXNRP	165	WXNRP S1 S2 D	Word Exclusive NOR
DWXNR	166	DWXNR S ₁ S ₂ D	S ₁ XNR S ₂ \longrightarrow \bigcirc
DWXNRP	167	DWXNRPS ₁ S ₂ D	

	Function No.		
SEG	174	SEG S D CW	
SEGP	175	SEGP S D CW	7 Segment
ASC	190	ASC S D CW	ASCII
ASCP	191	— ASCP S D CW —	

	Function No.		
FALS	204	FALS n	()
DUTY	205	DUTY © n1 n2	n ₁ On, n ₂ Off
WDT WDTP	202 203	WDT	Watch Dog Timer Clear
OUTOFF	208	OUTOFF	Off
STOP	008	STOP	PLC

	Function No.		
BSUM	170	BSUM S D	
BSUMP	171	BSUMP S D	Bit Summary
DBSUM	172	DBSUM S D	Word Data "1" Count
DBSUMP	173	DBSUMP S D	
ENCO	176	ENCO S D Z	Encode
ENCOP	177	ENCOP S D Z	2.100000
DECO	178	DECO S D Z	
DECOP	179	— DECOP S D Z	Decode
FILR	180	— FILR s © z]	
FILRP	181	FILRP S D Z	File Table Read
DFILR	182	— DFILR S D Z	
DFILRP	183	— DFILRP S D Z	
FILW	184	FILW s D z	
FILWP	185	FILWP S D Z	File Table Write
DFILW	186	DFILW S D Z	
DFILWP	187	— DFILWP S D Z	

	Function No.		
DIS	194	— DIS S D Z	Distribution ()
DISP	195	DISP S D Z H • Ni	ibble (4)
UNI	192	UNI S D Z	Union ()
UNIP	193	UNIP S D Z - Ni	ibble (4)
IORF	200	IORF S ₁ S ₂	Refresh
IORFP	201	— IORFP S ₁ S ₂	1.0110011

	Function No.			
JMP	012	—JMP	n]	Jump
JME	013	— JME	n	Jump End
CALL	014	— CALL	n <u> </u>	Subroutine Call
CALLP	015	— CALLP	n]	
SBRT	016	— SBRT	n	Subroutine
RET	004	RET	<u> </u>	Return

Loop

	Function No.		
FOR NEXT	206 207	FOR n NEXT	
BREAK	220	BREAK	For ~ Next Loop

	Function No.		
STC	002	STC	Set
CLC	003	CLC]	

Reset

	Function No.		
CLE	009	CLE	F115

	Function No.				
GET GETP	230 231	GET n N D n	(CPU ← ↑	RAM RAM)	Read
PUT PUTP	234 235	PUTP n N S n	(CPU ← ↑	RAM RAM)	Write

	Function No.		
READ	244	READ ts D S n X	FUEA Read
WRITE	245	WRITE t s S D n X	FUEA Write
RGET	232	RGET ts D S n X	FUEA Remote Read
RPUT	233	RPUT ts S D n X	FUEA Remote Write
CONN (MINI MAP)	246	CONN t s X	[MiniMap]
STATUS	247	STATUS t s DX	

	Function No.				
EI	238	— EI	n		가()
DI	239	— DI	n	$\exists \dashv$	()
EI	221	<u> </u>	EI		가 ()
DI	222		DI		()
TDINT n	226	— TDINT	n		
INT n	227	— INT	n	$\exists \dashv$	
IRET	225	IR E	ΞT		(Routine)

	Function No.				
NEG	240	NEG	$\bigcirc] \longrightarrow$		
NEGP	241	NEGP	$\bigcirc] \longrightarrow$	① 2	
DNEG	242	DNEG	$\bigcirc] \longrightarrow$		
DNEGP	243	DNEGP			

(D)

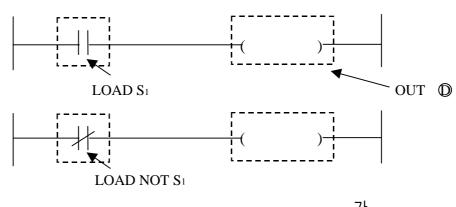
	Function No.		
BLD	248	B D N	Device D N
BLDN	249	BN D N	Device D N
BAND	250	—	Device D N AND .
BANDN	251		Device D N AND .
BOR	252	B D N	Device D N OR .
BORN	253	BN D N	Device D N OR .
BOUT	236	BOUT D N	Device D N
BSET	223	BSET D N	Device D N Set .
BRST	224	BRST D N	Device D N Reset .

4

4.1 ()

4.1.1 LOAD, LOAD NOT, OUT

							가					
		М	Р	K	L	F	Т	С	S	D	#D	
LOAD LOAD NOT	S1	0	0	0	0	0	0	0	0			1
OUT	0	0	0	0	O*				0			1



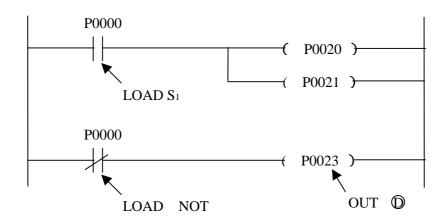
* 가

- LOAD S₁
 - 1)
- (S₁) On/Off
- LOAD NOT S₁
 - 1)
- b
- (S_1) On/Off
- OUT **①**
 - 1)
- OUT
- OUT

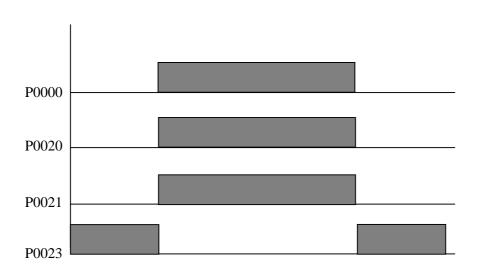
가

P000 가 On P023 Off

•

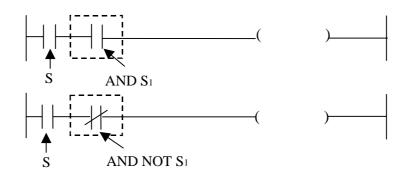


•

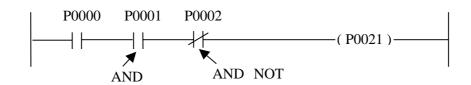


4.1.2 AND, AND NOT

가													
		М	Р	K	L	F	Т	С	S	D	#D		
AND AND NOT	S1	0	0	0	0	0	0	0	0				1

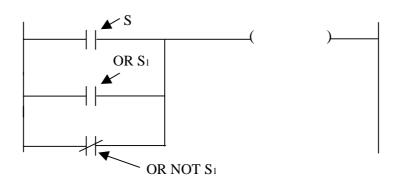


- AND S₁
 - 1)
- a
- , S S1 AND (S_1) a
- AND NOT S₁
 - 1)
- b (S_1) b , S S1 AND
- P0000 P0021 AND P0002 AND NOT P0021



4.1.3 OR, OR NOT

	가												
		М	Р	K	L	F	Т	С	S	D	#D		
OR OR NOT	S1	0	0	0	0	0	0	0	0				1



- OR S₁
 - 1)
- a
 - (S_1) a

, S S₁ OR

- OR NOT S1
 - 1)
 - b
 - (S₁) b

, S S_1 OR

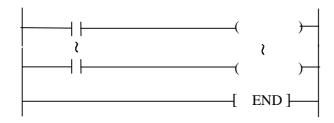
P0000, P0001 On P0021

P0000
P0001
OR S1

4.2

4.2.1 END





■ END

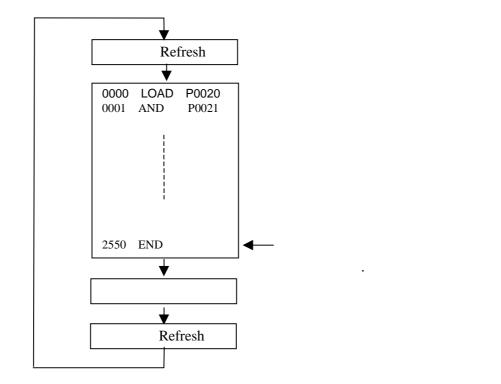
1)

•

1

• END 0000 7 .

• END . (Error)



4.3

4.3.1 NOP

	가												
		М	Р	K	L	F	Т	С	S	D	#D		
NOP													1

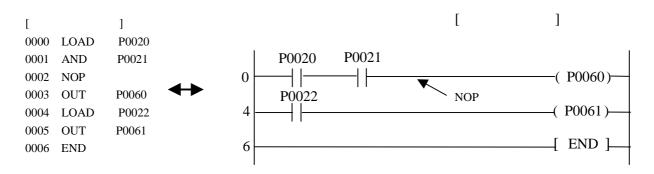
REMARK

• NOP

(Scan time)

• NOP

NOP

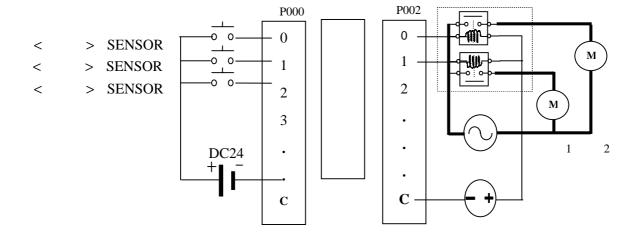


[LOAD, AND, OR, OUT]

1.

2 가

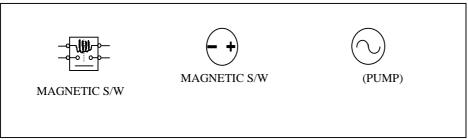
2. ()



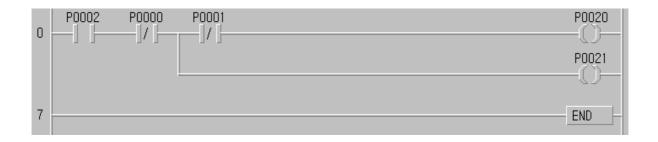
PLC (MASTER-K,2A/ ,5A/1COM)

MAGNETIC S/W

•



3. PLC

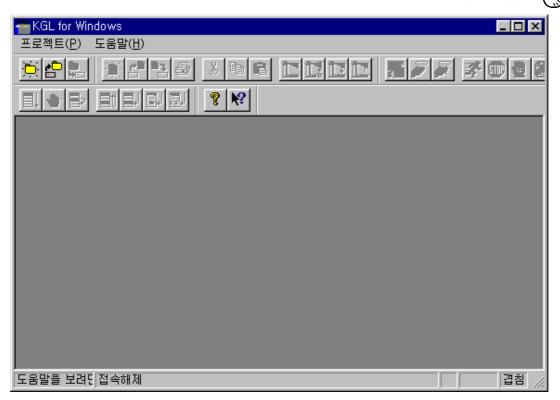


4. 3. 2 KGLWIN

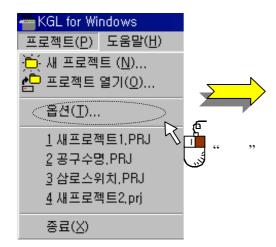
1)

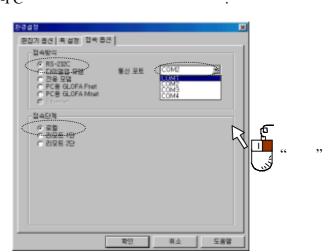
(1) KGL-WIN





(2) - - PLC-PC





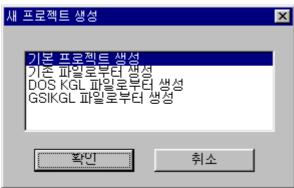
(3) KGL-WIN

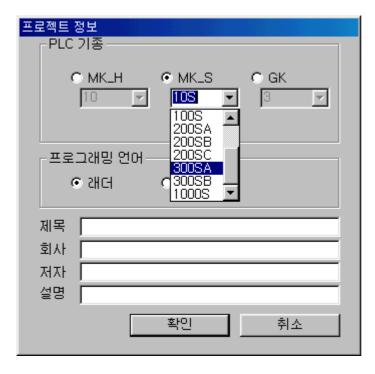


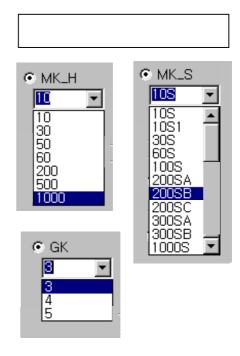
(3)

PLC

, , ,

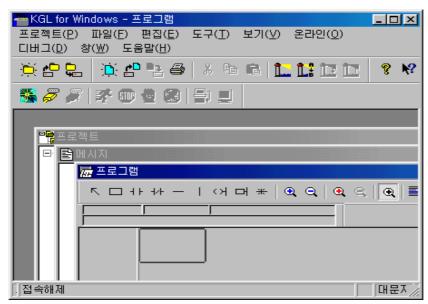




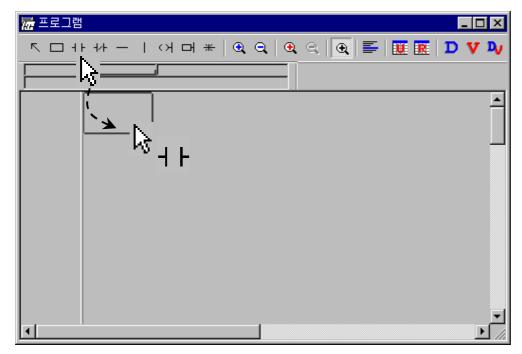


2)





(2) a [F3] .



[ENTER]

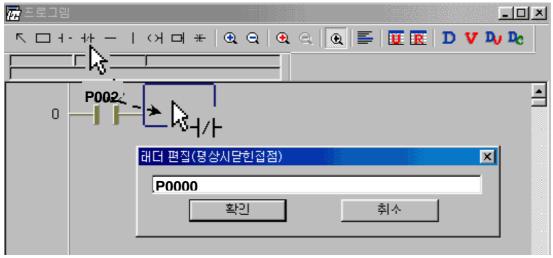
.



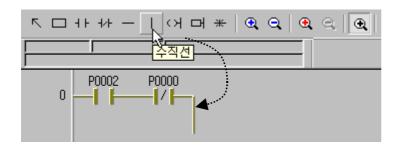
4 LG 55

(4) b [F4]

.



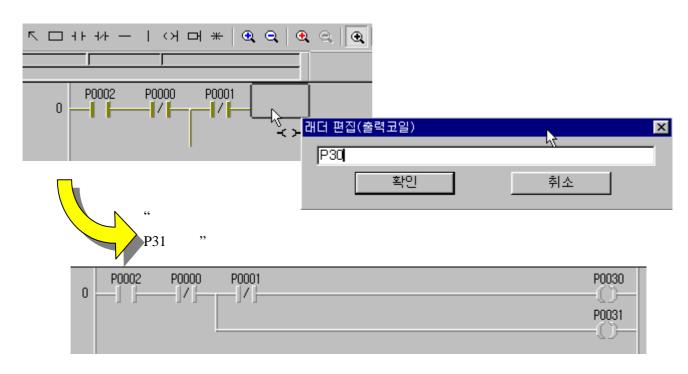
(5) [F6] .

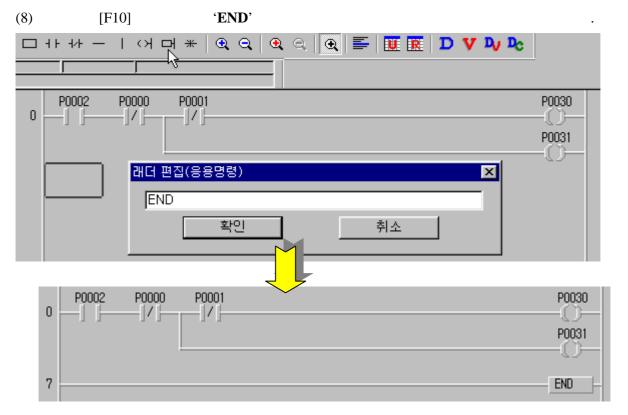


(6) '(4)' P1

```
0 P0002 P0000 P0001
```

(7) [F9] P30 .



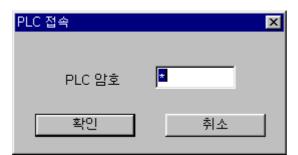




1)

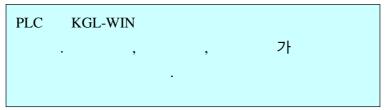


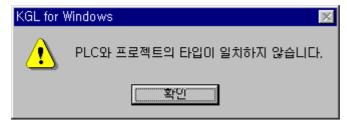
(2) . (



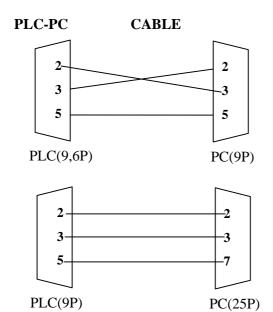
(3) **ERROR**

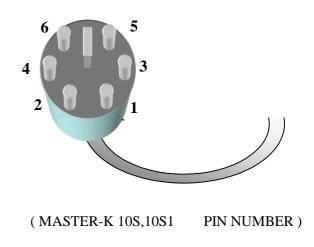




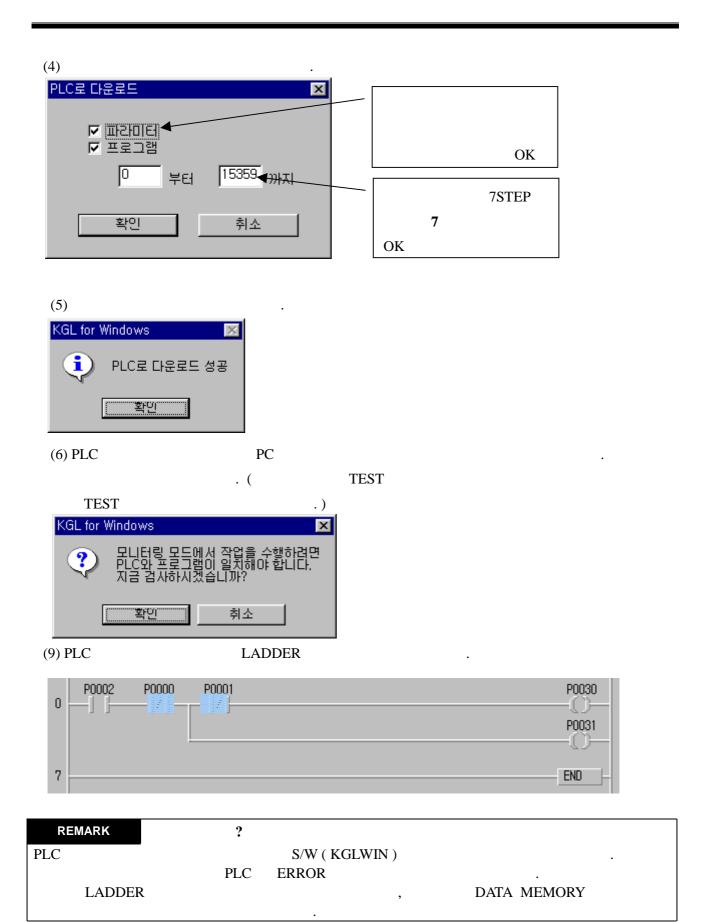


PLC





4 LG



:
1. P0020 P0021 P0060
P0060 P0060 Delete KEY

" "

2. /

:

[Ctrl+U]

:

. [Ctrl+M]

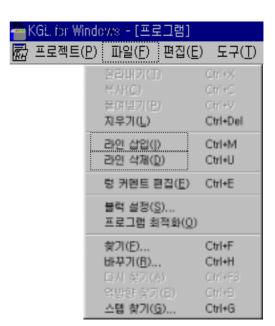
3.

INSERT KEY

4.





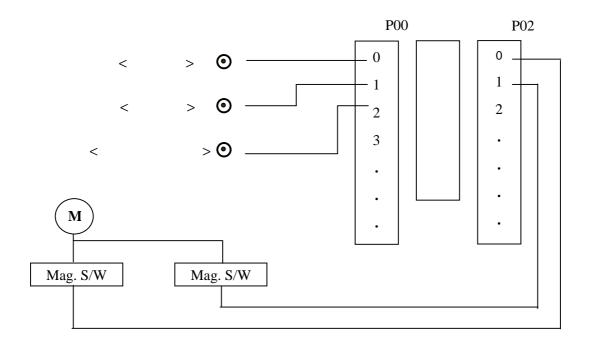


: • [LOAD, AND, OR, OUT]

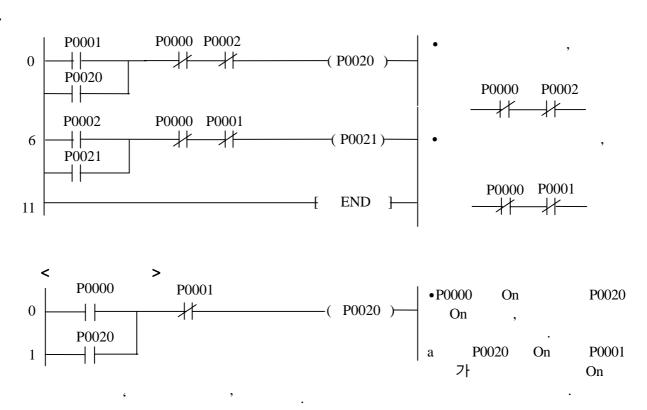
1. PB1 , PB2

, PB0 .

2.



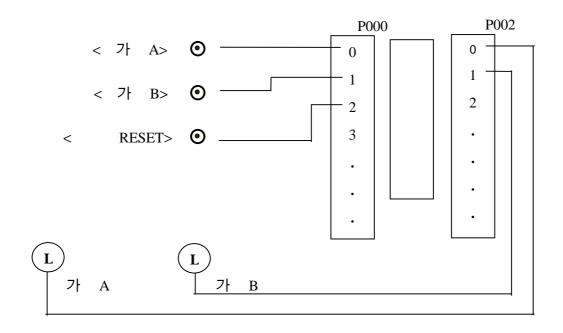
3.



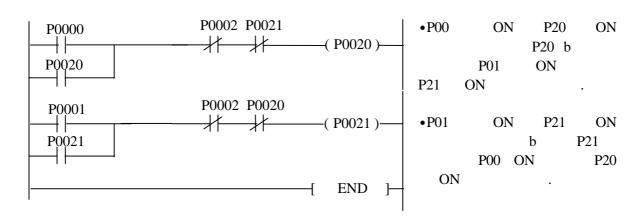
PB0, PB1

RESET (PB2)

2.



3.



REMARK

2, 3

2

b

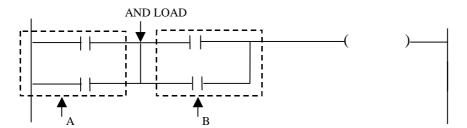
가 .

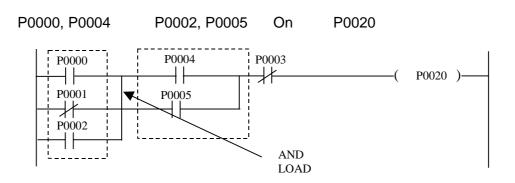
3

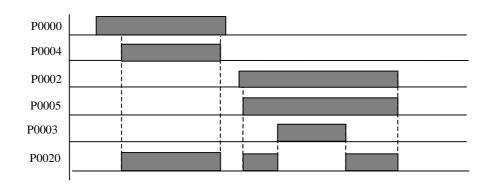
4.4

4.4.1 AND LOAD (Mnemonic)

	가												
		М	Р	K	L	F	Т	С	S	D	#D		
AND LOAD													1







. . 2 가

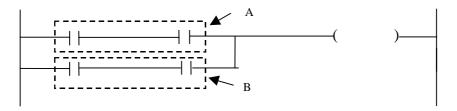
AND I	OAD	
LOAD	M0000	
OR	M0001	
LOAD	M0002	
OR	M0003	
AND LOAD		
LOAD	M0004	
OR	M0005	
AND LOAD		
LOAD	M0006	
OR	M0007	
AND LOAD		
LOAD	M0008	
OR	M0009	
AND LOAD		
OUT	P0020	
END		

AND LOAD	
LOAD	M0000
OR	M0001
LOAD	M0002
OR	M0003
LOAD	M0004
OR	M0005
LOAD	M0002
OR	M0007
LOAD	M0008
OR	M0009
AND LOAD	
OUT	P0020
END	
7 (8)	가
7 (8)	71

4 LG

4.4.2 OR LOAD(Mnemonic)

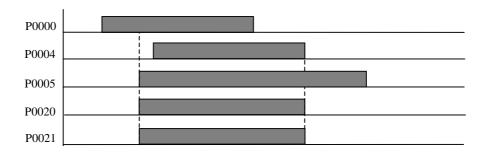
	가												
		М	Р	K	L	F	Т	С	S	D	#D		
OR LOAD													1



■ OR LOAD

P0000, P0005 P0004, P0005 On P0020, P0021

```
P0000 P0002 P0005 P00005 P00001 P00001 P00002 P00001 P00002 P00002 P00001 P00002 P00001 P0000
```



2 가

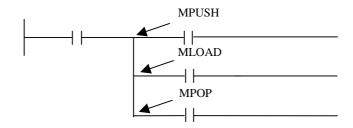
M0000 M0001 \dashv \vdash $\dashv \vdash$ ____(P0020) -M0002 M0003 $\dashv \vdash$ $+\vdash$ M0004 M0005 | | | - $\dashv \vdash$ M0006 M0007 $\dashv \vdash$ M0008 M0009 $\dashv \vdash$ $+\vdash$ _[END]_

	OR LOAD	
LOAD		M0000
AND		M0001
LOAD		M0002
AND		M0003
OR LOAD		
LOAD		M0004
AND		M0005
OR LOAD		
LOAD		M0006
AND		M0007
OR LOAD		
LOAD		M0008
AND		M0009
OR LOAD		
OUT		P0020
END		
	OR LOAD	

	OR LOAD)	
LOAD		M0000	
AND LOAD		M0001 M0002	
AND		M0002 M0003	
LOAD		M0004	
AND		M0005	
LOAD		M0006	
AND		M0007	
LOAD		M0008	
AND		M0009	
OR LOAD			
OR LOAD OR LOAD			
OR LOAD			
OUT		P0020	
END		10020	
7	(8)	가	
	` /		

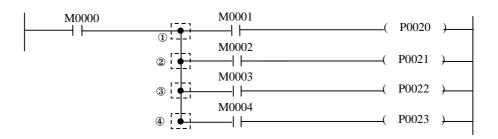
4.4.3 MPUSH, MLOAD, MPOP(Mnemonic)

	가												
		М	Р	K	L	F	Т	С	S	D	#D		
MPUSH MLOAD MPOP													1



■ MPUSH, MLOAD, MPOP

1) ・Ladder 가



① MPUSH : · M0000 가 PLC .

② MLOAD : ⋅ M0000 .

3 MLOAD : · M0000

⊕ MPOP : · M0000 PLC Read

Reset .

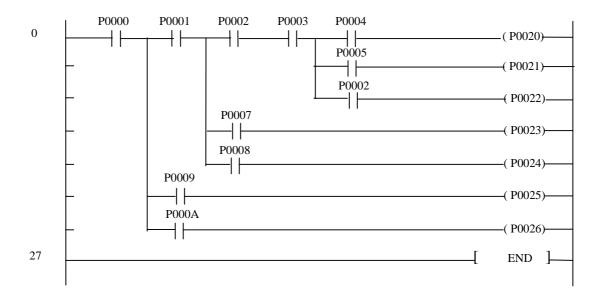
REMARK

• MPUSH ~ MPOP 8 가

• MPUSH :

• MLOAD :

• MPOP :

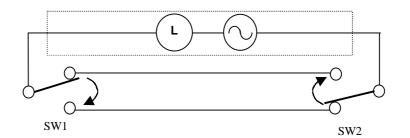


STEP		INSTRUCTION
0000	LOAD	P0000
0001	MPUSH	
0002	AND	P0001
0000	MPUSH	
0004	AND	P0002
0005	AND	P0003
0002	MPUSH	
0007	AND	P0004
0008	OUT	P0020
0009	MLOAD	
0010	AND	P0005
0011	OUT	P0021
0012	MPOP	
0013	AND	P0002
0014	OUT	P0022
0015	MLOAD	
0016	AND	P0007
0017	OUT	P0023
0018	MPOP	
0019	AND	P0008
0020	OUT	P0024
0021	MLOAD	
0022	AND	P0009
0023	OUT	P0025
0024	MPOP	
0025	AND	P000A
0026	OUT	P0026
0027	END	
0028	NOP	
0029	NOP	
0000	NOP	

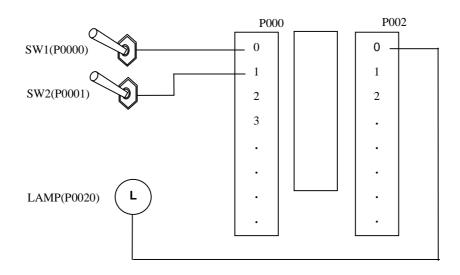
: ()

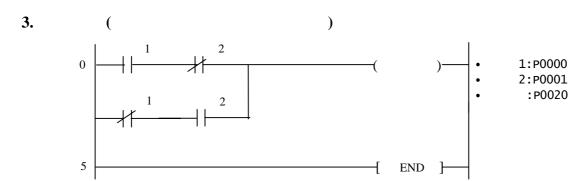
1.

SW1 ON 가 SW2 OFF . SW2 OFF .



2.

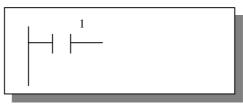




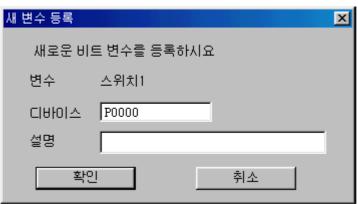
4. 4. 4 KGL-WIN





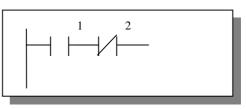


(3) 1 'P0000' .

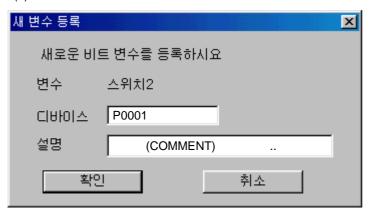


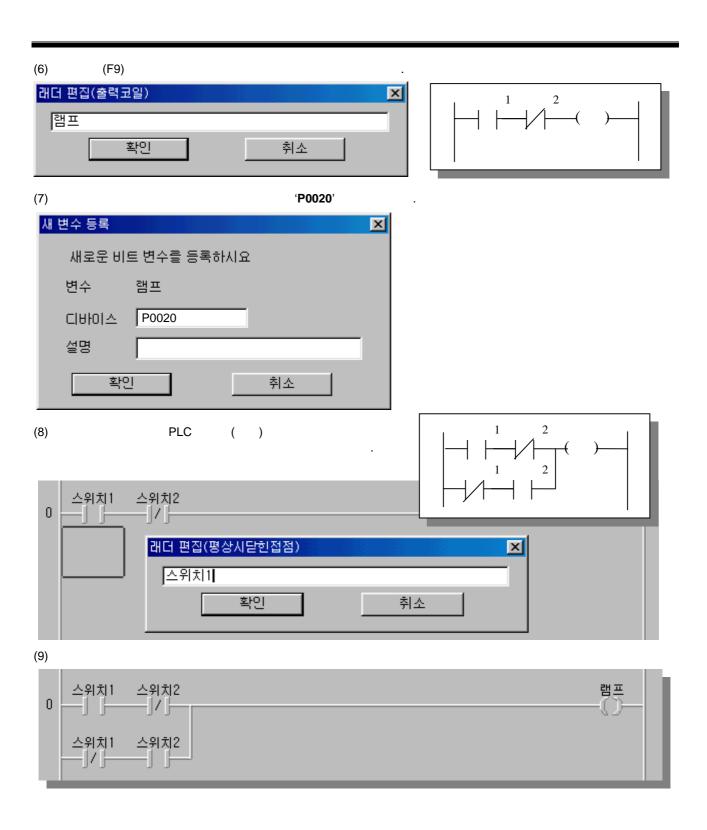
(4) b

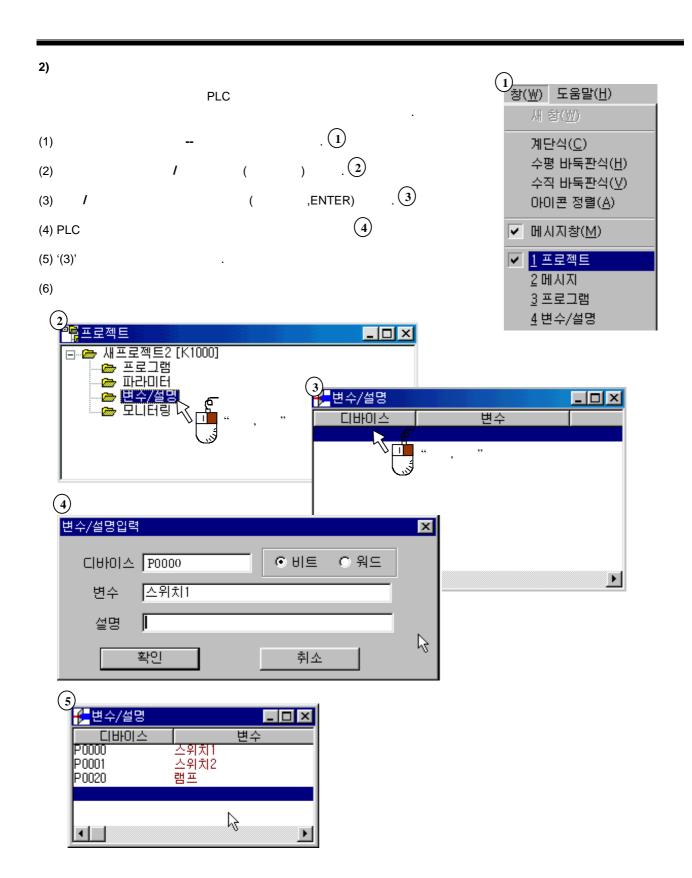




(5) 1 'P0001' .







4.5.1 NOT

						가					
	М	Р	K	L	F	Т	O	S	D	#D	
NOT											1



■ NOT

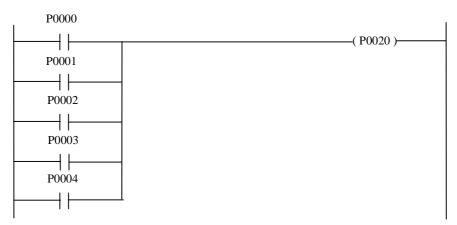
1)

. [NOT] a b , b a (, ,) .

①, ②

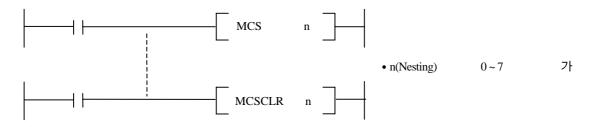


2



4.6.1 MCS, MCSCLR

						가						
	М	Р	K	L	F	Т	С	S	D	#D		
MCS MCSCLR											0	1



■ MCS, MCSCLR

1)

• MCS On MCS MCSCLR

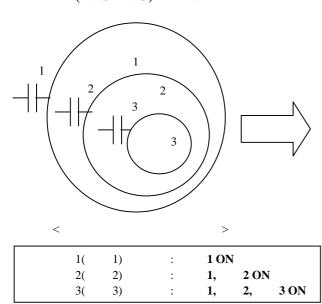
Off

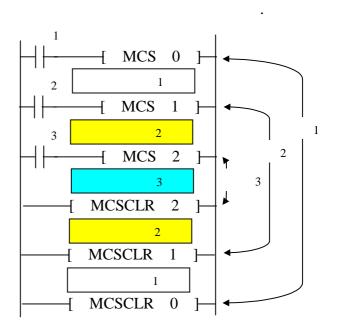
• MCS 0 가 가 7 가 가

• MCSCLR 가 MCS

() MCS MCSCLR

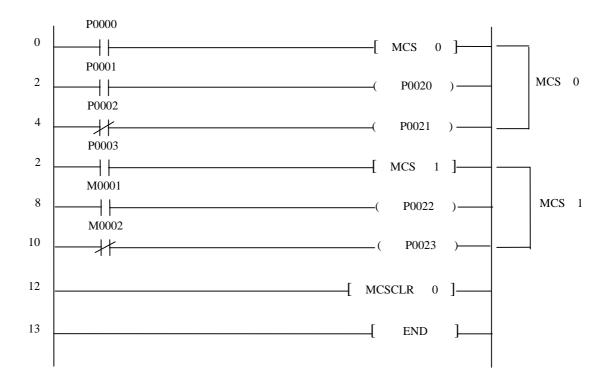
(NESTING) :





4

2) • MCS 2 MCSCLR 7 "0"

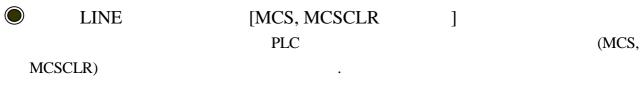


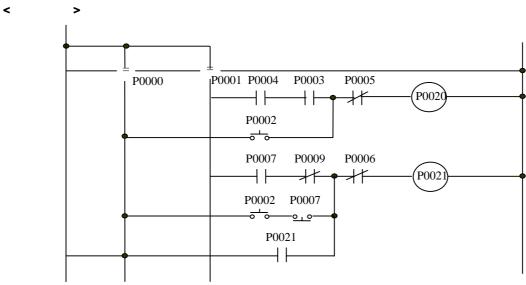
```
MCS On/Off Off MCS ~ MCSMLR
MCS(MCSCLR)

• : . Off

• : . ( )

• OUT : .
• SET, RST :
```



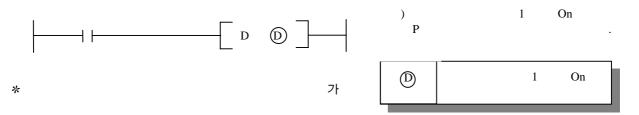


< P0000 P0001 0 MCS 0 \dashv \vdash P0002 3 M0010 \dashv \vdash P0020 P0007 5 M0011 $\dashv \vdash$ 8 MCSCLR 0 P0001 P0000 9 $\dashv \vdash$ # -[MCS 1]-P0003 P0004 12 M0020 $\dashv \vdash$ $\dashv \vdash$ P0007 P0009 15 M0021 18 -[MCSCLR P0005 M0010 19 P0020 \dashv \vdash M0020 P0006 M0011 23 P0021 $\dashv \vdash$ # P0021 28 $\dashv \vdash$ **END**

(D, D NOT)

4.7.1 D

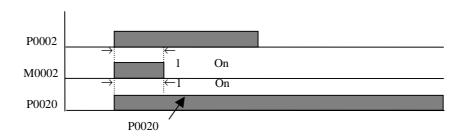
ľ								가					
			М	Р	K	L	F	Т	С	S	D	#D	
	D	0	0	0	0	O*							2



■ D

1) • Off \rightarrow On 1 On Off .

•

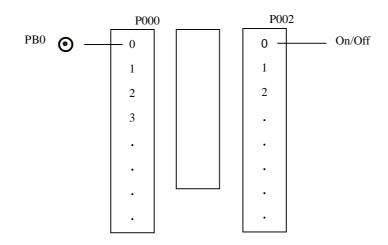


: On/Off [D]

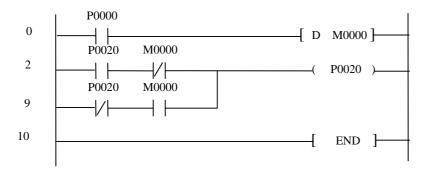
1. PB0 On , Off .

PB0 On/Off .

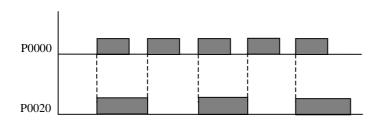
2.



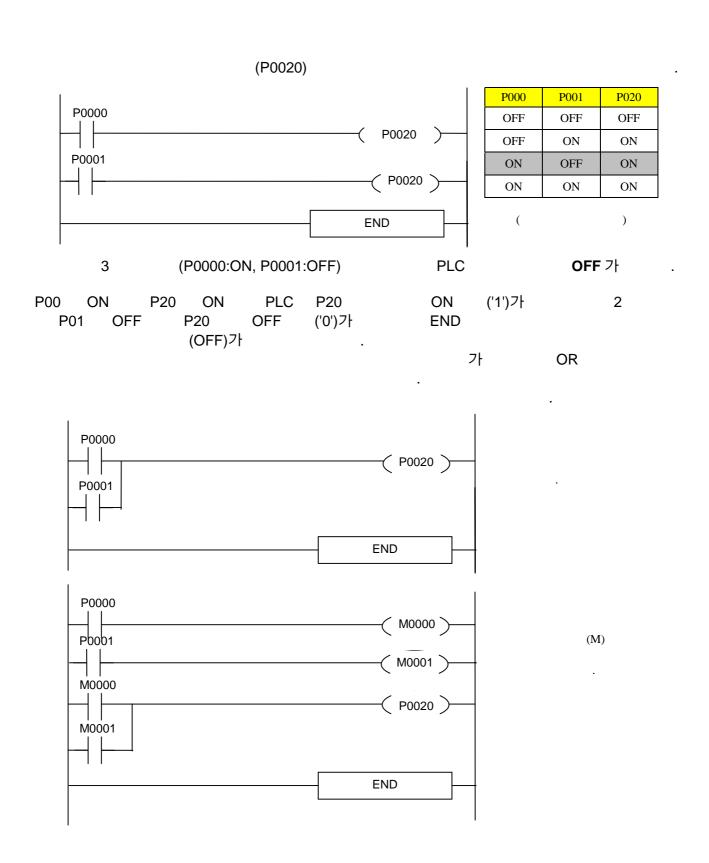
3.



4.



4

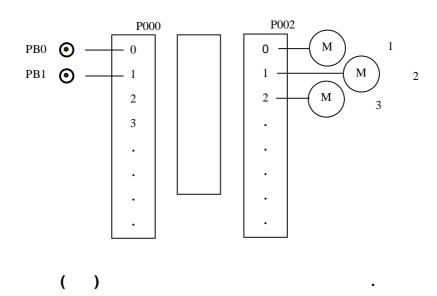


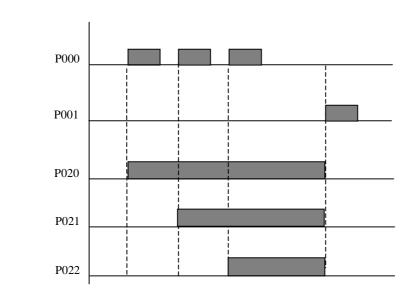
: 가 [D]

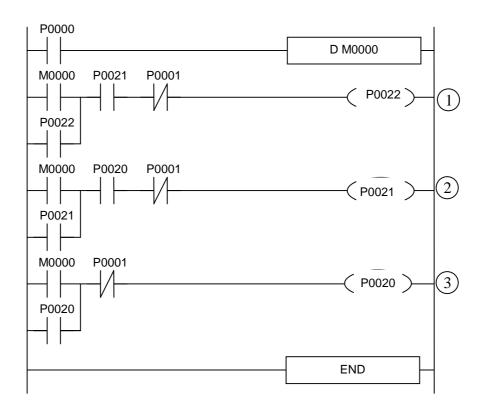
1. PB0 1 ON, 2가 ON, 3 ON PB0 ア

PLC .

2.





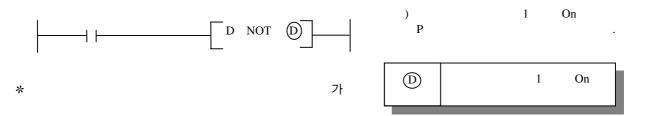


5.

1 ST SCAN	M0000 M0000 M0000	P021 P020	P001	P022			
-		P020		F 022			
SCAN	MOOOO	1 020	P001	P021			
	IVIOUU		P001	∠ P020			
1 ST	M0000	P021	P001	P022			
SCAN	M0000	P020	P001 /	P021			
	M0000		P001	P020			
	_						
2 PB0 ON							
1 ST	M0000	P021	P001	P022			
SCAN	M0000	P020	P001	P021	1	PB0 ON	P20 ON
	M0000		P001	P020			
1 ST	M0000	P021	P001	P022			
SCAN	M0000	P020	P001	P021			
	M0000		₹ 001	P020			
	1						
3 PB0 ON							
1 ST	M0000	P021	P001	P022	2	PB0 ON	P21 ON
SCAN	M0000	P020	P001	P021	1	PB0 ON	P20 ON
	M0000		P001	P020			
1 ST	M0000	P021	P001	P022			
SCAN	M0000	P020	P001	P021			
	M0000		P001	P020			
:		(a :ON, b	:OFF)				

4.7.2 D NOT

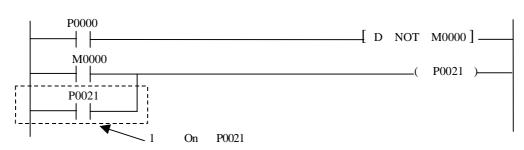
							가					
		М	Р	K	L	F	Т	C	Ø	D	#D	
D NOT	0	0	0	0	O*							2

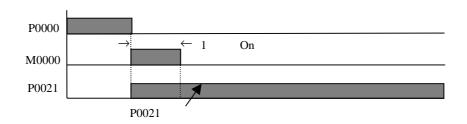


■ D NOT

1) $\cdot \qquad \text{On} \rightarrow \text{Off} \qquad \qquad 1 \qquad \text{On} \qquad \qquad \text{Off} \qquad .$

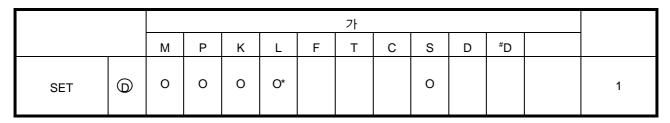
2) . P0000 On \rightarrow Off D NOT

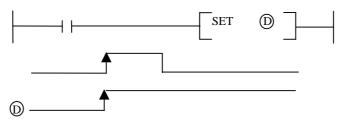




(SET,RST)

4.8.1 SET





米

가

SET

1)

On

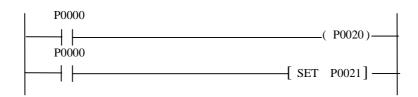
On

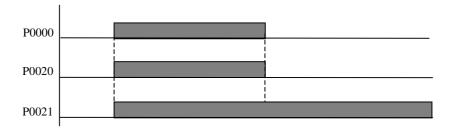
Off

On

2)

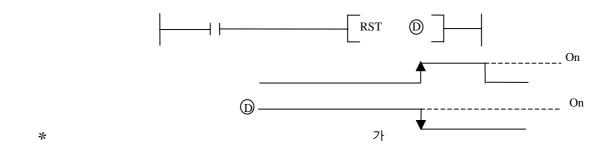
P0000 On \rightarrow Off P0020, P0021





4.8.2 RST

						가					
	М	Р	K	L	F	Т	С	S	D	#D	
RST D	0	0	0	O*				0			1



■ RST

1) · On Off Off Off

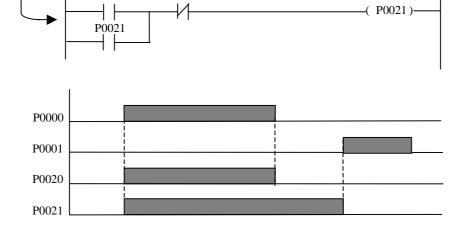
____[SET P0021]-

_[RST P0021]-

P0001

P0000

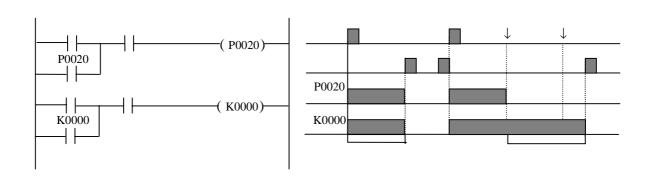
P0001

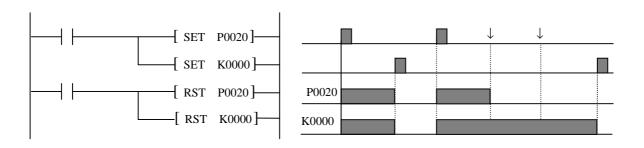


● P K , /

1. (P) (K)

, On .





(Latch area)

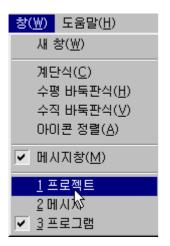
	Device		
	М	M0000~M191F	
	L	L000~L063F	
	T(100ms)	T000~T191	T144~191
	T(10ms)	T192~T255	T240~T255
	С	C000~C255	C240~C255
D	K80S, K200S, K300S	D0000~D4999	D3500~D4500
	K1000S	D0000~D9999	D6000~D8999
	S	S00.00~S99.99	S80~S99

	Device		
	М	M000~M31F (M000~M15F)	
	L	L000~L15F (L000~L07F)	L000~L015F (L000~L07F)
	T(100ms)	T000~T095 (T000~T031)	T072~T095 (T024~T031)
	T(10ms)	0096~0127 (0032~0047)	T120~T127 (T044~T047)
	С	C000~C127 (C000~C015)	C096~C127 (C012~C015)
D	K10S1	D000~D063	D048~D063
	K10S, 30S, 60S, 100S	D000~D255	D192~D255
	S	\$00.00~\$31.99 (\$00.00~\$15.99)	\$80~\$99 (\$12~\$15)

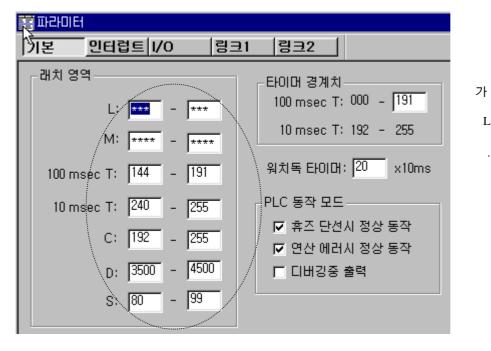
() K10S1

2.3.3

KGL-WIN







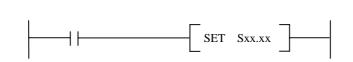
Device K200S パト . L,M

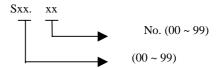
(STEP CONTROLER)

4.9.1

(SET S)

						가					
	М	Р	K	L	F	Т	С	S	D	#D	
SET S ①								0			1





■ SET Sxx.xx(

1)

가 On 가 On

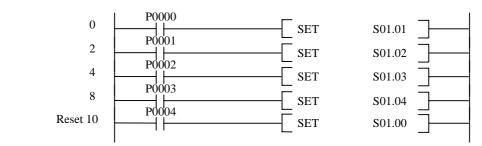
· 가 On Off On

On On .

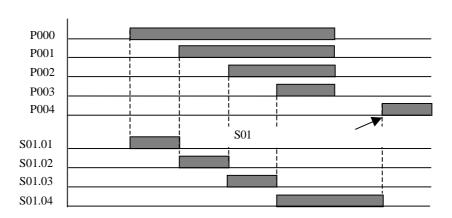
· SET Sxx.xx Sxx.00 On

2)

· -S01.**



· On On .



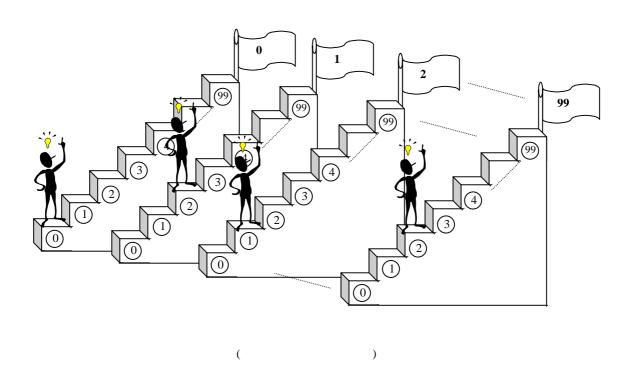
()

가 On 가 On 1 0 가 0, 2 1

ON (SET) .

On

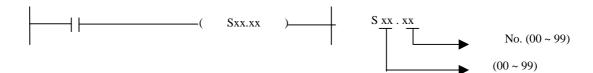
.



6. [SET S] 1 2 가 3 4가 1 SET S00.00 , Reset Reset S00.01 \dashv \vdash S00.02 2 SET S00.01 1 S00.02 S00.03 3 \dashv \vdash 2 SET S00.03 S00.04 SET S00.04 S00.01 \dashv \vdash 1 S00.02 2 \$\int^2\$. \dashv \vdash S00.03 \dashv \vdash 3 S00.04 \dashv \vdash 4 -[END]-

4.9.2 (OUT S)

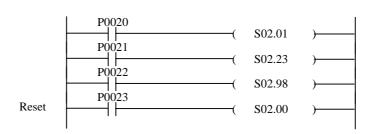
							가					
		М	Р	K	L	F	Т	С	Ø	D	#D	
OUT S	0	0	0	0	O*				0			1



■ OUT Sxx.xx()

1) · 가 On On On On 가 On Off On

2) · S02

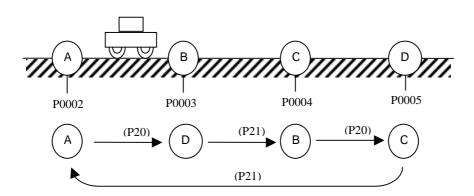


No	P020	P021	P022	P023	S02.01	S02.23	S02.98	S02.00
1	On	Off	Off	Off	0			
2	On	On	Off	Off		0		
3	On	On	On	Off			0	
4	On	On	On	On				0

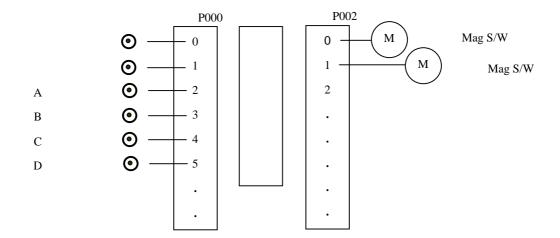
1.

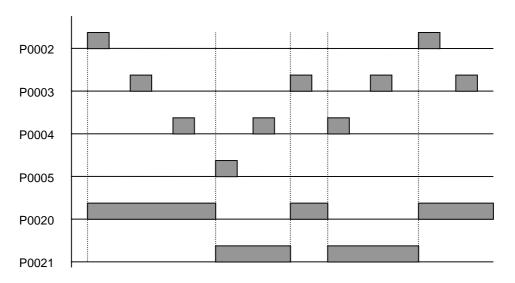
P0000 ON D D B C A ON A

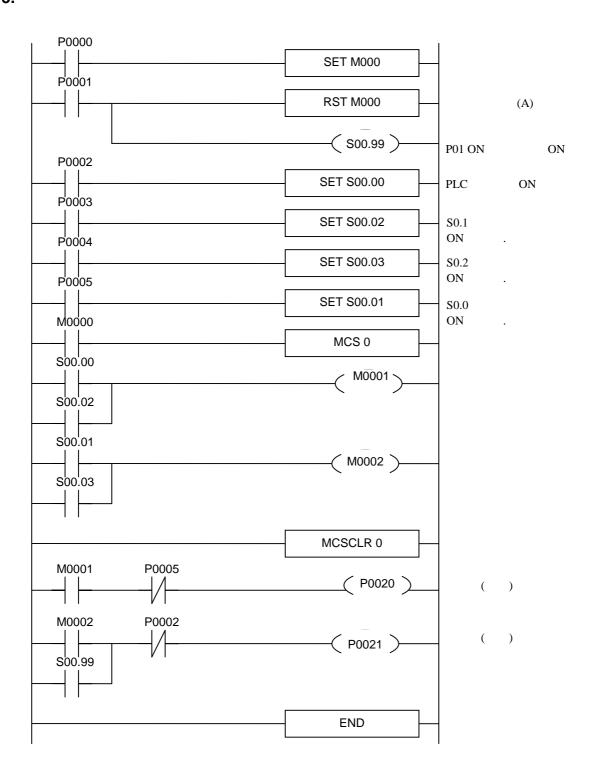
..



2.

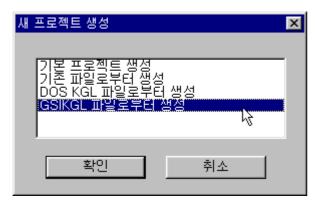






GSIKGL

KGLWIN



STEP 1

- - .



STEP 2

- 1. .
- 2. GSIKGL
- 3. (*.cmt)
- 4. (*.pgm), (*.cmt)







STEP 3

.

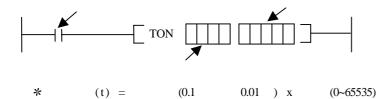
2. 가 .

(.)

4.10 (TON,TOFF,TMR,TMON,TRTG)

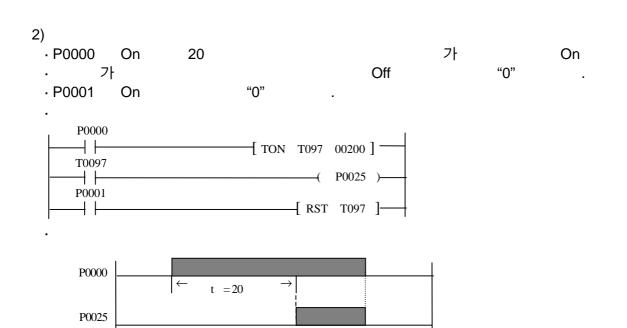
4.10.1 ON Delay (TON)

						가						
	М	Р	K	L	F	Т	С	Ø	D	#D		
TON						0						3
									0		0	



■ TON

(가



1. KGLWIN

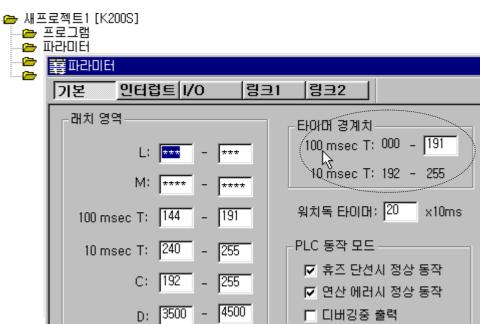




2.

	가	
100 ms	T000~T255	T000~T191
10 ms	T000~T255	T192~T255

KGL-WIN



s: 80

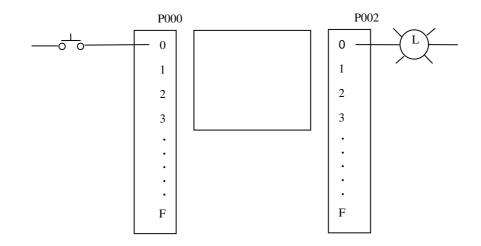
가

: [TON]

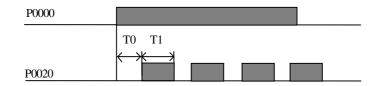
1.

2 () .

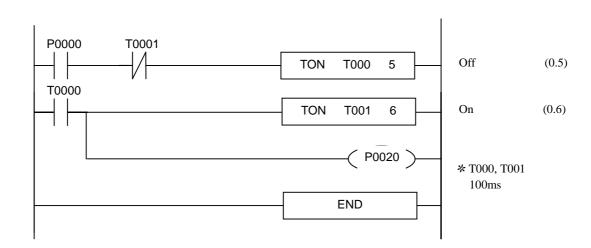
2.



3.



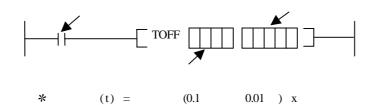
4.



4

4.10.1 OFF Delay (TOFF)

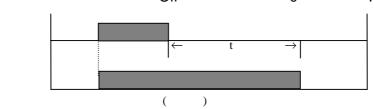
	가											
	М	Р	K	L	F	Т	С	S	D	#D		
TOFF						0						3
									0		0	



■ TOFF

1)
· On 가 On .
· Off 가 가 가 가 "0"

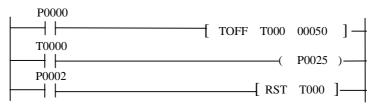
Off . • Reset Off "0" .



2) - P0000 On T000 On P0025 On . - P0000 Off フト"0"

Off .

· P0002가 On Off "0" .



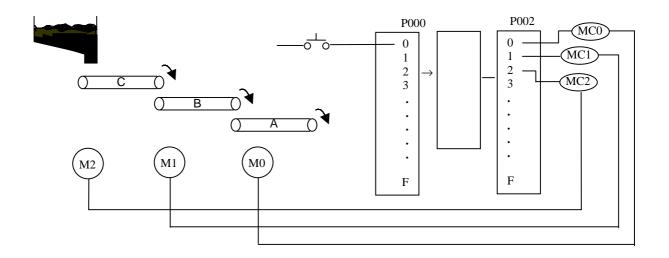
. poooo

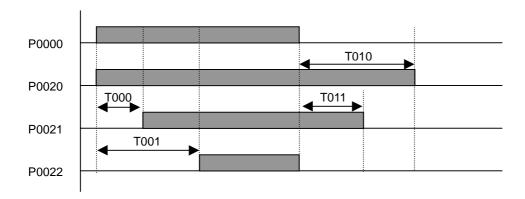
[TON, TOFF]

1.

$$(\mathsf{A} \to \mathsf{B} \to \mathsf{C}), \qquad (\mathsf{C} \to \mathsf{B} \to \mathsf{A}) \qquad .$$

2.





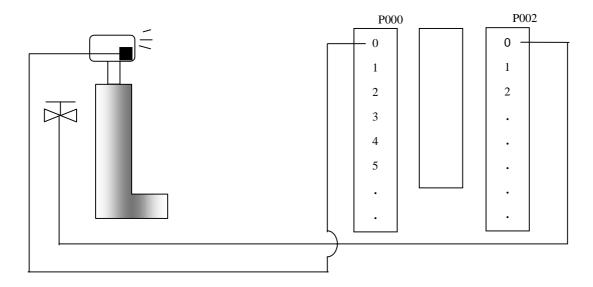
```
P0000
                                   TOFF T010 100
                                                        • A
                                                                               (10)
T0010
                                           ( P0020 )
P0000
                                    TON T000 50
                                                                               (5)
                                                        • B
T0000
                                   TOFF T011 50
                                                        • B
                                                                               (5)
T0011
                                           ( P0022 )
T0000
P0000
                                   TMON T001 100
                                                        • C
                                                                               (10 )
T0001
       P0000
                                           ( P0022 )
                                          END
```

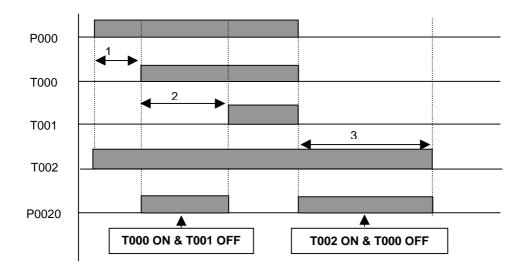
[TON, TOFF]

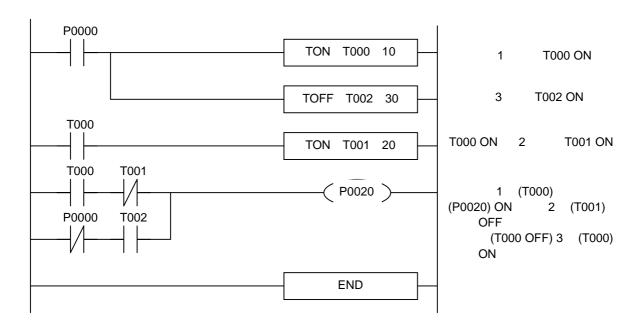
1.

가 1 2 3

2.

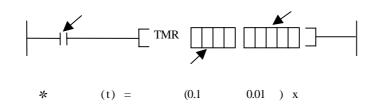






4.10.3 (TMR)

		가										
	М	Р	K	L	F	Т	С	S	D	#D		
TMR						0						3
									0		0	



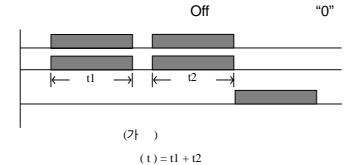
■ TMR

1) • On

가 가 ,

On .

· Reset



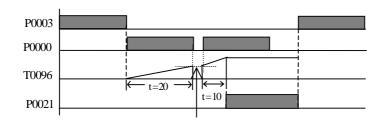
P0000

T0096

P0003

TMR T096 300]

P0021)



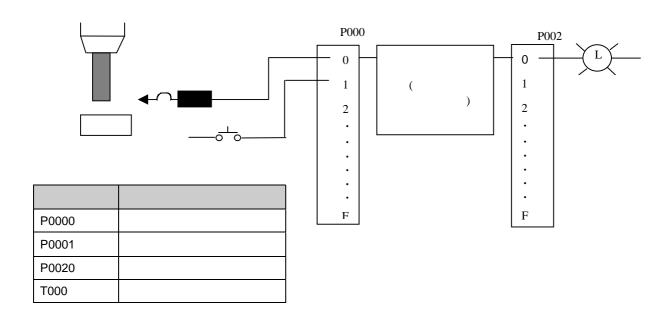
_[RST T096]-

: [TMR]

1.

·

2.



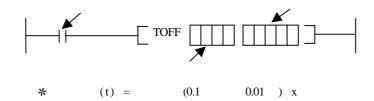
3.

```
P0000
                                TMR T000 36000
Toooo
                                                    • 1
                                  RST
                                         T000
P0001
                                                    RESET
T0000
                                    CTU
                                           C000
C0000
                                                    • 100
                                    <S> 00100
C0000
       P0001
                                    ( P0020 )
P0020
                                     END
```

. (.)

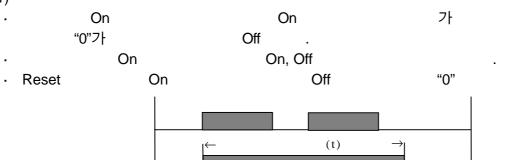
4.10.4 (TMON)

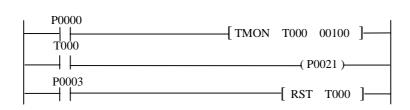
가															
	М	Р	K	L	F	Т	С	S	D	#D			(F110)	(F111)	(F112)
TMON						0						3			
									0		0				

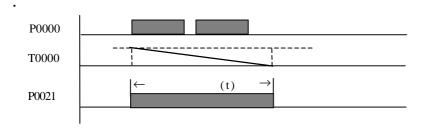


■ TMON

1)





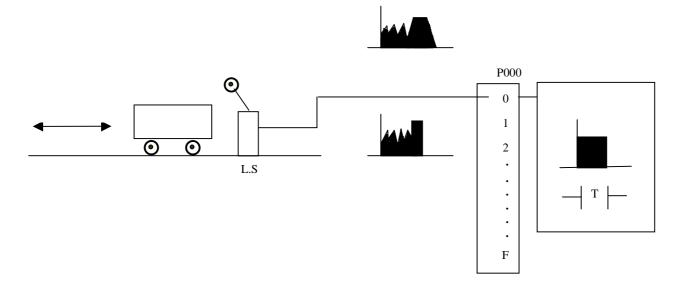


10. [TMON]

1.

가 (),

2.



P0000	
M0000	
T000	

3.

```
TMON T000 000

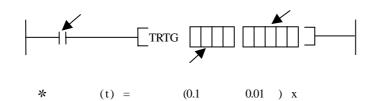
T0000

M0000

M
```

4.10.5

		가										
	М	Р	K	L	F	Т	С	S	D	#D		
TRTG						0						3
									0		0	



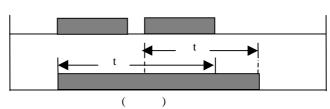
■ TRTG

1)

· On On 가 "0" Off .

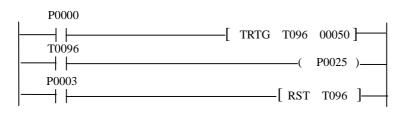
· 기 "0" Off \rightarrow On

- Reset On Off "0"

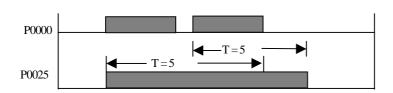


2) · P0000 On T096 On 가 "0" P0025 Off · "0" P0000 가

Reset P0003 On "0" Off .



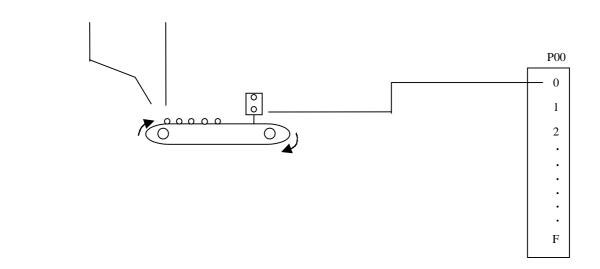
.



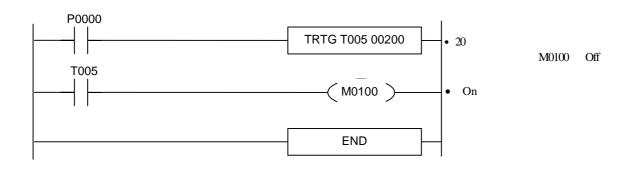
[TRTG]

2.

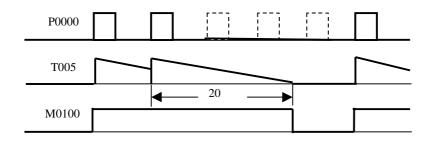
1.



3.



•



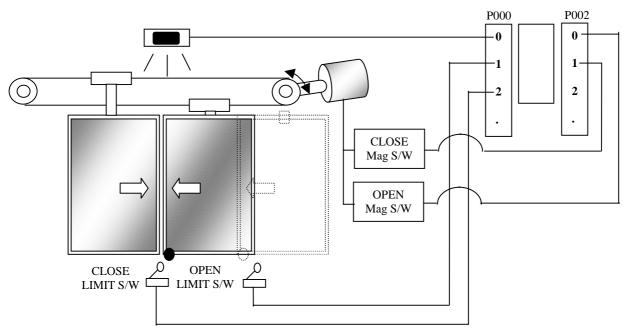
4

: [TRTG]

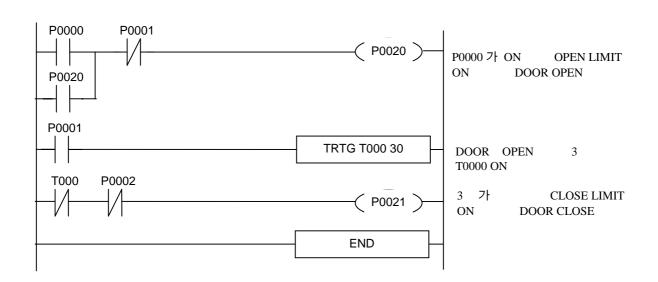
1.

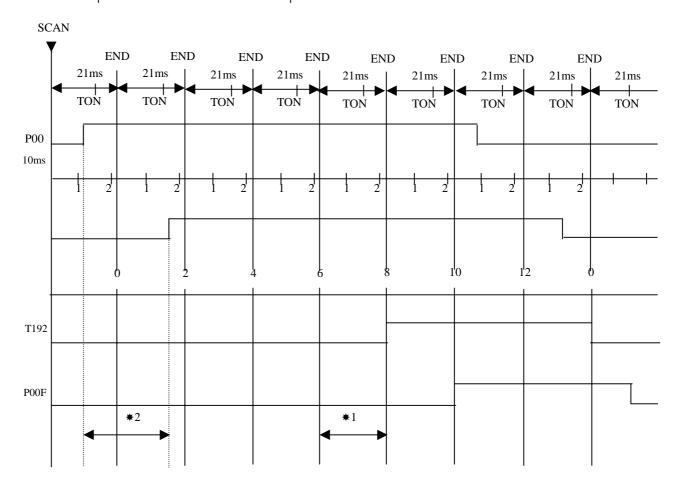
3

2.



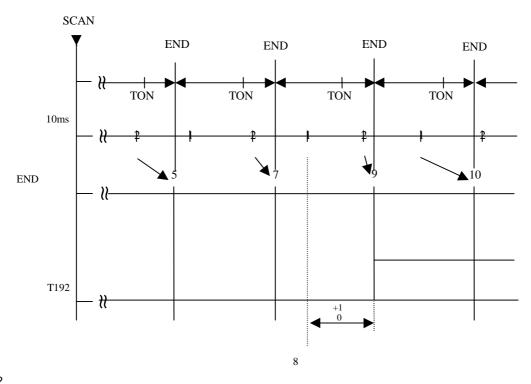
3.



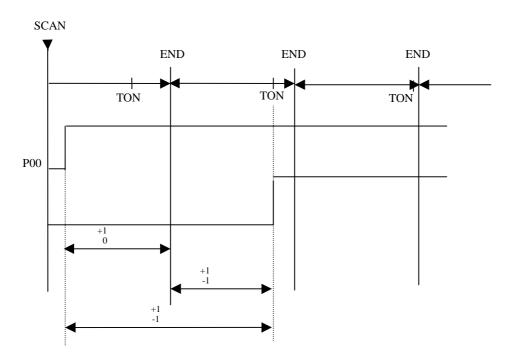




*1



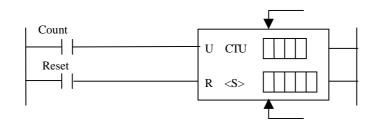
*****2



4.11 (CTU,CTD,CTUD,CTR)

4.11.1 (CTU)

		가										
	М	Р	K	L	F	Т	С	S	D	#D		
CTD							0					3
									0		0	

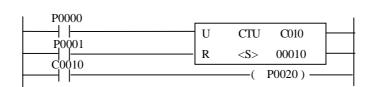


■ CTU

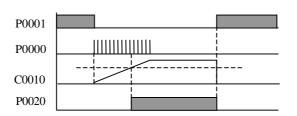
1)
• 7† +1 7†
• (65535) Count
• Reset 7† On Off "0" .

Reset

2) • P0000 Count Up 가 P0020 On • P0001 On Off "0" .

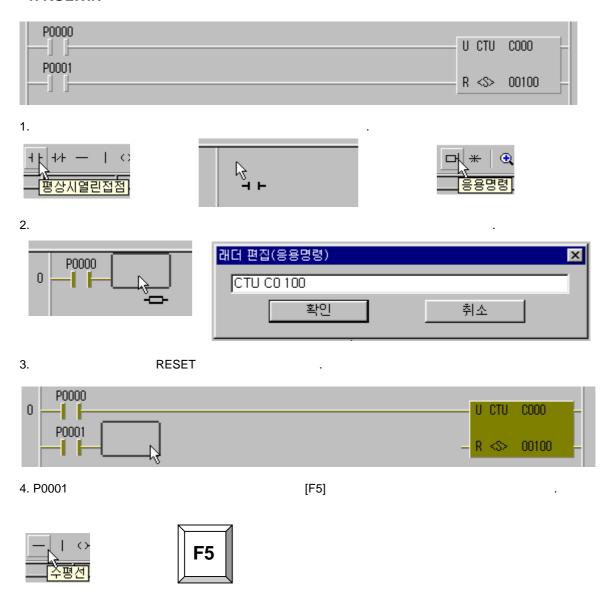


•



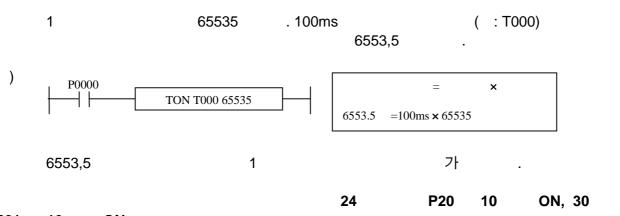
On

1. KGLWIN



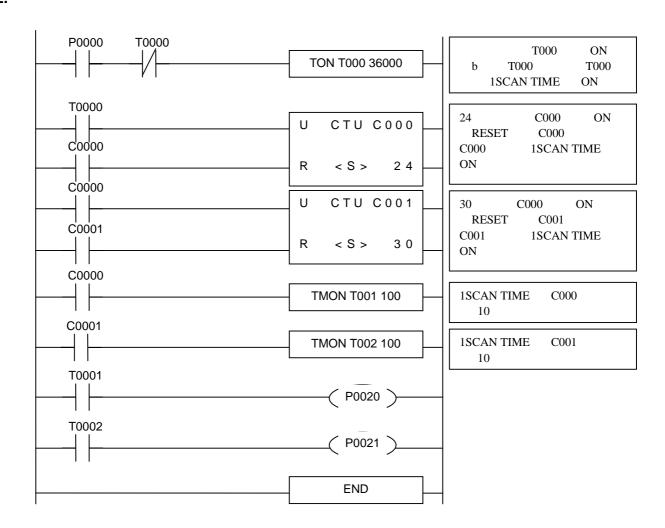
:

1.



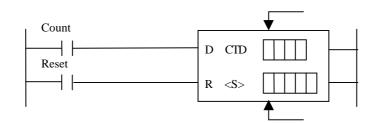
P21 10 ON

2.



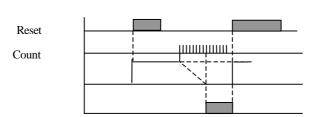
4.11.2 DOWN (CTD)

		가										
	М	Р	K	L	F	Т	С	S	D	#D		
CTD							0					3
									0		0	

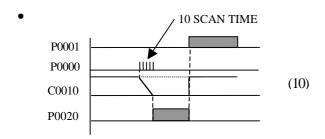


■ CTD

1)
• フト 1 "0" On
• Reset フト On Off フト .

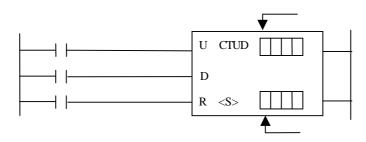


2) Count Down 가 "0" • P0000 10 On On P0020 Off 가 • P0001 On P0000 D CTD C010 P0001 R C0010 <S> 00010 (P0020)-



4.11.3 UP-DOWN (CTUD)

		가										
	М	Р	K	L	F	Т	С	S	D	#D		
СТИ							0					3
									0		0	



■ CTUD

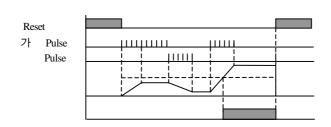
1)



• Down 가 1 .

• Reset 7 ↑ On "0" .
• Up, Down 7 ↑ On .

•

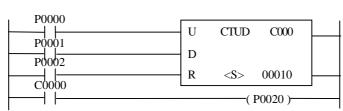


2) • P0000 Count Up 가 P0020

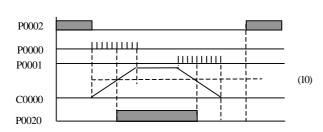
On .
• P0001 Count Down .

• Reset Off "0" .

•



•

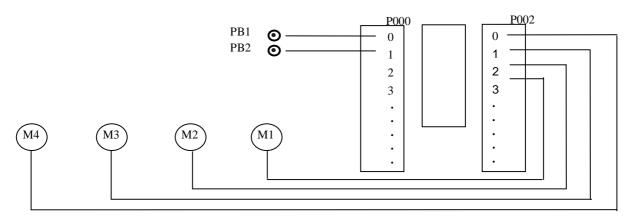


[CTUD]

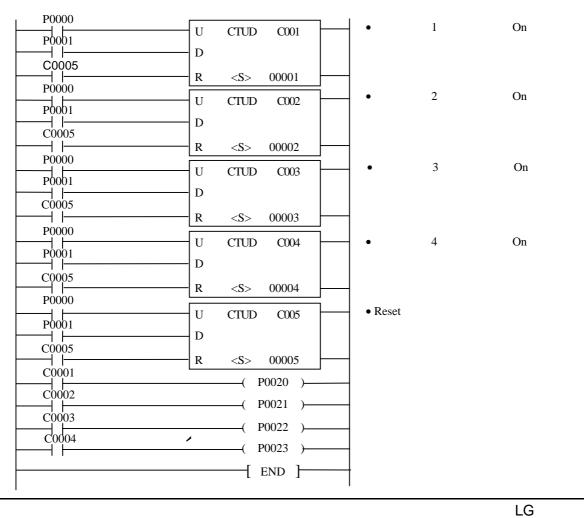
1.

PB1 4 1 가 PB2 1 가 PB1 , 1 가 4 PB2

2.

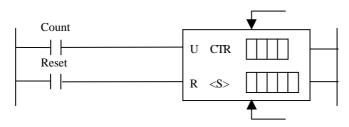


3.

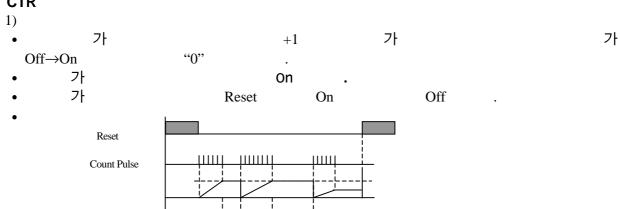


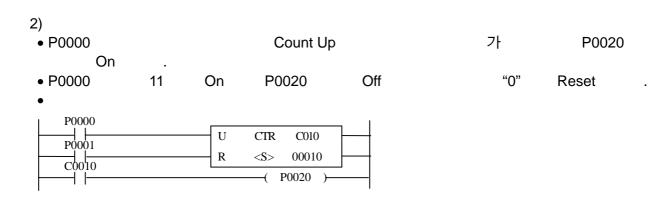
4.11.4 RING (CTR)

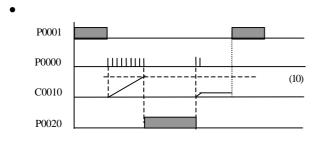
가												
	М	Р	K	L	F	Т	С	S	D	#D		
CTR							0					3
									О		0	

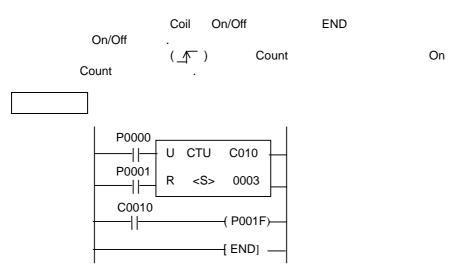


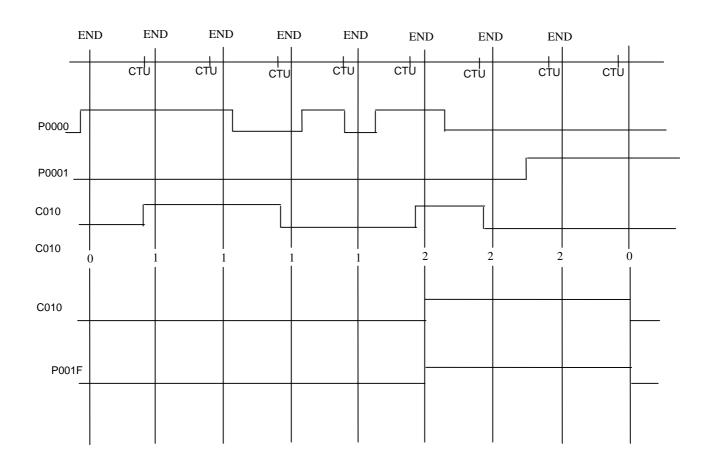
■ CTR

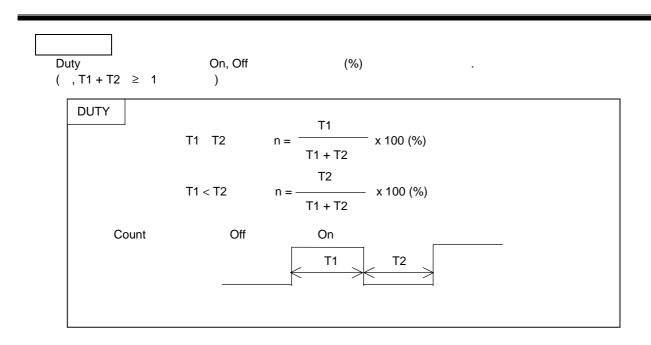






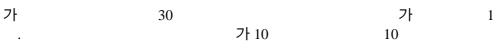




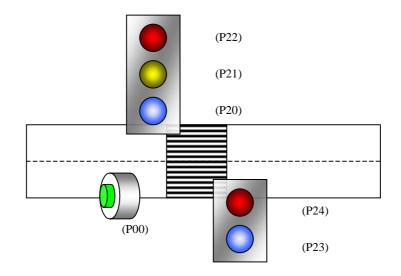


: (1)

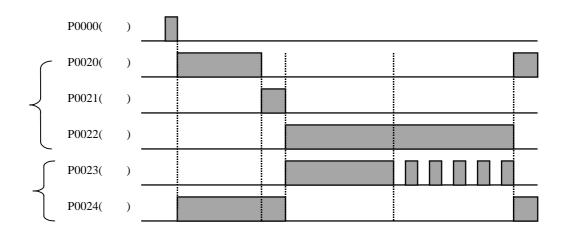
1.

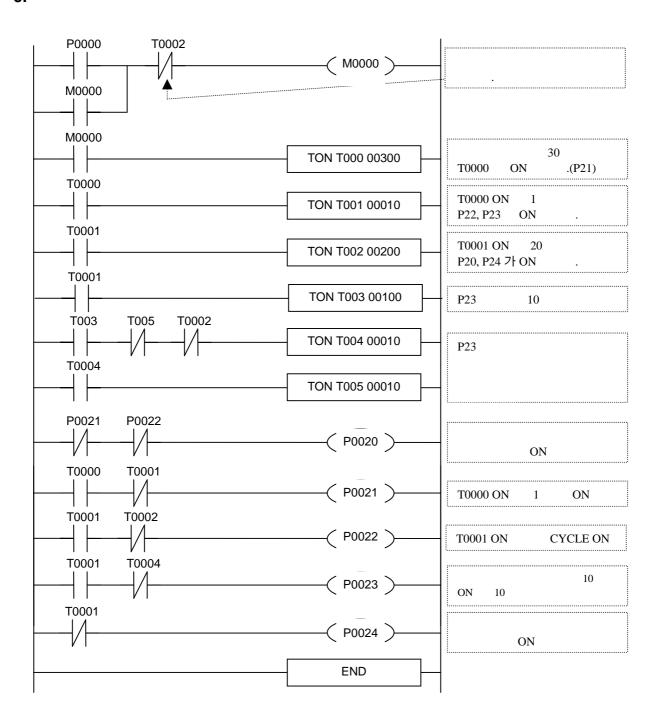


.



2.





: 가

 1.
 A
 \rightarrow , \rightarrow \rightarrow B

 B
 A
 A

 B
 B
 10

 B
 B
 10

 B
 B
 10

 B
 C
 C

 B
 C
 C

 B
 C
 C

 B
 C
 C

 B
 C
 C

 B
 C
 C

 B
 C
 C

 B
 C
 C

 B
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

 C
 C
 C

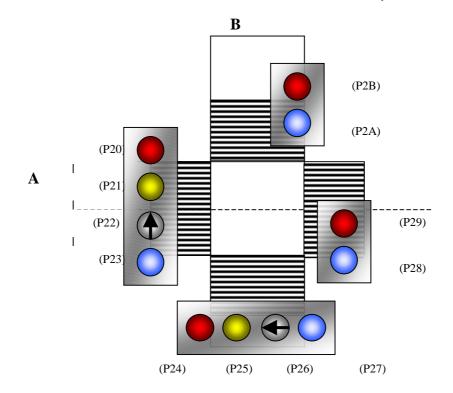
 C
 C
 C

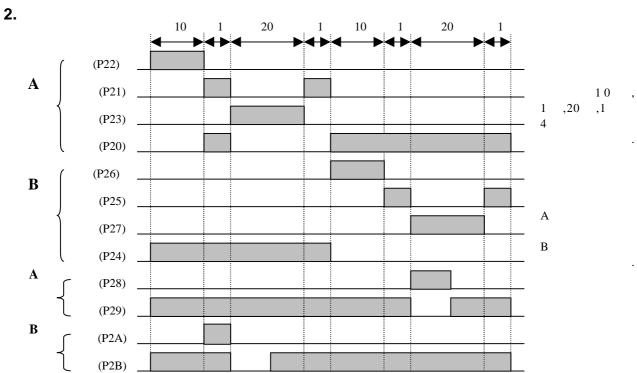
 C
 C
 C

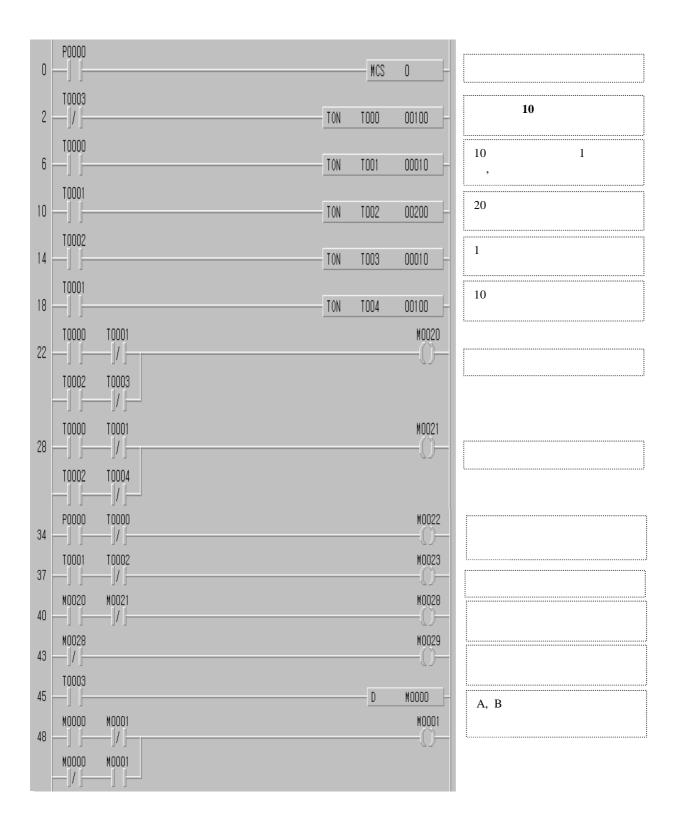
 C
 C
 C

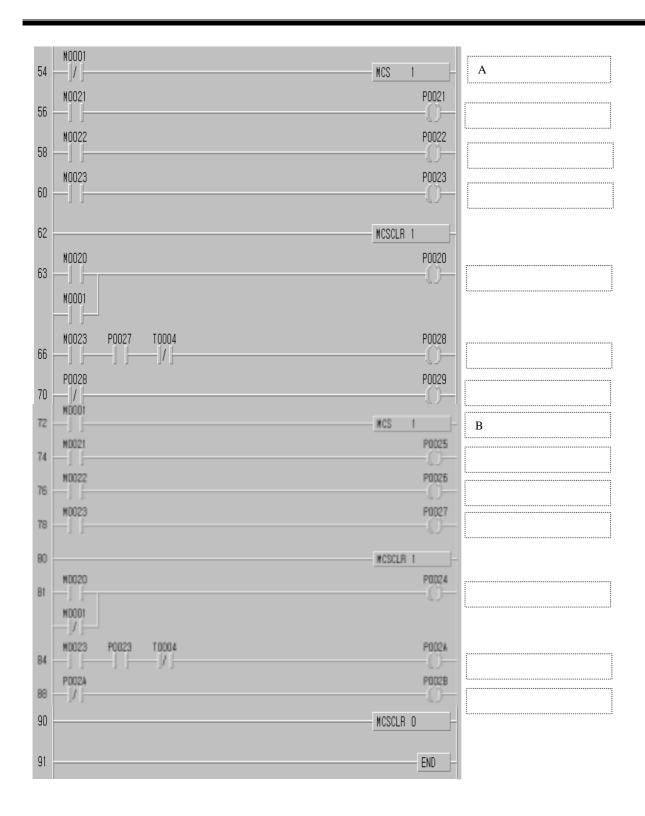
 C
 C
 C

 C
 C
 C<





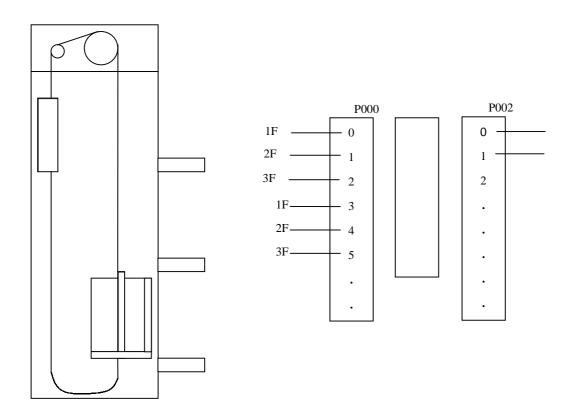




1.

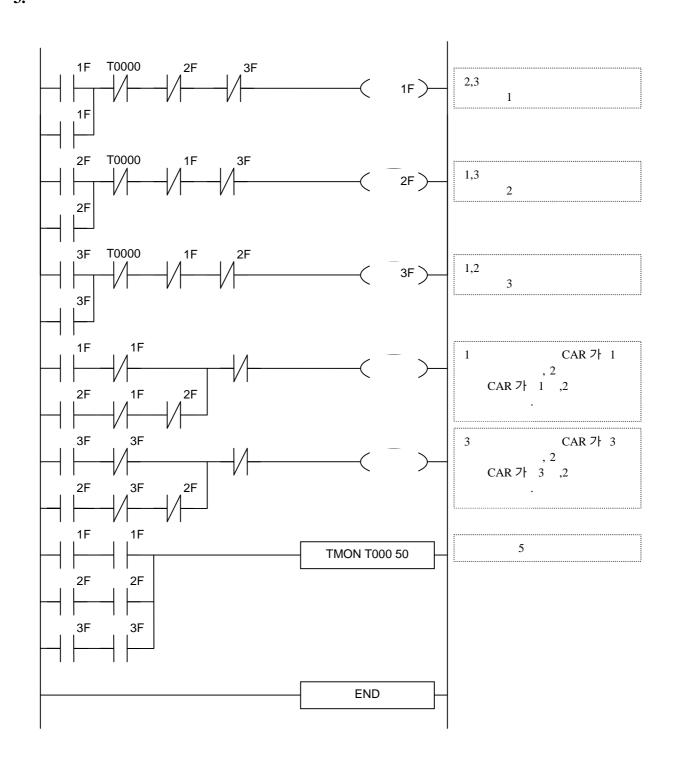
3 1 1 가

CAR HALL



	DEVICE	
1 F	P0000	1 (CAR,HALL)
2 F	P0001	2 (CAR,HALL)
3F	P0002	3 (CAR,HALL)
1 F	P0003	CAR 1
2 F	P0004	CAR 1
3F	P0005	CAR 1
1 F	M0000	1
2 F	M0001	2
3F	M0000	3
	P0020	CAR
	P0021	CAR

()



5 5.1 가 PLC PLC 7가 가 ① PLC PLC 3 PLC 4 **⑤** 7 가 , , , 2가 가 . (保全) (1) PLC 1 , 3 , 6 가 가 (2) . PLC PLC 가

5 LG

가

가

5.2 PLC

1		
	, , ,	
	Maker 가	
	Unit(I/O) ,	
()		
	Cable ,	
	(cable)	
Lamp	()	
Battery	가, Maker 가	
	(, check)	
Relay	「 」音 가	
Fuse	, 가	
Program	Master Priogram() Program ,	
(usersoft)		
	Fan Air-Filte	
	,	
	Check	
	Check	
	Check	

		,
Battery	2-3 (, Maker	
	.)	
()	5	Maker
Relay		,
		Maker
Fuse	10	

PLC 가 2 가 가 (, , ,) , , 가 (, ,) (, , ,) (, , ,)

3

NO			
1	Battery	1-2	3
			1-2
2	Fuse		Fuse ON/OFF

4

NO							
1		Unit		Relay	Unit	가	
	Unit		1				
2	CPU	1		PLC			
3	Memory	1				System	Down
4	Unit	1					

5 Data

NO			
1	Print		
		()	
2	Floppy Disk		Back-UP User

