In-EVM Mina State Verification

Technical Reference

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March 1, 2022

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Chapter 1

Introduction

This document is a technical reference to the in-EVM Mina state verification project.

1.1 Overview

The project's purpose is to provide Ethereum users with reliable Mina Protocol's state proof. The project UX consists of several steps:

- 1. Retrieve Mina Protocol's state proof.
- 2. Preprocess it by generating an auxiliary proof.
- 3. Submit the preprocessed proof to EVM-enabled cluster.
- 4. Verify the proof with EVM.

Such a UX defines projects parts:

- 1. Mina Protocol's state retriever (O(1) Labs' or Chainsafe's protocol implementation).
- 2. State proof generator.
- 3. Ethereum RPC proof submitter.
- 4. EVM-based proof verificator.

The overall architecture diagram is as follows:

Each of these parts will be considered independently.

Chapter 2

State Proof Generator

This introduces a description for Mina Protocol's state auxiliary proof generator. Crucial components which define this part design and performance are:

- 1. Input data format (Pickles proof data structure: 2.4.2)
- 2. Proof system used for the proof generation.
- 3. Circuit definition used for the proof system.

2.1 Introduction

WIP

To prove Mina blockchain's state on the Ethereum Virtual Machine, we use Redshift SNARK[1]. RedShift is a transparent SNARK that uses PLONK[2] proof system but replaces the commitment scheme. The authors utilize FRI[3] protocol to obtain transparency for the PLONK system.

However, FRI cannot be straightforwardly used with the PLONK system. To achieve the required security level without huge overheads, the authors introduce *list polynomial commitment* scheme as a part of the protocol. For more details, we refer the reader to [1].

The original RedShift protocol utilizes the classic PLONK[2] system. To provide better performance, we generilize the original protocol for use with PLONK with custom gates [4], [5] and lookup arguments [6], [7].

2.2 Optimizations

WIP

2.2.1 Batched FRI

Instead of checking each commitment individualy, it is possible to aggregate them for FRI. For polynomials f_0, \ldots, f_k :

- 1. Get θ from transcript
- 2. $f = f_0 \cdot \theta^{k-1} + \dots + f_k$
- 3. Run FRI over f, using oracles to f_0, \ldots, f_k

Thus, we can run only one FRI instance for all committed polynomials. See [1] for details.

2.2.2 Hash By Column

Instead of committing each of the polynomials, it is possible to use the same Merkle tree for several polynomials. This leads to the decrease of the number of Merkle tree paths which are required to be provided by the prover.

See [8], [1] for details.

2.2.3 Hash By Subset

Each i+1 FRI round supposes the prover to send all elements from a coset $H \in D^{(i)}$. Each Merkle leaf is able to contain the whole coset instead of separate values.

See [8] for details. Similar approach is described in [1]. However, the authors of [1] use more values per leaf, that leads to better performance.

2.3 RedShift Protocol

WIP

Notations:

$N_{\mathtt{wires}}$	Number of wires ('advice columns')
$N_{\mathtt{perm}}$	Number of wires that are included in the permutation argument
$N_{\mathtt{sel}}$	Number of selectors used in the circuit
$N_{\mathtt{const}}$	Number of constant columns
\mathbf{f}_i	Witness polynomials, $0 \le i < N_{\text{wires}}$
\mathbf{f}_{c_i}	Constant-related polynomials, $0 \le i < N_{\text{const}}$
\mathbf{gate}_i	Gate polynomials, $0 \le i < N_{\tt sel}$
$\sigma(\operatorname{col}:i,\operatorname{row}:j) = (\operatorname{col}:i',\operatorname{row}:j')$	Permutation over the table

For details on polynomial commitment scheme and polynomial evaluation scheme, we refer the reader to [1].

- 1. $\mathcal{L}' = (\mathbf{q}_0, ..., \mathbf{q}_{N_{\text{sel}}})$
- 2. Let ω be a 2^k root of unity
- 3. Let δ be a T root of unity, where $T \cdot 2^S + 1 = p$ with T odd and $k \leq S$
- 4. Compute N_{perm} permutation polynomials $S_{\sigma_i}(X)$ such that $S_{\sigma_i}(\omega^j) = \delta^{i'} \cdot \omega^{j'}$
- 5. Compute N_{perm} identity permutation polynomials: $S_{id_i}(X)$ such that $S_{id_i}(\omega^j) = \delta^i \cdot \omega^j$
- 6. Let $H = \{\omega^0, ..., \omega^n\}$ be a cyclic subgroup of \mathbb{F}^*
- 7. Let $Z(X) = \prod a \in H^*(X a)$

${\bf Preprocessing:}$

2.3.1 Prover View

1. Choose masking polynomials:

$$h_i(X) \leftarrow \mathbb{F}_{\leq k}[X] \text{ for } 0 \leq i < N_{\text{wires}}$$

Remark: For details on choice of k, we refer the reader to [1].

2. Define new witness polynomials:

$$f_i(X) = \mathbf{f}_i(X) + h_i(X)Z(X)$$
 for $0 < i < N_{\text{wires}}$

- 3. Add commitments to f_i to transcript
- 4. Get $\beta, \gamma \in \mathbb{F}$ from hash(transcript)
- 5. For $0 \le i < N_{\text{perm}}$

$$p_i = f_i + \beta \cdot S_{id_i} + \gamma$$
$$q_i = f_i + \beta \cdot S_{\sigma_i} + \gamma$$

6. Define:

$$\begin{aligned} p'(X) &= \prod_{0 \leq i < N_{\text{perm}}} p_i(X) \in \mathbb{F}_{< N_{\text{perm}} \cdot n}[X] \\ q'(X) &= \prod_{0 \leq i < N_{\text{perm}}} q_i(X) \in \mathbb{F}_{< N_{\text{perm}} \cdot n}[X] \end{aligned}$$

7. Compute $P(X), Q(X) \in \mathbb{F}_{< n+1}[X]$, such that:

$$P(\omega) = Q(\omega) = 1$$

$$P(\omega^{i}) = \prod_{1 \le j < i} p'(\omega^{i}) \text{ for } i \in 2, \dots, n+1$$

$$Q(\omega^{i}) = \prod_{1 \le j < i} q'(\omega^{i}) \text{ for } i \in 2, \dots, n+1$$

- 8. Compute commitments to P, Q and add them to transcript.
- 9. Get $\alpha_0, \ldots, \alpha_5 \in \mathbb{F}$ from hash(transcript)
- 10. Get τ from hash(transcript)
- 11. Define polynomials $(F_0, \ldots, F_4$ copy-satisfability, \mathtt{gate}_0 is PI-constraining gate)):

$$\begin{split} F_0(X) &= L_1(X)(P(X)-1) \\ F_1(X) &= L_1(X)(Q(X)-1) \\ F_2(X) &= P(X)p'(X) - P(X\omega) \\ F_3(X) &= Q(X)q'(X) - Q(X\omega) \\ F_4(X) &= L_n(X)(P(X\omega) - Q(X\omega)) \\ F_5(X) &= \sum_{0 \leq i < N_{\mathrm{sel}}} (\tau^i \cdot \mathbf{q}_i(X) \cdot \mathrm{gate}_i(X)) + PI(X) \end{split}$$

12. Compute:

$$F(X) = \sum_{i=0}^{5} \alpha_i F_i(X)$$
$$T(X) = \frac{F(X)}{Z(X)}$$

- 13. $N_T := \max(N_{\text{perm}}, \deg_{\text{gates}} 1)$, where \deg_{gates} is the highest degree of the degrees of gate polynomials.
- 14. Split T(X) into separate polynomials $T_0(X), ..., T_{N_T-1}(X)^1$
- 15. Add commitments to $T_0(X), ..., T_{N_T-1}(X)$ to transcript.
- 16. Get $y \in \mathbb{F}/H$ from hash(transcript)
- 17. Run evaluation scheme with the committed polynomials and y. Remark: Depending on the circuit, evaluation can be done also on $y\omega, y\omega^{-1}$.
- 18. The proof is $\pi_{\texttt{comm}}$ and $\pi_{\texttt{eval}}$, where:
 - $\bullet \quad \pi_{\texttt{comm}} = \{f_{0,\texttt{comm}}, \dots, f_{N_{\texttt{wires}}-1,\texttt{comm}}, P_{\texttt{comm}}, Q_{\texttt{comm}}, T_{0,\texttt{comm}}, \dots, T_{N_T-1,\texttt{comm}}\}$
 - $\pi_{\texttt{eval}}$ is evaluation proofs for $f_0(y), \ldots, f_{N_{\texttt{wires}}-1}(y), P(y), P(y\omega), Q(y), Q(y\omega), T_0(y), \ldots, T_{N_T-1}(y)$

 $^{^1\}mathrm{Commit}$ scheme supposes that polynomials should be degree $\leq n$

2.3.2 Verifier View

- 1. Let $f_{0,\text{comm}}, \ldots, f_{N_{\text{wires}}-1,\text{comm}}$ be commitments to $f_0(X), \ldots, f_{N_{\text{wires}}-1}(X)$
- 2. transcript = setup_values $||f_{0,\text{comm}}|| \dots ||f_{N_{\text{wires}}-1,\text{comm}}|$
- 3. $\beta, \gamma = hash(transcript)$
- 4. Let $P_{\text{comm}}, Q_{\text{comm}}$ be commitments to P(X), Q(X)
- 5. transcript = transcript $||P_{comm}||Q_{comm}|$
- 6. $\alpha_0, \ldots, \alpha_5 = hash(transcript)$
- 7. $\tau = hash(transcript)$
- 8. $N_T := \max(N_{perm}, \deg_{gates} 1)$, where \deg_{gates} is the highest degree of the degrees of gate polynomials.
- 9. Let $T_{0,\text{comm}},...,T_{N_T-1,\text{comm}}$ be commitments to $T_0(X),...,T_{N_T-1}(X)$
- 10. transcript = transcript $||T_{0,\text{comm}}||...||T_{N_T-1,\text{comm}}||$
- 11. $y = hash|_{\mathbb{F}/H}(\text{transcript})$
- 12. Run evaluation scheme verification with the committed polynomials and y to check values $f_i(y), P(y), P(y\omega), Q(y), Q(y\omega), T_i(y)$.

Remark: Depending on the circuit, evaluation can be done also on $f_i(y\omega), f_i(y\omega^{-1})$ for some i.

13. Calculate:

$$\begin{split} F_0(y) &= L_1(y)(P(y) - 1) \\ F_1(y) &= L_1(y)(Q(y) - 1) \\ p'(y) &= \prod p_i(y) = \prod f_i(y) + \beta \cdot S_{id_i}(y) + \gamma \\ F_2(y) &= P(y)p'(y) - P(y\omega) \\ q'(y) &= \prod q_i(y) = \prod f_i(y) + \beta \cdot S_{\sigma_i}(y) + \gamma \\ F_3(y) &= Q(y)q'(y) - Q(y\omega) \\ F_4(y) &= L_n(y)(P(y\omega) - Q(y\omega)) \\ F_5(y) &= \sum_{0 \leq i < N_{\text{sel}}} (\tau^i \cdot \mathbf{q}_i(y) \cdot \text{gate}_i(y)) + PI(y) \\ T(y) &= \sum_{0 \leq j < N_T} y^{n \cdot j} T_j(y) \end{split}$$

14. Check the identity:

$$\sum_{i=0}^{5} \alpha_i F_i(y) = Z(y) T(y)$$

2.4 Mina Verification Algorithm

WIP

2.4.1 Pasta Curves

Let $n_1 = 17$, $n_2 = 16$. Pasta curves parameters:

- $p = 2^{254} + 45560315531419706090280762371685220353$
- $q = 2^{254} + 45560315531506369815346746415080538113$
- Pallas:

$$\mathbb{G}_1 = \{(x, y) \in \mathbb{F}_p | y^2 = x^3 + 5\}$$

 $|\mathbb{G}_1| = q$

• Vesta:

$$\mathbb{G}_2 = \{(x, y) \in \mathbb{F}_q | y^2 = x^3 + 5\}$$

 $|\mathbb{G}_2| = p$

Verification Algorithm 2.4.2

Notations

$N_{\mathtt{wires}}$	Number of wires ('advice columns')
$N_{\mathtt{perm}}$	Number of wires that are included in the permutation argument
$N_{\mathtt{prev}}$	Number of previous challenges
$S_{\sigma_i}(\mathbf{X})$	Permutation polynomials for $0 \le i < N_{\text{perm}}$
pub(X)	Public input polynomial
$w_i(X)$	Witness polynomials for $0 \le i < N_{\tt wires}$
$\eta_i(X)$	Previous challenges polynomials for $0 \le i < N_{\tt prev}$
ω	<i>n</i> -th root of unity

Denote multi-scalar multiplication $\sum_{s_i \in \mathbf{s}, G_i \in \mathbf{G}} [s_i] G_i$ by $\mathtt{MSM}(\mathbf{s}, \mathbf{G})$ for $l_{\mathbf{s}} = l_{\mathbf{G}}$ where $l_{\mathbf{s}} = |\mathbf{s}|, l_{\mathbf{G}} = |\mathbf{G}|$. If $l_{\mathbf{s}} < l_{\mathbf{G}}$, then we use only first $l_{\mathbf{s}}$ elements of \mathbf{G}

Proof π constains (here \mathbb{F}_r is a scalar field of \mathbb{G}):

- Commitments:
 - Witness polynomials: $w_{0,\text{comm}},...,w_{N_{\text{wires}},\text{comm}} \in \mathbb{G}$
 - Permutation polynomial: $z_{\texttt{comm}} \in \mathbb{G}$
 - $\bullet \ \ \text{Quotinent polynomial:} \ t_{\texttt{comm}} = (t_{1,\texttt{comm}}, t_{2,\texttt{comm}}, ..., t_{N_{\texttt{perm}},\texttt{comm}}) \in (\mathbb{G}^{N_{\texttt{perm}}} \times \mathbb{G})$
- Evaluations:
 - $w_0(\zeta), ..., w_{N_{\text{wires}}}(\zeta) \in \mathbb{F}_r$
 - $w_0(\zeta\omega), ..., w_{N_{\text{wires}}}(\zeta\omega) \in \mathbb{F}_r$
 - $z(\zeta), z(\zeta\omega) \in \mathbb{F}_r$

 - $$\begin{split} \bullet \ S_{\sigma_0}(\zeta),...,S_{\sigma_{N_{\mathrm{perm}}}}(\zeta) \in \mathbb{F}_r \\ \bullet \ S_{\sigma_0}(\zeta\omega),...,S_{\sigma_{N_{\mathrm{perm}}}}(\zeta\omega) \in \mathbb{F}_r \end{split}$$
 - $\bar{L}(\zeta\omega) \in \mathbb{F}_r^2$
- Opening proof o_{π} for inner product argument:
 - $(L_i, R_i) \in \mathbb{G} \times \mathbb{G}$ for $0 \leq i < lr_rounds$
 - $\delta, \hat{G} \in \mathbb{G}$
 - $z_1, z_2 \in \mathbb{F}_r$
- previous challenges:
 - $\{\eta_i(\xi_j)\}_j, \eta_{i,\text{comm}}, \text{ for } 0 \leq i < \text{prev}$

Remark: For simplicity, we do not use distinct proofs index i for each element in the algorithm below. For instance, we write pub_{comm} instead of $pub_{i,comm}$.

²See https://o1-labs.github.io/mina-book/crypto/plonk/maller_15.html

Algorithm 1 Verification

```
Input: \pi_0, \ldots, \pi_{\mathtt{batch\_size}} (see 2.4.2)
 Output: acc or rej
             1. for each \pi_i:
                              1.1 pub_{comm} = MSM(\mathbf{L}, pub) \in \mathbb{G}, where L is Lagrange bases vector
                              1.2 random_oracle(p_{\text{comm}}, \pi_i):
                                          1.2.1 H_{\mathbb{F}_q}.absorb(pub_{\mathtt{comm}}||w_{0,\mathtt{comm}}||...||w_{N_{\mathtt{wires}},\mathtt{comm}})
                                          1.2.2 \ \beta, \gamma = H_{\mathbb{F}_q}.\mathtt{squeeze}()
                                         1.2.3 H_{\mathbb{F}_a}.absorb(z_{\text{comm}})
                                          1.2.4 \alpha = \phi(H_{\mathbb{F}_q}.\mathtt{squeeze}())
                                          1.2.5~H_{\mathbb{F}_q}.\mathtt{absorb}(t_{1,\mathtt{comm}}||...||t_{N_{\mathtt{perm}},\mathtt{comm}}||...||\infty||)
                                         1.2.6 \zeta = \phi(H_{\mathbb{F}_q}.\mathtt{squeeze}())
                                         1.2.7 Transfrorm H_{\mathbb{F}_q} to H_{\mathbb{F}_r}
                                         1.2.8~H_{\mathbb{F}_r}.\mathtt{absorb}(pub(\zeta)||w_0(\zeta)||...||w_{N_{\mathrm{wires}}}(\zeta)||S_0(\zeta)||...||S_{N_{\mathrm{perm}}}(\zeta))
                                         1.2.9 \ H_{\mathbb{F}_r}.\mathtt{absorb}(pub(\zeta\omega)||w_0(\zeta\omega)||...||w_{N_{\mathrm{wires}}}(\zeta\omega)||S_0(\zeta\omega)||...||S_{N_{\mathrm{perm}}}(\zeta\omega))
                                    1.2.10~H_{\mathbb{F}_r}.absorb(\bar{L}(\zeta\omega))
                                    1.2.11 v = \phi(H_{\mathbb{F}_r}.\mathtt{squeeze}())
                                    1.2.12 u = \phi(H_{\mathbb{F}_n}.\mathtt{squeeze}())
                                    1.2.13 Compute evaluation of \eta_i(\zeta), \eta_i(\zeta\omega) for 0 \le i < N_{\text{prev}}
                                    1.2.14 Compute evaluation of L(\zeta)
                              1.3 \ \mathbf{f}_{\mathrm{base}} \coloneqq \{S_{\sigma_{N_{\mathtt{perm}}-1},\mathtt{comm}}, \mathtt{gate}_{\mathrm{mult},\mathtt{comm}}, w_{0,\mathtt{comm}}, w_{1,\mathtt{comm}}, w_{2,\mathtt{comm}}, q_{\mathtt{const},\mathtt{comm}}, \mathtt{gate}_{\mathrm{psdn},\mathtt{comm}}, \mathtt{gate}_{\mathrm{rc},\mathtt{comm}}, \mathtt{gate}_{\mathrm{rc},\mathtt{comm}}, \mathtt{gate}_{\mathrm{psdn},\mathtt{comm}}, \mathtt{gate}_{\mathrm{rc},\mathtt{comm}}, \mathtt{gate}_{\mathrm{rc},\mathtt
                                                \texttt{gate}_{\texttt{ec\_add},\texttt{comm}}, \texttt{gate}_{\texttt{ec\_dbl},\texttt{comm}}, \texttt{gate}_{\texttt{ec\_endo},\texttt{comm}}, \texttt{gate}_{\texttt{ec\_vbase},\texttt{comm}} \}
                              1.4 s_{\text{perm}} := (w_0(\zeta) + \gamma + \beta \cdot S_{\sigma_0}(\zeta)) \cdot \dots \cdot (w_5(\zeta) + \gamma + \beta \cdot S_{\sigma_{N_{\text{near}}}}(\zeta))
                              1.5 \ \mathbf{f}_{\text{scalars}} \coloneqq \{ -z(\zeta\omega) \cdot \beta \cdot \alpha_0 \cdot zkp(\zeta) \cdot s_{\text{perm}}, w_0(\zeta) \cdot w_1(\zeta), w_0(\zeta), w_1(\zeta), 1 \}
                                                s_{\text{psdn}}, s_{\text{rc}}, s_{\text{ec\_add}}, s_{\text{ec\_dbl}}, s_{\text{ec\_endo}}, s_{\text{ec\_vbase}}
                              1.6 f_{\text{comm}} = \text{MSM}(\mathbf{f}_{\text{base}}, \mathbf{f}_{\text{scalars}})
                              1.7 \bar{L}_{\text{comm}} = f_{\text{comm}} - t_{\text{comm}} \cdot (\zeta^n - 1)
                              1.8 PE is a set of elements of the form (f_{\texttt{comm}}, f(\zeta), f(\zeta\omega)) for the following polynomials:
                                                \eta_0, \ldots, \eta_{N_{\text{prev}}}, pub, w_0, \ldots, w_{N_{\text{wires}}}, z, S_{\sigma_0}, \ldots, S_{\sigma_{N_{\text{perm}}}}, L
                              1.9 \mathcal{P}_i = \{H_{\mathbb{F}_q}, \zeta, v, u, \mathbf{PE}, o_{\pi_i}\}
             2. final_check(\mathcal{P}_0, \dots, \mathcal{P}_{\mathtt{batch\_size}})
```

Algorithm 2 Final Check

Input: $\pi_0, \dots, \pi_{\mathtt{batch_size}}$, where $\pi_i = \{H_{i,\mathbb{F}_q}, \zeta_i, \zeta_i\omega, v_i, u_i, \mathbf{PE}_i, o_{\pi_i}\}$

Output: acc or rej

- 1. $\rho_1 \to \mathbb{F}_r$
- 2. $\rho_2 \to \mathbb{F}_r$
- 3. $r_0 = r'_0 = 1$
- 4. for $0 \le i < \mathtt{batch_size}$:
 - 4.1 $cip_i = \texttt{combined_inner_product}(\zeta_i, \zeta_i\omega, v_i, u_i, \mathbf{PE}_i)$
 - $4.2~H_{i,\mathbb{F}_a}$.absorb (cip_i-2^{255})
 - 4.3 $U_i = (H_{i,\mathbb{F}_q}.\mathtt{squeeze}()).\mathtt{to_group}()$
 - 4.4 Calculate opening challenges $\xi_{i,j}$ from o_{π_i}
 - 4.5 $h_i(X) := \prod_{k=0}^{\log(d+1)-1} (1 + \xi_{\log(d+1)-k} X^{2^k})$, where $d = \text{lr_rounds}$
 - $4.6 \ b_i = h_i(\zeta) + u_i \cdot h_i(\zeta\omega)$
 - 4.7 $C_i = \sum_j v_i^j (\sum_k r_i^k f_{j,\text{comm}})$, where $f_{j,\text{comm}}$ from \mathbf{PE}_i .
 - 4.8 $Q_i = \sum (\xi_{i,j} \cdot L_{i,j} + \xi_{i,j}^{-1} \cdot R_j) + cip_i \cdot U_i + C_i$
 - $4.9 \ c_i = \phi(H_{i,\mathbb{F}_q}.\mathtt{squeeze}())$
 - 4.10 $r_i = r_{i-1} \cdot \rho_1$
 - 4.11 $r'_i = r'_{i-1} \cdot \rho_2$
 - 4.12 Check $\hat{G}_i = \langle s, G \rangle$, where s is set of h(X) coefficients.

Remark: This check can be done inside the MSM below using r'_i .

5.
$$res = \sum_{i} r^{i} (c_{i}Q_{i} + delta_{i} - (z_{i,1}(\hat{G}_{i} + b_{i}U_{i}) + z_{i,2}H))$$

6. return res == 0

Algorithm 3 Combined Inner Product

Input: $\xi, r, f_0(\zeta_1), \dots, f_k(\zeta_1), f_0(\zeta_2), \dots, f_k(\zeta_2)$

Output: s

1.
$$s = \sum_{i=0}^{k} \xi^{i} \cdot (f_{i}(\zeta_{1}) + r \cdot f_{i}(\zeta_{2}))$$

We use the same 15-wires PLONK circuits that are designed for Mina.³

2.5 Elliptic Curve Arithmetic

WIP

2.5.1 Unified Incomplete Addition and Doubling

Row 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14
$$i$$
 x_1 y_1 x_2 y_2 x_3 y_3 inf same_x s inv_y inv_x

Evaluations:

³https://o1-labs.github.io/mina-book/specs/15_wires/15_wires.html

- Addition case:
 - $(x_3, y_3) = (x_1, y_1) + (x_2, y_2)$
 - $\inf = 1$ if (x_3, y_3) is a point-at-infinity, $\inf = 0$ otherwise
 - same_x = 1 if $x_1 = x_2$, same_x = 0 otherwise

 - $s=\frac{y_1-y_2}{x_1-x_2}$ if $x_1\neq x_2, s=0$ otherwise $\operatorname{inv}_y=\frac{1}{y_2-y_1}$ if $y_2\neq y_1, \operatorname{inv}_y=0$ otherwise
 - $\operatorname{inv}_x = \frac{1}{x_2 x_1}$ if $x_2 \neq x_1$, $\operatorname{inv}_x = 0$ otherwise
- Doubling case:
 - $(x_3, y_3) = 2(x_1, y_1)$
 - $x_2 = x_1, y_2 = y_1$
 - inf = 1 if (x_3, y_3) is a point-at-infinity, inf = 0 otherwise

 - $s=\frac{3x_1^2}{2y_1}$ if $y_1\neq 0,\, s=0$ otherwise $\mathrm{inv}_y=0$

 - $inv_x = 0$

Constraints ($\max degree = 3$):

- 1. $w_7 \cdot (w_2 w_0) = 0$
- 2. $(w_2 w_0) \cdot w_{10} (1 w_7) = 0$
- 3. $w_7 \cdot (2w_8 \cdot w_1 3w_0^2) + (1 w_7) \cdot (w_2 w_0 \cdot w_8 (w_3 w_1))$
- 4. $w_8^2 = w_0 + w_2 + w_4$
- 5. $w_5 = w_8 \cdot (w_0 w_4) w_1$
- 6. $(w_3 w_1) \cdot (w_7 w_6) = 0$
- 7. $(w_3 w_1) \cdot w_9 w_6 = 0$

Copy constraints:

1. $w_6 = 0$

Details. The gate uses basic group law formulae. Let $P = (x_1, y_1), Q = (x_2, y_2), R = (x_3, y_3)$ and R = P + Q. Then:

- $(x_2 x_1) \cdot s = y_2 y_1$
- $s^2 = x_1 + x_2 + x_3$
- $y_3 = s \cdot (x_1 x_3) y_1$

For point doubling R = P + P = 2P:

- $2s \cdot y_1 = 3x_1^2$
- $s^2 = 2x_1 + x_3$
- $y_3 = s \cdot (x_1 x_3) y_1$

The gate does not handle cases $\mathcal{O} + P$ or $\mathcal{O} + \mathcal{O}$. To ensure that operations with point-at-infinity are not included in the circuit's trace, copy constraint $w_6 = 0$ (inf = 0) was introduced.

Constraints details:

- $x_2 x_1$ zero check:
 - 1. $w_7 \cdot (w_2 w_0) = 0 \longleftrightarrow \mathtt{same}_{\mathtt{x}} \cdot (x_2 x_1)$ If $x_1 \neq x_2$, then same x = 0
 - $2. \ (w_2-w_0)\cdot w_{10}-(1-w_7)=0 \longleftrightarrow (x_2-x_1)\cdot \mathtt{inv}_x-(1-\mathtt{same_x})$ If $x_1 \neq x_2$, then $inv_x = (x_2 - x_1)^{-1}$
- Group law constraints:
 - 1. $w_7 \cdot (2w_8 \cdot w_1 3w_0^2) + (1 w_7) \cdot ((w_2 w_0 \cdot w_8 (w_3 w_1)) \longleftrightarrow$ $same_x \cdot (2s \cdot y_1 - 3x_1^2) + (1 - same_x) \cdot (x_2 - x_1 \cdot s - (y_2 - y_1))$ If $x_1 = x_2$ then use doubling $2s \cdot y_1 = 3x_1^2$. Otherwise use addition $(x_2 - x_1) \cdot s = y_2 - y_1$.

- 2. $w_8^2 = w_0 + w_2 + w_4 \longleftrightarrow s^2 = x_1 + x_2 + x_3$ Constrains x_3 . It does not depend on x_1, x_2 equality.
- 3. $w_5 = w_8 \cdot (w_0 w_4) w_1 \longleftrightarrow y_3 = s \cdot (x_1 x_3) y_1$ Constrains y_3 . It does not depend on x_1, x_2 equality.
- P + (-P) constraints:
 - 1. $(w_3 w_1) \cdot (w_7 w_6) = 0 \longleftrightarrow (y_2 y_1) \cdot (\mathtt{same}_x \mathtt{inf}) = 0$ We can get inifinity point iff $x_1 = x_2$ and $y_1 \neq y_2$. If $y_1 \neq y_2$ then $inf = same_x$.
 - 2. $(w_3 w_1) \cdot w_9 w_6 = 0 \longleftrightarrow (y_2 y_1) \cdot \operatorname{inv}_y \inf$ The prover sets $inv_y = 0$ for $y_1 = y_2$. If $y_1 \neq y_2$ then $inv_y = (y_2 - y_1)^{-1}$

Variable Base Scalar Multiplication 2.5.2

For R = [r]T, where $r = 2^n + k$ and $k = [k_n...k_0], k_i \in \{0, 1\}$:

- 1. P = [2]T
- 2. for i from n-1 to 0:

$$2.1 \ Q = k_{i+1} ? T : -T$$

$$2.2 R = P + Q + P$$

3.
$$R = k_0 ? R - T : R$$

The first and last steps of the alforithm are verified by the unified addition and doubling circuit.

Two gates are used in the circuit. Call them VBSM₁ and VBSM₂. VBSM₁ is applied to even rows and VBSM₂ is used with odd rows. Each two rows perform calculations with five bits of the scalar.

Evaluations:

- b_i are bits of the k, first b_1 is the most significant bit of k, n is an accumulator of b_i .
- $(x_1, y_1) (x_0, y_0) = (x_0, y_0) + (x_T, (2b_1 1)y_T)$
- $(x_2, y_2) (x_1, y_1) = (x_1, y_1) + (x_T, (2b_1 1)y_T)$
- $(x_3, y_3) (x_2, y_2) = (x_2, y_2) + (x_T, (2b_1 1)y_T)$
- $(x_4, y_4) (x_3, y_3) = (x_3, y_3) + (x_T, (2b_1 1)y_T)$
- $(x_5, y_5) (x_4, y_4) = (x_4, y_4) + (x_T, (2b_1 1)y_T)$ $s_0 = \frac{y_0 (2b_0 1) \cdot y_T}{x_0 x_T}$
- $s_1 = \frac{y_1 (2b_1 1) \cdot y_T}{x_0 x_T}$
- $s_2 = \frac{x_1 x_T}{y_2 (2b_2 1) \cdot y_T}$
- $= \frac{y_3 (2b_3 1) \cdot y_T}{2b_3 (2b_3 1) \cdot y_T}$
- $s_4 = \frac{y_4 (2b_4 1) \cdot y_T}{2b_4 1}$

Constraints:

- $next(w_2) \cdot (w_2 1) = 0$
- $next(w_3) \cdot (w_3 1) = 0$

⁴Using the results from https://arxiv.org/pdf/math/0208038.pdf

```
• next(w_4) \cdot (w_4 - 1) = 0
```

- $next(w_5) \cdot (w_5 1) = 0$
- $next(w_6) \cdot (w_6 1) = 0$
- $(w_2 w_0) \cdot \text{next}(w_7) = w_3 (2\text{next}(w_2) 1) \cdot w_1$
- $(w_7 w_0) \cdot \text{next}(w_8) = w_8 (2\text{next}(w_3) 1) \cdot w_1$
- $(w_{10} w_0) \cdot \text{next}(w_9) = w_{11} (2\text{next}(w_4) 1) \cdot w_1$
- $(w_{12} w_0) \cdot \text{next}(w_{10}) = w_{13} (2\text{next}(w_5) 1) \cdot w_1$
- $(\text{next}(w_0) w_0) \cdot \text{next}(w_{11}) = \text{next}(w_1) (2\text{next}(w_6) 1) \cdot w_1$
- $(2 \cdot w_3 \text{next}(w_7) \cdot (2 \cdot w_2 \text{next}(w_7)^2 + w_0))^2 = (2 \cdot w_2 \text{next}(w_7)^2 + w_0)^2 \cdot (w_7 w_0 + \text{next}(w_7)^2)$
- $(2 \cdot w_8 \text{next}(w_8) \cdot (2 \cdot w_7 \text{next}(w_8)^2 + w_0))^2 = (2 \cdot w_7 \text{next}(w_8)^2 + w_0)^2 \cdot (w_9 w_0 + \text{next}(w_8)^2)$
- $(2 \cdot w_{10} \text{next}(w_9) \cdot (2 \cdot w_9 \text{next}(w_9)^2 + w_0))^2 = (2 \cdot w_9 \text{next}(w_9)^2 + w_0)^2 \cdot (w_{11} w_0 + \text{next}(w_9)^2)$
- $(2 \cdot w_{12} \text{next}(w_{10}) \cdot (2 \cdot w_{11} \text{next}(w_{10})^2 + w_0))^2 = (2 \cdot w_{11} \text{next}(w_{10})^2 + w_0)^2 \cdot (w_{13} w_0 + \text{next}(w_{10})^2)$
- $(2 \cdot w_{14} \text{next}(w_{11}) \cdot (2 \cdot w_{13} \text{next}(w_{11})^2 + w_0))^2 = (2 \cdot w_{13} \text{next}(w_{11})^2 + w_0)^2 \cdot (\text{next}(w_0) w_0)^2$ $w_0 + \text{next}(w_{11})^2$
- $(w_8 + w_3) \cdot (2 \cdot w_2 \text{next}(w_7)^2 + w_0) = (w_2 w_7) \cdot (2 \cdot w_3 \text{next}(w_7) \cdot (2 \cdot w_2 \text{next}(w_7)^2 + w_0))$
- $(w_{10} + w_8) \cdot (2 \cdot w_7 \text{next}(w_8)^2 + w_0) = (w_7 w_9) \cdot (2 \cdot w_8 \text{next}(w_8) \cdot (2 \cdot w_7 \text{next}(w_8)^2 + w_0))$
- $(w_{12} + w_{10}) \cdot (2 \cdot w_9 \text{next}(w_9)^2 + w_0) = (w_9 w_{11}) \cdot (2 \cdot w_{10} \text{next}(w_9) \cdot (2 \cdot w_9 \text{next}(w_9)^2 + w_0))$ $(w_{14} + w_{10}) \cdot (2 \cdot w_{11} \text{next}(w_{10})^2 + w_0) = (w_{11} w_{13}) \cdot (2 \cdot w_{12} \text{next}(w_{10}) \cdot (2 \cdot w_{11} \text{next}(w_{10})^2 + w_0))$
- $(\text{next}(w_1) + w_{14}) \cdot (2 \cdot w_{13} \text{next}(w_{11})^2 + w_0) = (w_{13} \text{next}(w_0) \cdot (2 \cdot w_{14} \text{next}(w_{11}) \cdot (2 \cdot w_{13} \text{next}(w_{11})$ $next(w_{11})^2 + w_0)$
- $w_5 = 32 \cdot (w_4) + 16 \cdot \text{next}(w_2) + 8 \cdot \text{next}(w_3) + 4 \cdot \text{next}(w_4) + 2 \cdot \text{next}(w_5) + \text{next}(w_6)$

Copy constraints:

- (x_T, y_T) in row j are copy constrained with (x_T, y_T) in row j + 2
- (x_0, y_0) in row i are copy constrained with values from the first doubling circuit
- (x_0, y_0) in row $j, j \neq i$ are copy constrained with (x_5, y_5) in row j-1
- n=0 in row i and n in the row $j, j \neq i$ is copy contrained with n' in the row j-2

2.5.3Variable Base Endo-Scalar Multiplication

For R = [b]T, where $b = [b_n...b_0]$ and $b_i \in \{0, 1\}$: ⁵

1.
$$P = [2](\phi(T) + T)$$

2. for i from $\frac{\lambda}{2} - 1$ to 0:

2.1
$$Q = r_{2i+1} ? \phi([2r_{2i} - 1]T) : [2r_{2i} - 1]T$$

$$2.2 R - P = P + Q$$

The first step of the alforithm are verified by the doubling and unified addition circuit.

Evaluations:

- The first x_P, y_P are equal to $2 \cdot ((x_T, y_T) + ((\text{endo}) \cdot x_T, y_T))$
- b_i are bits of the k, first b_1 is the most significant bit of k, n is an accumulator of b_i .

⁵Using the results from https://eprint.iacr.org/2019/1021.pdf

```
 \begin{array}{l} \bullet \quad (x_R,y_R) - (x_P,y_P) = (x_P,y_P) + (1 + (\mathtt{endo} - 1) \cdot b_2) x_T, (2b_1 - 1) y_T) \\ \bullet \quad (\mathtt{next}(x_P),\mathtt{next}(y_P)) - (x_R,y_R) = (x_R,y_R) + ((\mathtt{endo} - 1) \cdot b_2) x_T, (2b_1 - 1) y_T) \\ \bullet \quad s_1 = \frac{(2b_1 - 1) \cdot y_T - y_P}{(1 + (\mathtt{endo} - 1) \cdot b_2) x_T - x_P} \\ \bullet \quad s_3 = \frac{(2b_1 - 1) \cdot y_T - y_R}{(1 + (\mathtt{endo} - 1) \cdot b_2) x_T - x_R} \end{array}
```

Constraints:

- $w_{11} \cdot (w_{11} 1) = 0$
- $w_{12} \cdot (w_{12} 1) = 0$
- $w_{13} \cdot (w_{13} 1) = 0$
- $w_{14} \cdot (w_{14} 1) = 0$
- $((1 + (\text{endo} 1) \cdot w_{12}) \cdot w_0 w_4) \cdot w_9 = (2 \cdot w_{11} 1) \cdot w_1 w_5$
- $\bullet \quad (2 \cdot w_4 w_9^2 + (1 + (\texttt{endo} 1) \cdot w_{12}) \cdot w_0) \cdot ((w_4 w_7) \cdot w_9 + w_8 + w_5) = (w_4 w_7) \cdot 2 \cdot w_5$
- $(w_8 + w_5)^2 = (w_4 w_7)^2 \cdot (w_9^2 (1 + (endo 1) \cdot w_{12}) \cdot w_0 + w_7)$
- $((1 + (\mathtt{endo} 1) \cdot w_{12}) \cdot w_0 w_7) \cdot w_{10} = (2 \cdot w_{13} 1) \cdot w_1 w_8$
- $\bullet \quad (2 \cdot w_7 w_{10}^2 + (1 + (\texttt{endo} 1) \cdot w_{14}) \cdot w_0) \cdot ((w_7 \texttt{next}(w_4)) \cdot w_{10} + \texttt{next}(w_5) + w_8) = (w_7 \texttt{next}(w_4)) \cdot 2 \cdot w_8 + (w_7 w_{10}) \cdot 2 \cdot w_8 + (w$
- $(\text{next}(w_4) + w_8)^2 = (w_7 \text{next}(w_4))^2 \cdot (w_{10}^2 (1 + (\text{endo} 1) \cdot w_{14}) \cdot w_0 + \text{next}(w_4))$
- $next(w_6) = 16 \cdot w_6 + 8 \cdot w_{11} + 4 \cdot w_{12} + 2 \cdot w_{13} + w_{14}$

Copy constraints:

- (x_T, y_T) in row j are copy constrained with (x_T, y_T) in row j + 1
- (x_P, y_P) in row i are copy constrained with values from the first doubling circuit

2.5.4 Fixed-base scalar multiplication circuit

We precompute all values $w(B, s, k) = (k_i + 2) \cdot 8^s B$, where $k_i \in \{0, ...7\}$, $s \in \{0, ..., 83\}$ and $w(B, s, k) = (k_i \cdot 8^s - \sum_{j=0}^{84} 8^{j+1}) \cdot B$, where $k_i \in \{0, ...7\}$, s = 84.

Define the following functions:

- $\begin{array}{c} 1. \ \phi_1: (x_1, x_2, x_3, x_4) \mapsto \\ x_3 \cdot (-u_0' \cdot x_2 \cdot x_1 + u_0' \cdot x_1 + u_0' \cdot x_2 u_0' + u_2' \cdot x_1 \cdot x_2 u_2' \cdot x_2 + u_4' \cdot x_1 \cdot x_2 u_4' \cdot x_2 u_6' \cdot x_1 \cdot x_2 + u_1' \cdot x_2 \cdot x_1 u_1' \cdot x_1 u_1' \cdot x_2 + u_1' u_3' \cdot x_1 \cdot x_2 + u_3' \cdot x_2 u_5' \cdot x_1 \cdot x_2 + u_5' \cdot x_2 + u_7' \cdot x_1 \cdot x_2) (x_4 u_0' \cdot x_2 \cdot x_1 + u_0' \cdot x_1 + u_0' \cdot x_2 u_0' + u_2' \cdot x_1 \cdot x_2 u_2' \cdot x_2 + u_4' \cdot x_1 \cdot x_2 u_4' \cdot x_2 u_6' \cdot x_1 \cdot x_2) \end{array}$
- $2. \ \phi_2: (x_1, x_2, x_3, x_4) \mapsto \\ x_3 \cdot (-v_0' \cdot x_2 \cdot x_1 + v_0' \cdot x_1 + v_0' \cdot x_2 v_0' + v_2' \cdot x_1 \cdot x_2 v_2' \cdot x_2 + v_4' \cdot x_1 \cdot x_2 v_4' \cdot x_2 v_6' \cdot x_1 \cdot x_2 + v_1' \cdot x_2 \cdot x_1 v_1' \cdot x_1 v_1' \cdot x_2 + v_1' v_3' \cdot x_1 \cdot x_2 + v_3' \cdot x_2 v_5' \cdot x_1 \cdot x_2 + v_5' \cdot x_2 + v_7' \cdot x_1 \cdot x_2) (x_4 v_0' \cdot x_2 \cdot x_1 + v_0' \cdot x_1 + v_0' \cdot x_2 v_0' + v_2' \cdot x_1 \cdot x_2 v_2' \cdot x_2 + v_4' \cdot x_1 \cdot x_2 v_4' \cdot x_2 v_6' \cdot x_1 \cdot x_2)$

Constraints:

- For i + 0:
 - $b_i \cdot (b_i 1) = 0$, where $i \in \{0, ..., 5\}$
 - $\phi_1(b_0, b_1, b_2, u_0) = 0$, where $(u_i', v_i') = w(B, 0, i)$
 - $\phi_1(b_3, b_4, b_5, u_1) = 0$, where $(u_i', v_i') = w(B, 1, i)$
 - $\phi_2(b_0, b_1, b_2, v_0) = 0$, where $(u_i', v_i') = w(B, 0, i)$
 - $\phi_2(b_3, b_4, b_5, v_1) = 0$, where $(u_i', v_i') = w(B, 1, i)$
 - $acc = b_0 + b_1 \cdot 2 + b_2 \cdot 2^2 + b_3 \cdot 2^3 + b_4 \cdot 2^4 + b_5 \cdot 2^5$
 - $(x_1, y_1) = (u_0, v_0)$
 - $(x_2, y_2) = (x_1, y_1) + (u_1, v_1)$ incomplete addition, where $x_1 \neq u_1$
- For $i + z, z \in 1, ..., 41$:

- $b_i \cdot (b_i 1) = 0$, where $i \in \{0, ..., 5\}$
- $\phi_1(b_0, b_1, b_2, u_0) = 0$, where $(u'_i, v'_i) = w(B, z \cdot 2, i)$
- $\phi_1(b_3, b_4, b_5, u_1) = 0$, where $(u'_i, v'_i) = w(B, z \cdot 2 + 1, i)$
- $\phi_2(b_0, b_1, b_2, v_0) = 0$, where $(u_i', v_i') = w(B, z \cdot 2, i)$
- $\phi_2(b_3, b_4, b_5, v_1) = 0$, where $(u_i', v_i') = w(B, z \cdot 2 + 1, i)$
- $acc = b_0 + b_1 \cdot 2 + b_2 \cdot 2^2 + b_3 \cdot 2^3 + b_4 \cdot 2^4 + b_5 \cdot 2^5 + acc_{prev} \cdot 2^6$
- $(x_1, y_1) = (u_0, v_0) + (x_2, y_2)_{prev}$ incomplete addition, where $u_0 \neq x_2$
- $(x_2, y_2) = (x_1, y_1) + (u_1, v_1)$ incomplete addition, where $x_1 \neq u_1$
- For i + 42:
 - $b_i \cdot (b_i 1) = 0$, where $i \in \{0, ..., 2\}$
 - $\phi_1(b_0, b_1, b_2, u_0) = 0$, where $(u_i', v_i') = w(B, 84, i)$
 - $\phi_2(b_0, b_1, b_2, v_0) = 0$, where $(u'_i, v'_i) = w(B, 84, i)$
 - $b = b_0 + b_1 \cdot 2 + b_2 \cdot 2^2 + acc_{prev} \cdot 2^3$
 - $(x_w, y_w) = (u_0, v_0) + (x_2, y_2)_{prev}$ complete addition from Orchard

2.6 Multi-Scalar Multiplication Circuit

WIP

Input: $G_0, ..., G_{k-1} \in \mathbb{G}, s_0, ..., s_{k-1} \in \mathbb{F}_r$, where \mathbb{F}_r is scalar field of \mathbb{G} . Output: $S = \sum_{i=0}^k s_i \cdot G_i$

2.6.1 Naive Algorithm

Using endomorphism:

- 1. $A = \infty$
- 2. for j from 0 to k-1:

$$2.1 \ r \coloneqq s_i, T \coloneqq G_i$$

$$2.2 S = [2](\phi(T) + T)$$

2.3 for i from $\frac{\lambda}{2} - 1$ to 0:

2.3.1
$$Q = r_{2i+1} ? \phi([2r_{2i} - 1]T) : [2r_{2i} - 1]T$$

$$2.3.2 R = S + Q$$

$$2.3.3 \ S = R + S$$

$$2.4 \ A = A + S$$

rows
$$\approx k \cdot (sm_rows + 1 + 2) \approx 67k$$
,

where sm rows is the number of rows in the scalar multiplication circuit.

Without endomorphism:

- 1. $A = \infty$
- 2. for j from 0 to k-1:

$$2.1 \ r \coloneqq s_i, T \coloneqq G_i$$

$$2.2 S = [2]T$$

2.3 for i from n-1 to 0:

$$2.3.1 \ Q = k_{i+1} ? T : -T$$

$$2.3.2 R = S + Q$$

$$2.3.3 S = R + S$$

$$2.4 \ S = k_0 ? S - T : S$$

$$2.5 \ A = A + S$$

$$rows \approx k \cdot (sm_rows + 1 + 1) \approx 105k$$
,

where sm_rows is the number of rows in the scalar multiplication circuit.

2.6.2 Simultaneous Doubling

Remark: Simultaneous doubling incurs a negligible completeness error for independently chosen random terms of the sum.

Using endomorphism:

1.
$$A = \sum_{j=0}^{k} [2](\phi(G_j) + G_j)$$

2. for
$$i$$
 from $\frac{\lambda}{2} - 1$ to 0:

2.1 for
$$j$$
 from 0 to $k-1$:

$$2.1.1 \ r := s_i, T := G_i$$

2.1.2
$$Q = r_{2i+1} ? \phi([2r_{2i} - 1]T) : [2r_{2i} - 1]T$$

$$2.1.3 \ A = A + Q$$

2.2 if $i \neq 0$:

$$2.2.1 \ A = 2 \cdot A$$

$$\text{rows} \approx \tfrac{\lambda}{2} \cdot (k \cdot \texttt{add_rows} + \texttt{dbl_rows}) + 2k \approx 64 \cdot (k+1) \approx 66k + 64,$$

where

- add_rows is the number of rows in the addition circuit.
- dbl_rows is the number of rows in the doubling circuit.

Without endomorphism:

1.
$$A = \sum_{j=0}^{k} [2]G_j$$

2. for i from n-1 to 0:

2.1 for
$$j$$
 from 0 to $k-1$:

$$2.1.1 \ r \coloneqq s_j, T \coloneqq G_j$$

$$2.1.2 \ Q = k_{i+1} ? T : -T$$

$$2.1.3 \ A = A + Q$$

2.2 if $i \neq 0$:

$$2.2.1 \ A = 2 \cdot A$$

3.
$$A = A + \sum_{j=0}^{k} [1 - s_{j,0}]G_j$$

rows
$$\approx \frac{2}{5}n \cdot (k \cdot add_rows + dbl_rows) + k \approx 103 \cdot (k+1) + 2k \approx 104k + 103$$
,

where

- add_rows is the number of rows in the addition circuit.
- dbl_rows is the number of rows in the doubling circuit.

2.7 Poseidon Circuit

WIP

Mina uses Poseidon hash with width = 3. Therefore, each permutation state is represented by 3 elements and each row contains 5 states.

Denote *i*-th permutation state by $T_i = (T_{i,0}, T_{i,1}, T_{i,2})$.

```
6
                                                                                                                  10
Row
                                                    4
                                                                                                                                                           14
                                T_{0,2}
                                                    T_{4,1}
                                                              T_{4,2}
                                                                         T_{1,0}
                                                                                   T_{1,1}
                                                                                             T_{1,2}
                                                                                                       T_{2,0}
                                                                                                                 T_{2,1}
           T_{0,0}
                      T_{0,1}
                                          T_{4,0}
                                                                                                                                                           T_{3,2}
                                                   T_{54,1}
                                                                                                                 T_{52,1}
                               T_{50,2}
                                                                                                       T_{52,0}
                                                                                                                                                           T_{53,2}
i + 10 T_{50,0}
                     T_{50,1}
                                        T_{54,0}
                                                                        T_{51,0}
                                                                                            T_{51,2}
                                                              T_{54,2}
                                                                                 T_{51,1}
                                                                                                                           T_{52,2}
                                                                                                                                      T_{53,0}
i + 11 T_{55,0}
                               T_{55,2}
                     T_{55,1}
                                        . . .
                                                    . . .
                                                                                   . . .
```

State change constraints:

$$\mathtt{STATE}(i+1) = \mathtt{STATE}(i)^{\alpha} \cdot \mathtt{MDS} + \mathtt{RC}$$

Denote the index of the first state in the row by start (e.g. start = 50 for 10-th row). We can expand the previous formula to:

- For i from start to start + 5:

 - $$\begin{split} \bullet \ T_{i+1,0} &= T_{i,0}^5 \cdot \text{MDS}[0][0] + T_{i,1}^5 \cdot \text{MDS}[0][1] + T_{i,2}^5 \cdot \text{MDS}[0][2] + \text{RC}_{i+1,0} \\ \bullet \ T_{i+1,1} &= T_{i,0}^5 \cdot \text{MDS}[1][0] + T_{i,1}^5 \cdot \text{MDS}[1][1] + T_{i,2}^5 \cdot \text{MDS}[1][2] + \text{RC}_{i+1,1} \\ \bullet \ T_{i+1,2} &= T_{i,0}^5 \cdot \text{MDS}[2][0] + T_{i,1}^5 \cdot \text{MDS}[2][1] + T_{i,2}^5 \cdot \text{MDS}[2][2] + \text{RC}_{i+1,2} \end{split}$$

Notice that the constraints above include the state from the next row (start + 5).

Other Circuits 2.8

WIP

Combined Inner Product 2.8.1

$$\sum_{i=0}^{k} \xi^{i} \cdot (f_{i}(\zeta_{1}) + r \cdot f_{i}(\zeta_{2}))$$

Constraints for i + z, where $z \mod 2 = 0$:

- $(w_0 + w_1 \cdot \text{next}(w_5)) \cdot w_6 = w_7$
- $(w_2 + w_3 \cdot \mathtt{next}(w_5)) \cdot w_9 = w_8$
- $\bullet \quad w_5 \cdot w_6 = w_9$
- $w_5 \cdot w_9 = \operatorname{next}(w_9)$
- $w_5 \cdot \operatorname{next}(w_9) = \operatorname{next}(w_5)$
- $w_4 + w_7 + w_8 + \text{next}(w_7) + \text{next}(w_8) = \text{next}(w_4)$

Constraints for i + z, where $z \mod 2 = 1$:

- $(w_0 + w_1 \cdot w_5) \cdot w_9 = w_7$
- $(w_2 + w_3 \cdot w_5) \cdot w_6 = w_8$

2.8.2 **Endo-Scalar Computation**

Let α be equals to $\phi(b)$, where $b \in 0, 1^{\lambda}$.

```
Row
          0
                                        5
                                                    7
                                                          8
                                                                9
                                                                      10
                                                                            11
                                                                                  12
                                                                                        13
                                                          x_2
i
                n_8
                      a_0
                            b_0
                                  a_8
                                        b_8
                                             x_0
                                                                x_3
                                                                      x_4
                                                                            x_5
i + 15
                                        b_8
          n_0
                n_8
                      a_0
                            b_0
                                  a_8
                                             x_0
                                                    x_1
                                                          x_2
                                                                x_3
                                                                      x_4
                                                                            x_5
                                                                                  x_6
                                                                                        x_7
```

Evaluations:

- In the first row $n_0 = 0$, $a_0 = 2$, $b_0 = 2$.
- x_i are 2-bits chunks of the b, first x_0 is the most significant bit of b, n is an accumulator of x_i .
- The values (a_8, b_8) are 8 iterations of the following computations:

$$(a_i, b_i) = (2 \cdot a_{i-1} + c_f(x_{i-1}), 2 \cdot b_{i-1} + d_f(x_{i-1})), \text{ where } c_f(x) = 2/3 \cdot x^3 - 5/2 \cdot x^2 + 11/6 \cdot x \text{ and } d_f(x) = 2/3 \cdot x^3 - 7/2 \cdot x^2 + 29/6 \cdot x - 1.$$

Constraints:

- $w_7 \cdot (w_7 1) \cdot (w_7 2) \cdot (w_7 3) = 0$
- $w_8 \cdot (w_8 1) \cdot (w_8 2) \cdot (w_8 3) = 0$
- $w_9 \cdot (w_9 1) \cdot (w_9 2) \cdot (w_9 3) = 0$
- $w_{10} \cdot (w_{10} 1) \cdot (w_{10} 2) \cdot (w_{10} 3) = 0$
- $w_{11} \cdot (w_{11} 1) \cdot (w_{11} 2) \cdot (w_{11} 3) = 0$
- $w_{12} \cdot (w_{12} 1) \cdot (w_{12} 2) \cdot (w_{12} 3) = 0$
- $w_{13} \cdot (w_{13} 1) \cdot (w_{13} 2) \cdot (w_{13} 3) = 0$
- $w_{14} \cdot (w_{14} 1) \cdot (w_{14} 2) \cdot (w_{14} 3) = 0$
- $w_4 = 256 \cdot w_2 + 128 \cdot c_f(w_6) + 64 \cdot c_f(w_7) + 32 \cdot c_f(w_8) + 16 \cdot c_f(w_9) + 8 \cdot c_f(w_{10}) + 4 \cdot c_f(w_{11}) + 2 \cdot c_f(w_{12}) + c_f(w_{13})$
- $w_5 = 256 \cdot w_3 + 128 \cdot d_f(w_6) + 64 \cdot d_f(w_7) + 32 \cdot d_f(w_8) + 16 \cdot d_f(w_9) + 8 \cdot d_f(w_{10}) + 4 \cdot d_f(w_{11}) + 2 \cdot d_f(w_{12}) + d_f(w_{13})$
- $w_1 = 256 \cdot w_0 + 128 \cdot w_6 + 64 \cdot w_7 + 32 \cdot w_8 + 16 \cdot w_9 + 8 \cdot w_{10} + 4 \cdot w_{11} + 2 \cdot w_{12} + w_{13}$

Copy constraints:

• n_0, a_0, b_0 in row j + 1 are copy constrained with (n_8, a_8, b_8) in row j

Chapter 3

In-EVM State Proof Verifier

This introduces a description for in-EVM Mina Protocol state proof verification mechanism. Crucial components which define this part design are:

- $1. \ \ Verification \ architecture \ description.$
- 2. Verification logic API reference.
- 3. Input data structures description.

3.1 Verification Logic Architecture

The verification logic is split to several parts:

1. LPC/FRI auxiliary proof deserialization

2.

3.2 Verification Logic API Reference

3.3 Input Data Structures

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