

## Assignment 3

### 16-bit ALU

#### Introduction: -

ALU is the fundamental building block of the processor, which is responsible for carrying out the **arithmetic**, **logic** functions, **Shift** functions and **Comparison** functions.

#### Specification:

- **ALU Operands** (A, B)
- **ALU Result** (ALU\_OUT)
- ALU operands and output Result are of **16-bit** width.
- **ALU Result** (ALU\_OUT) is registered.
- The ALU function is carried out according to the value of the **ALU\_FUN** input signal stated in the table in the following page and any other value for **ALU\_FUN** not stated in the table, **ALU\_OUT** must equal to **16'b0**
- **Arith\_flag** is activated "High" only when ALU performs one of the arithmetic operations (Addition, Subtraction, Multiplication, division), otherwise "LOW"
- **Logic\_flag** is activated "High" only when ALU performs one of the Boolean operations (AND, OR, NAND, NOR, XOR, XNOR), otherwise "LOW"
- **CMP\_flag** is activated "High" only when ALU performs one of the Comparison operations (Equal, Greater than, less than), otherwise "LOW"
- **Shift\_flag** is activated "High" only when ALU performs one of the shifting operations (shift right, shift left), otherwise "LOW"

carry  
point 3  
case can do 2 diff expr

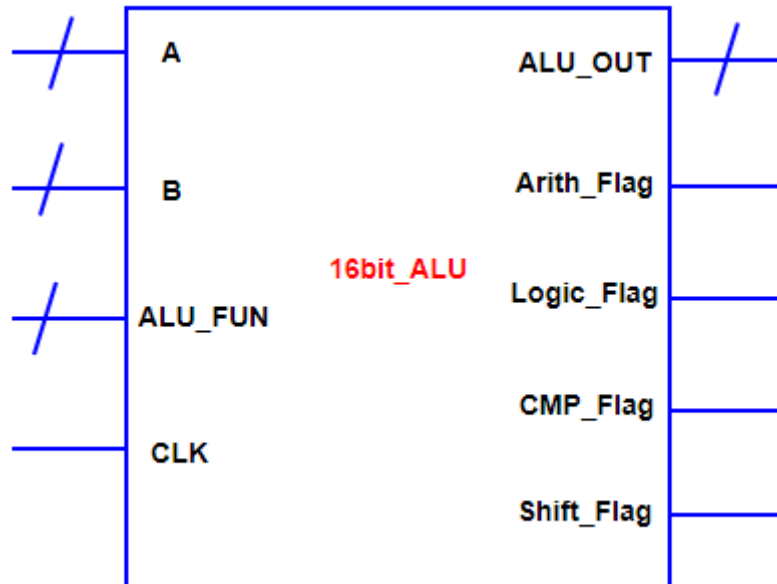
### ALU\_FUN Table

ALU_FUN	Operation	ALU_OUT
0000	Arithmetic : <b>Addition</b>	
0001	Arithmetic : <b>Subtraction</b>	
0010	Arithmetic : <b>Multiplication</b>	
0011	Arithmetic : <b>Division</b>	
0100	Logic : <b>AND</b>	
0101	Logic : <b>OR</b>	
0110	Logic : <b>NAND</b>	
0111	Logic : <b>NOR</b>	
1000	Logic : <b>XOR</b>	
1001	Logic : <b>XNOR</b>	
1010	CMP: <b>A = B</b>	<b>Equal to 1</b>
1011	CMP: <b>A &gt; B</b>	<b>Equal to 2</b>
1100	CMP: <b>A &lt; B</b>	<b>Equal to 3</b>
1101	SHIFT: <b>A &gt;&gt; 1</b>	
1110	SHIFT: <b>A &lt;&lt; 1</b>	

**Hint:** Use Case statement to describe the behavior of this table and use default case if needed.

**Hint:** You can use if statement inside case branches

## Block Interface



1. Write a Verilog Code to capture the above specifications as well as the synthesis diagram of your code.
2. Write a testbench to test all the ALU functions with operating clock frequency 100 KHz