

U TOTAL:

2612

D TOTAL:

2249

B_DET_CTOf_FADC_U01	B_HW_CTOf1_Sl03_Ch04	52
B_DET_CTOf_FADC_U02	B_HW_CTOf1_Sl04_Ch04	43
B_DET_CTOf_FADC_U03	B_HW_CTOf1_Sl03_Ch05	53
B_DET_CTOf_FADC_U04	B_HW_CTOf1_Sl04_Ch05	43
B_DET_CTOf_FADC_U05	B_HW_CTOf1_Sl03_Ch06	0
B_DET_CTOf_FADC_U06	B_HW_CTOf1_Sl04_Ch06	57
B_DET_CTOf_FADC_U07	B_HW_CTOf1_Sl03_Ch07	50
B_DET_CTOf_FADC_U08	B_HW_CTOf1_Sl04_Ch07	48
B_DET_CTOf_FADC_U09	B_HW_CTOf1_Sl03_Ch12	45
B_DET_CTOf_FADC_U10	B_HW_CTOf1_Sl04_Ch12	29
B_DET_CTOf_FADC_U11	B_HW_CTOf1_Sl03_Ch13	42
B_DET_CTOf_FADC_U12	B_HW_CTOf1_Sl04_Ch13	63
B_DET_CTOf_FADC_U13	B_HW_CTOf1_Sl03_Ch14	58
B_DET_CTOf_FADC_U14	B_HW_CTOf1_Sl04_Ch14	55
B_DET_CTOf_FADC_U15	B_HW_CTOf1_Sl03_Ch15	57
B_DET_CTOf_FADC_U16	B_HW_CTOf1_Sl04_Ch15	54
B_DET_CTOf_FADC_U17	B_HW_CTOf1_Sl05_Ch04	41
B_DET_CTOf_FADC_U18	B_HW_CTOf1_Sl06_Ch04	46
B_DET_CTOf_FADC_U19	B_HW_CTOf1_Sl05_Ch05	38
B_DET_CTOf_FADC_U20	B_HW_CTOf1_Sl06_Ch05	56
B_DET_CTOf_FADC_U21	B_HW_CTOf1_Sl05_Ch06	51
B_DET_CTOf_FADC_U22	B_HW_CTOf1_Sl06_Ch06	54
B_DET_CTOf_FADC_U23	B_HW_CTOf1_Sl05_Ch07	49
B_DET_CTOf_FADC_U24	B_HW_CTOf1_Sl06_Ch07	49
B_DET_CTOf_FADC_U25	B_HW_CTOf1_Sl05_Ch12	59
B_DET_CTOf_FADC_U26	B_HW_CTOf1_Sl06_Ch12	58
B_DET_CTOf_FADC_U27	B_HW_CTOf1_Sl05_Ch13	39
B_DET_CTOf_FADC_U28	B_HW_CTOf1_Sl06_Ch13	50
B_DET_CTOf_FADC_U29	B_HW_CTOf1_Sl05_Ch14	61
B_DET_CTOf_FADC_U30	B_HW_CTOf1_Sl06_Ch14	55
B_DET_CTOf_FADC_U31	B_HW_CTOf1_Sl05_Ch15	50
B_DET_CTOf_FADC_U32	B_HW_CTOf1_Sl06_Ch15	31
B_DET_CTOf_FADC_U33	B_HW_CTOf1_Sl07_Ch04	38
B_DET_CTOf_FADC_U34	B_HW_CTOf1_Sl08_Ch04	47
B_DET_CTOf_FADC_U35	B_HW_CTOf1_Sl07_Ch05	31
B_DET_CTOf_FADC_U36	B_HW_CTOf1_Sl08_Ch05	56
B_DET_CTOf_FADC_U37	B_HW_CTOf1_Sl07_Ch06	37
B_DET_CTOf_FADC_U38	B_HW_CTOf1_Sl08_Ch06	42
B_DET_CTOf_FADC_U39	B_HW_CTOf1_Sl07_Ch07	49
B_DET_CTOf_FADC_U40	B_HW_CTOf1_Sl08_Ch07	49
B_DET_CTOf_FADC_U41	B_HW_CTOf1_Sl07_Ch12	46
B_DET_CTOf_FADC_U42	B_HW_CTOf1_Sl08_Ch12	374
B_DET_CTOf_FADC_U43	B_HW_CTOf1_Sl07_Ch13	49

B_DET_CTOf_FADC_D01	B_HW_CTOf1_Sl03_Ch00	46
B_DET_CTOf_FADC_D02	B_HW_CTOf1_Sl04_Ch00	44
B_DET_CTOf_FADC_D03	B_HW_CTOf1_Sl03_Ch01	59
B_DET_CTOf_FADC_D04	B_HW_CTOf1_Sl04_Ch01	44
B_DET_CTOf_FADC_D05	B_HW_CTOf1_Sl03_Ch02	0
B_DET_CTOf_FADC_D06	B_HW_CTOf1_Sl04_Ch02	56
B_DET_CTOf_FADC_D07	B_HW_CTOf1_Sl03_Ch03	58
B_DET_CTOf_FADC_D08	B_HW_CTOf1_Sl04_Ch03	24
B_DET_CTOf_FADC_D09	B_HW_CTOf1_Sl03_Ch08	43
B_DET_CTOf_FADC_D10	B_HW_CTOf1_Sl04_Ch08	54
B_DET_CTOf_FADC_D11	B_HW_CTOf1_Sl03_Ch09	49
B_DET_CTOf_FADC_D12	B_HW_CTOf1_Sl04_Ch09	52
B_DET_CTOf_FADC_D13	B_HW_CTOf1_Sl03_Ch10	50
B_DET_CTOf_FADC_D14	B_HW_CTOf1_Sl04_Ch10	42
B_DET_CTOf_FADC_D15	B_HW_CTOf1_Sl03_Ch11	50
B_DET_CTOf_FADC_D16	B_HW_CTOf1_Sl04_Ch11	54
B_DET_CTOf_FADC_D17	B_HW_CTOf1_Sl05_Ch00	49
B_DET_CTOf_FADC_D18	B_HW_CTOf1_Sl06_Ch00	44
B_DET_CTOf_FADC_D19	B_HW_CTOf1_Sl05_Ch01	42
B_DET_CTOf_FADC_D20	B_HW_CTOf1_Sl06_Ch01	47
B_DET_CTOf_FADC_D21	B_HW_CTOf1_Sl05_Ch02	54
B_DET_CTOf_FADC_D22	B_HW_CTOf1_Sl06_Ch02	49
B_DET_CTOf_FADC_D23	B_HW_CTOf1_Sl05_Ch03	38
B_DET_CTOf_FADC_D24	B_HW_CTOf1_Sl06_Ch03	41
B_DET_CTOf_FADC_D25	B_HW_CTOf1_Sl05_Ch08	67
B_DET_CTOf_FADC_D26	B_HW_CTOf1_Sl06_Ch08	57
B_DET_CTOf_FADC_D27	B_HW_CTOf1_Sl05_Ch09	51
B_DET_CTOf_FADC_D28	B_HW_CTOf1_Sl06_Ch09	48
B_DET_CTOf_FADC_D29	B_HW_CTOf1_Sl05_Ch10	63
B_DET_CTOf_FADC_D30	B_HW_CTOf1_Sl06_Ch10	61
B_DET_CTOf_FADC_D31	B_HW_CTOf1_Sl05_Ch11	48
B_DET_CTOf_FADC_D32	B_HW_CTOf1_Sl06_Ch11	37
B_DET_CTOf_FADC_D33	B_HW_CTOf1_Sl07_Ch00	52
B_DET_CTOf_FADC_D34	B_HW_CTOf1_Sl08_Ch00	50
B_DET_CTOf_FADC_D35	B_HW_CTOf1_Sl07_Ch01	33
B_DET_CTOf_FADC_D36	B_HW_CTOf1_Sl08_Ch01	60
B_DET_CTOf_FADC_D37	B_HW_CTOf1_Sl07_Ch02	26
B_DET_CTOf_FADC_D38	B_HW_CTOf1_Sl08_Ch02	47
B_DET_CTOf_FADC_D39	B_HW_CTOf1_Sl07_Ch03	41
B_DET_CTOf_FADC_D40	B_HW_CTOf1_Sl08_Ch03	47
B_DET_CTOf_FADC_D41	B_HW_CTOf1_Sl07_Ch08	38
B_DET_CTOf_FADC_D42	B_HW_CTOf1_Sl08_Ch08	59
B_DET_CTOf_FADC_D43	B_HW_CTOf1_Sl07_Ch09	39