PRELAB 03
HIBBAN BUTT
CS 5780

1. Two of the many things you can learn are: the basic theory of operation and summarized configuration information.
2.
3. It's there to divide the input clock frequency to the timer.
4. It stores the top value at which the timer will reset and start a new counting period once it hits it.
5. It will modify the GPIO pin's output when the timer value matches the value stored in it.
6. It represents an analog voltage that ranges between the high and low of the digital signal.
7. It allows for alternate peripherals—such as timers—to modify a pin's state, affording the user more options when selecting pins for their peripherals.
8. In code, the alternate function registers are defined in *stm32f0xb.h*. But as for documentation, the peripheral manual can be used to determine what alternate function register should be modified for the pins being used.