



Principles of Compiler Construction

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Lecture 9. Semantic Analysis and Intermediate Code Generation

1. Introduction
2. Types and Declarations
3. Assignments and Expressions
4. Type Checking
5. Boolean Expressions
6. Backpatching and Flow-of-Control Statements

1. Introduction

- Review

- Front end vs. back end
 - $m \times n$: m front ends and n back ends.
- Interface between front ends and back ends
 - Intermediate representation
 - Why IR? Extendability and optimization.
- Semantic (static) analysis
 - The most common analysis
 - Type checking
 - Other static checking
 - Unreachable code
 - Use of uninitialized variables
 - etc.

Static Checking

- Semantic analysis also focuses on the well-formness of source code
 - Due to the expressiveness power of Context-Free Grammars.
 - For example,
 - Number matching of actual parameters.
 - Context sensitive requirements cannot be specified using a context free grammar.
 - **break** statement must be in a loop or **switch**.
 - Requires a complicated and unnatural context free grammar.

Intermediate Representation

- High level intermediate representations
 - AST and DAG
 - Suitable for tasks like static type checking
- Low level intermediate representations
 - 3-address code: $x = y \text{ op } z$
 - Suitable for machine-dependent tasks, such as register allocation and instruction selection.
- IR choice/design are application specific
 - C language is commonly used (AT&T Bell Lab Advanced C++)

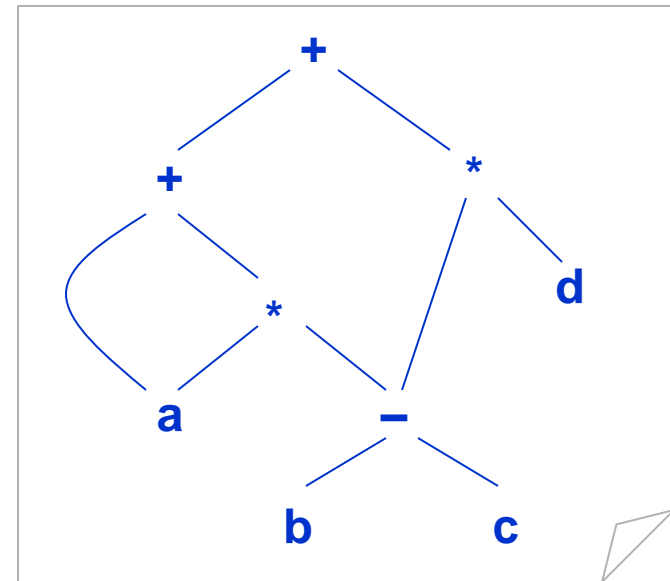
Three-Address Code

- Compiler-generated temporary variables

- $x + y * z$
- $t_1 = y * z$
 $t_2 = x + t_1$

- An example

- $t_1 = b - c$
 $t_2 = a * t_1$
 $t_3 = a + t_2$
 $t_4 = t_1 * d$
 $t_5 = t_3 + t_4$



Addresses

- Addresses in 3-address code
 - Name (variables in source code)
 - May be implemented as a pointer or reference to its entry in the symbol table.
 - Constant
 - Type conversions must be considered.
 - Compiler-generated temporary
 - Useful for optimization.
 - Register allocation.

Instructions

- Common 3-address instructions

- $x = y \text{ op } z$ // arithmetic and logical
 $x = \text{op } y$ // negation and conversion
 $x = y$ // copy
- **goto** L // unconditional jump
if x **goto** L // conditional jump
ifFalse x **goto** L // conditional jump
if $x \text{ op } y$ **goto** L // relational operation
- **param** x_1 // parameter passing
param x_2
...
param x_n
call p, n // procedure call
 $y = \text{call } p, n$ // function call
return y // return a value

Instructions (cont')

- Common 3-address instructions

- $x = y[i]$ // indexed copy, i is the offset
 $x[i] = y$
- $x = \&y$ // address and pointer assignment
 $x = *y$
 $*x = y$

Three-Address Code: Example

- Source code
 - **do** $i = i + 1$;
 while ($a[i] < v$);
- Translation to 3-address code (symbolic labels)
 - L: $t_1 = i + 1$
 $i = t_1$
 $t_2 = i * 8$
 $t_3 = a[t_2]$
 if $t_3 < v$ **goto** L
- Another translation form (position numbers)
 - 100: $t_1 = i + 1$
 101: $i = t_1$
 102: $t_2 = i * 8$
 103: $t_3 = a[t_2]$
 104: **if** $t_3 < v$ **goto** 100

Implementations of Three-Address Code

- Quadruples (quads)
 - Pros and cons?
- Triples
 - Pros and cons?
- Indirect triples
 - Pros and cons?

Space consuming
Flexibility to optimizations

1) Quadruples

- Source code
 - $a = b * -c + b * -c$
- Three-address code
 - $t_1 = \text{minus } c$
 $t_2 = b * t_1$
 $t_3 = \text{minus } c$
 $t_4 = b * t_3$
 $t_5 = t_2 + t_4$
 $a = t_5$
- Quads

	op	arg ₁	arg ₂	result
0	minus	c		t ₁
1	*	b	t ₁	t ₂
2	minus	c		t ₃
3	*	b	t ₃	t ₄
4	+	t ₂	t ₄	t ₅
5	=	t ₅		a
...	...			

2) Triples

- Three-address code

- $t_1 = \text{minus } c$
 $t_2 = b * t_1$
 $t_3 = \text{minus } c$
 $t_4 = b * t_3$
 $t_5 = t_2 + t_4$
 $a = t_5$

	op	arg ₁	arg ₂
0	minus	c	
1	*	b	(0)
2	minus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)
...	...		

3) Indirect Triples

- Three-address code

- $t_1 = \text{minus } c$
 $t_2 = b * t_1$
 $t_3 = \text{minus } c$
 $t_4 = b * t_3$
 $t_5 = t_2 + t_4$
 $a = t_5$

35	(0)
36	(1)
37	(2)
38	(3)
39	(4)
40	(5)
...	...

	op	arg ₁	arg ₂
0	minus	c	
1	*	b	(0)
2	minus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)
...	...		

In Java, array of instruction objects



2. Types and Declarations

- Declaration
 - Literals: implicitly
 - Variables: explicitly
 - Other names: explicitly
- Type checking in strong-typing languages
 - Type compatibility
 - Type inference
 - Implicit type conversion
 - Resolving overloading operators

Simplified Grammar

- Declare only one name at a time

$$D \rightarrow T \textbf{id} ; D \mid \varepsilon$$
$$T \rightarrow B C \mid \textbf{record} \{ D \}$$
$$B \rightarrow \textbf{int} \mid \textbf{double}$$
$$C \rightarrow [\textbf{num}] C \mid \varepsilon$$

Translation of Type Declarations

- Computing types and their widths

$T \rightarrow B$	$\{ t = B.type; w = B.width \}$
C	$\{ T.type = C.type; T.width = C.width \}$
$B \rightarrow \text{int}$	$\{ B.type = \text{INTEGER}; B.width = 4 \}$
$B \rightarrow \text{double}$	$\{ B.type = \text{DOUBLE}; B.width = 8 \}$
$C \rightarrow [\text{num}] C_1$	$\{ C.type = \text{array}(\text{num.value}, C_1.type);$ $C.width = \text{num.value} \times C_1.width \}$
$C \rightarrow \varepsilon$	$\{ C.type = t; C.width = w \}$

Just try it: **int[2][3]**
What is T.type and T.width?

Type
expression

Translation of Type Declarations (cont')

- Computing relative addresses

$P \rightarrow$ { offset = 0 }

D

top denotes the
current symbol table

$D \rightarrow T \text{ id ;}$ { top.put(**id**.lexeme, T.type, offset);
offset += T.width }

D_1

$D \rightarrow \varepsilon$

Embedded actions can be
removed with markers

Another Example

- Enter types and their widths

P → { offset = 0 }

D

D → D ; D

D → **id** : T { table.**enter**(id.name, T.type, offset);
offset += T.width }

T → **integer** { T.type = INTEGER; T.width = 4 }

T → **real** { T.type = REAL; T.width = 8 }

T → **array** [**num**] **of** T₁ { T.type = **array**(**num**.value, T₁.type);
T.width = **num**.value × T₁.width }

T → ^ T₁ { T.type = **pointer**(T₁.type);
T.width = 4 }

Just try it: **k: array [5] of ^real**
What are the side effects?

Another Example (cont')

○ Declarations in nested procedures

```

P → { tableStack.push(new Table(null));
      offsetStack.push(0) }

D   { addWidth(tableStack.top(), offsetStack.top());
      tableStack.pop();
      offsetStack.pop() }

D → D ; D

D → proc id ; { tableStack.push(new Table(tableStack.top()));
                offsetStack.push(0) }

D1 ; S { addWidth(tableStack.top(), offsetStack.top());
          t = tableStack.top(); tableStack.pop();
          offsetStack.pop();
          tableStack.top().enter(id.name, t) }

D → id : T { tableStack.top().enter(id.name, T.type, offsetStack.top());
            offsetStack.top() += T.width }
  
```

Another Example (cont')

- Field names in records

```
T → record      { tableStack.push(new Table(null));  
                  offsetStack.push(0) }  
  
D end           { T.type = record(tableStack.top());  
                  T.width = offsetStack.top();  
                  tableStack.pop();  
                  offsetStack.pop() }
```

3. Assignments and Expressions

- Intermediate code generation
 - Code concatenation
 - `gen(...)`
 - `||`
 - No side effects
 - Incremental generation
 - DBv1: `emit(...)`
 - DBv2: overloading `gen(...)`
 - Side effects

Translation of Expressions

- Code concatenation (syntax-directed definition)

	Productions	Semantic Rules
1	$S \rightarrow \mathbf{id} = E ;$	$S.code = E.code \parallel \text{gen}(\text{top.get}(\mathbf{id.lexeme}) \text{'=' } E.addr)$
2	$E \rightarrow E_1 + E_2$	$E.addr = \mathbf{new Temp}();$ $E.code = E_1.code \parallel E_2.code \parallel \text{gen}(E.addr \text{'=' } E_1.addr \text{'+' } E_2.addr)$
3	$E \rightarrow - E_1$	$E.addr = \mathbf{new Temp}();$ $E.code = E_1.code \parallel \text{gen}(E.addr \text{'=' 'minus' } E_1.addr)$
4	$E \rightarrow (E_1)$	$E.addr = E_1.addr;$ $E.code = E_1.code$
5	$E \rightarrow \mathbf{id}$	$E.addr = \text{top.get}(\mathbf{id.lexeme});$ $E.code = ''$

Translation of Expressions (cont')

- Incremental translation (translation scheme)

$S \rightarrow \text{id} = E ;$	$\{ \text{gen}(\text{top.get}(\text{id.lexeme}) \text{'=' } E.\text{addr}) \}$
$E \rightarrow E_1 + E_2$	$\{ E.\text{addr} = \text{new Temp}();$ $\text{gen}(E.\text{addr} \text{'=' } E_1.\text{addr} \text{'+' } E_2.\text{addr}) \}$
$E \rightarrow - E_1$	$\{ E.\text{addr} = \text{new Temp}();$ $\text{gen}(E.\text{addr} \text{'=' } \text{'minus'} E_1.\text{addr}) \}$
$E \rightarrow (E_1)$	$\{ E.\text{addr} = E_1.\text{addr} \}$
$E \rightarrow \text{id}$	$\{ E.\text{addr} = \text{top.get}(\text{id.lexeme}) \}$

Another Example

○ Declared variables

$S \rightarrow \text{id} := E ;$ { $p = \text{symbolTable.lookup}(\text{id.name});$
 $\text{if } (p == \text{null}) \text{ throw new } \text{SomeException}();$
 $\text{emit}(p \text{ '=' } E.\text{place}) \}$

$E \rightarrow E_1 + E_2$ { $E.\text{place} = \text{new Temp}();$
 $\text{emit}(E.\text{place} \text{ '=' } E_1.\text{place} \text{ '+' } E_2.\text{place}) \}$

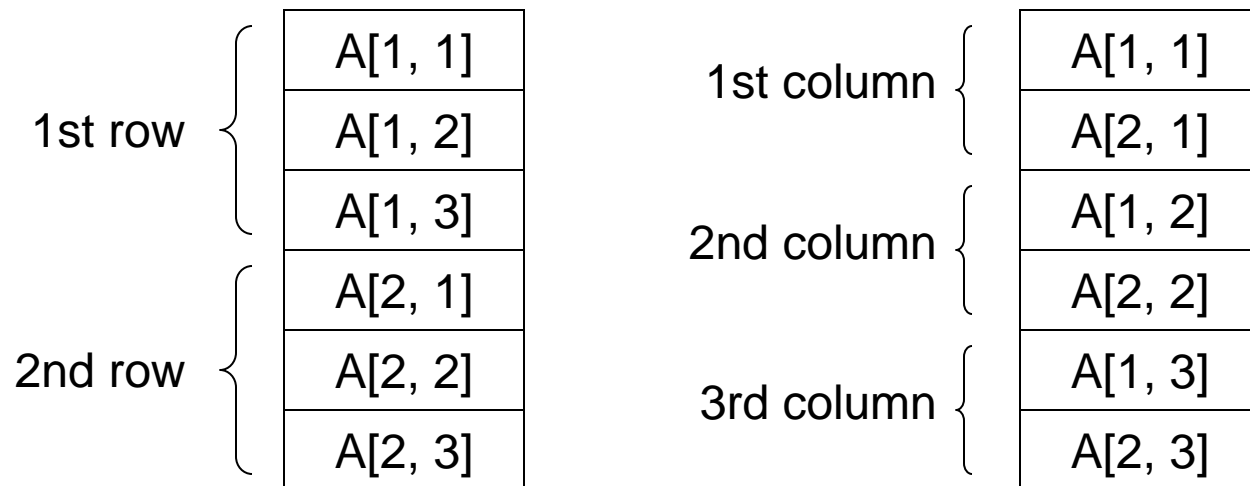
$E \rightarrow - E_1$ { $E.\text{place} = \text{new Temp}();$
 $\text{emit}(E.\text{place} \text{ '=' } \text{'minus'} E_1.\text{place}) \}$

$E \rightarrow (E_1)$ { $E.\text{place} = E_1.\text{place} \}$

$E \rightarrow \text{id}$ { $p = \text{symbolTable.lookup}(\text{id.name});$
 $\text{if } (p == \text{null}) \text{ throw new } \text{SomeException}();$
 $E.\text{place} = p \}$

Addressing Array Elements

- 2-dimensional array layout
 - Row major vs. column major



Addressing Array Elements

- Relative address of array elements

- $A[i]$

- $\text{base} + (i - \text{low}) \times w$

- $i \times w + (\text{base} - \text{low} \times w)$

Constant
for optimization

- $A[i_1, i_2]$

- $\text{base} + ((i_1 - \text{low}_1) \times n_2 + i_2 - \text{low}_2) \times w$

- $((i_1 \times n_2) + i_2) \times w + (\text{base} - (\text{low}_1 \times n_2 + \text{low}_2) \times w)$

- $A[i_1, i_2, \dots, i_k]$

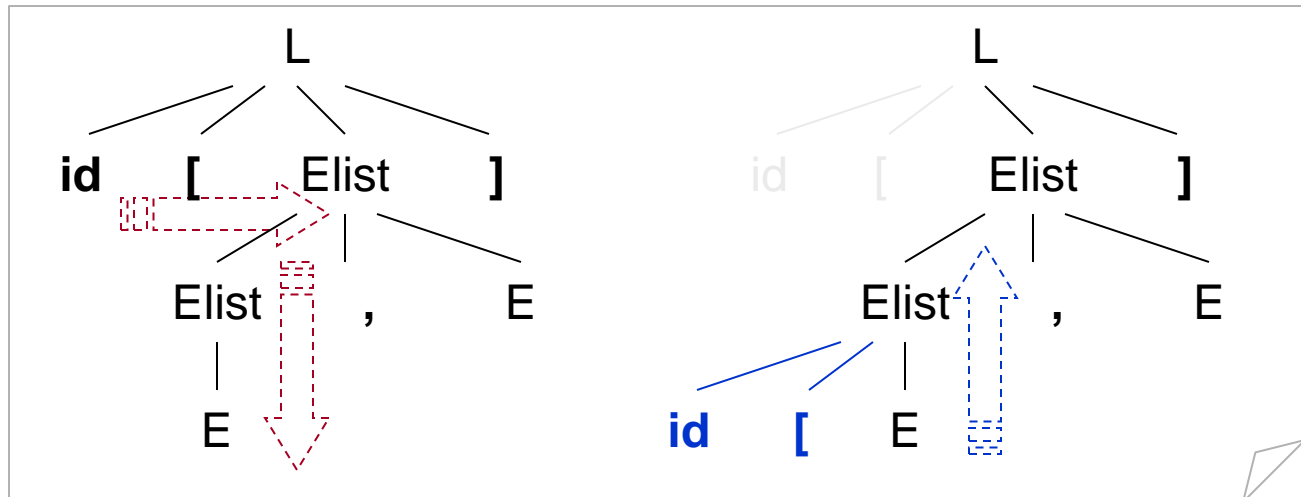
- $((\dots((i_1 \times n_2 + i_2) \times n_3 + i_3) \dots) \times n_k + i_k) \times w +$
 $\text{base} - ((\dots((\text{low}_1 \times n_2 + \text{low}_2) \times n_3 + \text{low}_3) \dots) \times n_k + \text{low}_k) \times w$

Addressing Tips

- For each increment of a new dimension, addressing is calculated recursively, e.g. from k to $k + 1$
 - For variable part V : $V \times n_{k+1} + i_{k+1}$
 - For constant part C : $C \times n_{k+1} + \text{low}_{k+1}$

Grammar for Array References

- Array references in Pascal: `a[2, 3]`
 - $L \rightarrow \text{id [Elist]} \mid \text{id}$
 - $\text{Elist} \rightarrow \text{Elist , E} \mid \text{E}$
- Grammar transformation (why?)
 - $L \rightarrow \text{Elist]} \mid \text{id}$
 - $\text{Elist} \rightarrow \text{Elist , E} \mid \text{id [E}$



Translation Scheme

○ Addressing array elements in Pascal

- (1) $S \rightarrow L := E$ { **if** (L.offset == **null**) emit(L.place '=' E.place)
 else emit(L.place '[' L.offset ']' '=' E.place) }
- (2) $E \rightarrow E_1 + E_2$ { E.place = **new** Temp();
 emit(E.place '=' E₁.place '+' E₂.place) }
- (3) $E \rightarrow (E_1)$ { E.place = E₁.place }
- (4) $E \rightarrow L$ { **if** (L.offset == **null**) E.place = L.place
 else {
 E.place = **new** Temp();
 emit(E.place '=' L.place '[' L.offset ']')
 } }
- (5) $L \rightarrow \text{Elist }]$ { L.place = **new** Temp();
 emit(L.place '=' **constant**(Elist.array));
 L.offset = **new** Temp();
 emit(L.offset '=' Elist.place '*' **width**(Elist.array) }
- (6) $L \rightarrow \text{id}$ { L.place = **id**.place;
 L.offset = **null** }

L is a simple id (if L.offset is null)
or an array reference

L.place = base – C * w
L.offset = V * w

Translation Scheme (cont')

○ Addressing array elements in Pascal (cont')

(7) $Elist \rightarrow Elist_1, E$ { $t = \text{new Temp}();$
 $m = Elist_1.ndim + 1;$
 $\text{emit}(t '=' Elist_1.place '*' \text{limit}(Elist_1.array, m));$
 $\text{emit}(t '+=' E.place);$
 $Elist.array = Elist_1.array;$
 $Elist.place = t;$
 $Elist.ndim = m$ }
 (8) $Elist \rightarrow id [E$ { $Elist.array = id.place;$
 $Elist.place = E.place;$
 $Elist.ndim = 1$ }

$Elist.array = \text{base}$
 $Elist.place = V$
 $Elist.ndim = \text{dimensions}$

Another Translation Scheme

- Array references in C/C++: $a[2][3]$
 - For all n , $\text{low}_n = 0$
 - Addressing formula
 - $A[i]$
 - $\text{base} + i \times w$
 - $A[i_1][i_2]$
 - $\text{base} + i_1 \times w_1 + i_2 \times w_2$
 - w_1 is the width of a row
 - w_2 is the width of an element in a row
 - $A[i_1][i_2] \dots [i_k]$
 - $\text{base} + i_1 \times w_1 + i_2 \times w_2 + \dots + i_k \times w_k$

Java does NOT use
row-major storage for arrays

Another Translation Scheme (cont')

○ Translation scheme

$S \rightarrow \mathbf{id} = E ;$	{ gen(top.get(id .lexeme) '=' E.addr) }
$S \rightarrow L = E ;$	{ gen(L.array.base '[' L.addr '] '=' E.addr) }
$E \rightarrow E_1 + E_2$	{ E.addr = new Temp(); gen(E.addr '=' E ₁ .addr '+' E ₂ .addr) }
$E \rightarrow \mathbf{id}$	{ E.addr = top.get(id .lexeme) }
$E \rightarrow L$	{ E.addr = new Temp(); gen(E.addr '=' L.array.base '[' L.addr ']') }
$L \rightarrow \mathbf{id} [E]$	{ L.array = top.get(id .lexeme); L.type = L.array.type.element; L.addr = new Temp(); gen(L.addr '=' E.addr '*' L.type.width) }
$L \rightarrow L_1 [E]$	{ L.array = L ₁ .array; L.type = L ₁ .type.element; t = new Temp(); L.addr = new Temp(); gen(t '=' E.addr '*' L.type.width); gen(L.addr '=' L ₁ .addr '+' t) }

L only for array reference
 E.addr = E.place
 L.array.base = L.place
 L.addr = L.offset

4. Type Checking

- Strong typing vs. weak typing
 - Strongness is relative
- Type definitions
 - Primitive types: enumeration of constant
 - Composite types: type expressions
 - Type of functions: signatures
 - **if** f has type $s \rightarrow t$ **and** x has type s
then expression $f(x)$ has type t

Translation Scheme: An Example

- Type checking, inference and implicit casting

```
E → E1 * E2 { E.place := new Temp();  
    if (E1.type == TK_INT && E2.type == TK_INT) {  
        emit(E.place '=' E1.place '*int' E2.place);  
        E.type = TK_INT;  
    } elseif (E1.type == TK_REAL && E2.type == TK_REAL) {  
        emit(E.place '=' E1.place '*real' E2.place);  
        E.type = TK_REAL;  
    } elseif (E1.type == TK_INT && E2.type == TK_REAL) {  
        t := new Temp();  
        emit(t '=' 'int2real' E1.place);  
        emit(E.place '=' t '*real' E2.place);  
        E.type = TK_REAL;  
    } elseif (...) { ... }  
}
```



5. Boolean Expressions

- Boolean expressions are used in
 - Flows of control
 - Computing logical values

Computing Logical Values

- **a < b** equals to **if (a < b) then 1 else 0**

$E \rightarrow E_1 \text{ or } E_2$	{ E.place = new Temp(); emit(E.place '=' E ₁ .place ' or ' E ₂ .place) }
$E \rightarrow E_1 \text{ and } E_2$	{ E.place = new Temp(); emit(E.place '=' E ₁ .place ' and ' E ₂ .place) }
$E \rightarrow \text{not } E_1$	{ E.place = new Temp(); emit(E.place '=' ' not ' E ₁ .place) }
$E \rightarrow (E_1)$	{ E.place = E ₁ .place }
$E \rightarrow \text{id}_1 \text{ relop id}_2$	{ E.place = new Temp(); emit('if' id ₁ .place relop .op id ₂ .place ' goto ' currentStmt+3); emit(E.place '=' ' 0 '); emit('goto' currentStmt+2); emit(E.place '=' ' 1 ') }
$E \rightarrow \text{true}$	{ E.place = new Temp(); emit(E.place '=' ' 1 ') }
$E \rightarrow \text{false}$	{ E.place = new Temp(); emit(E.place '=' ' 0 ') }

Computing Logical Values: An Example

- Source code
 - $a < b$ **or** $c < d$ **and** $e < f$
- Intermediate code

```
100:  if a < b goto 103
101:  t1 = 0
102:  goto 104
103:  t1 = 1
104:  if c < d goto 107
105:  t2 = 0
106:  goto 108
107:  t2 = 1
```

```
108:  if e < f goto 111
109:  t3 = 0
110:  goto 112
111:  t3 = 1
112:  t4 = t2 and t3
113:  t5 = t1 or t4
114:  ...
```

Short-Circuit Evaluation

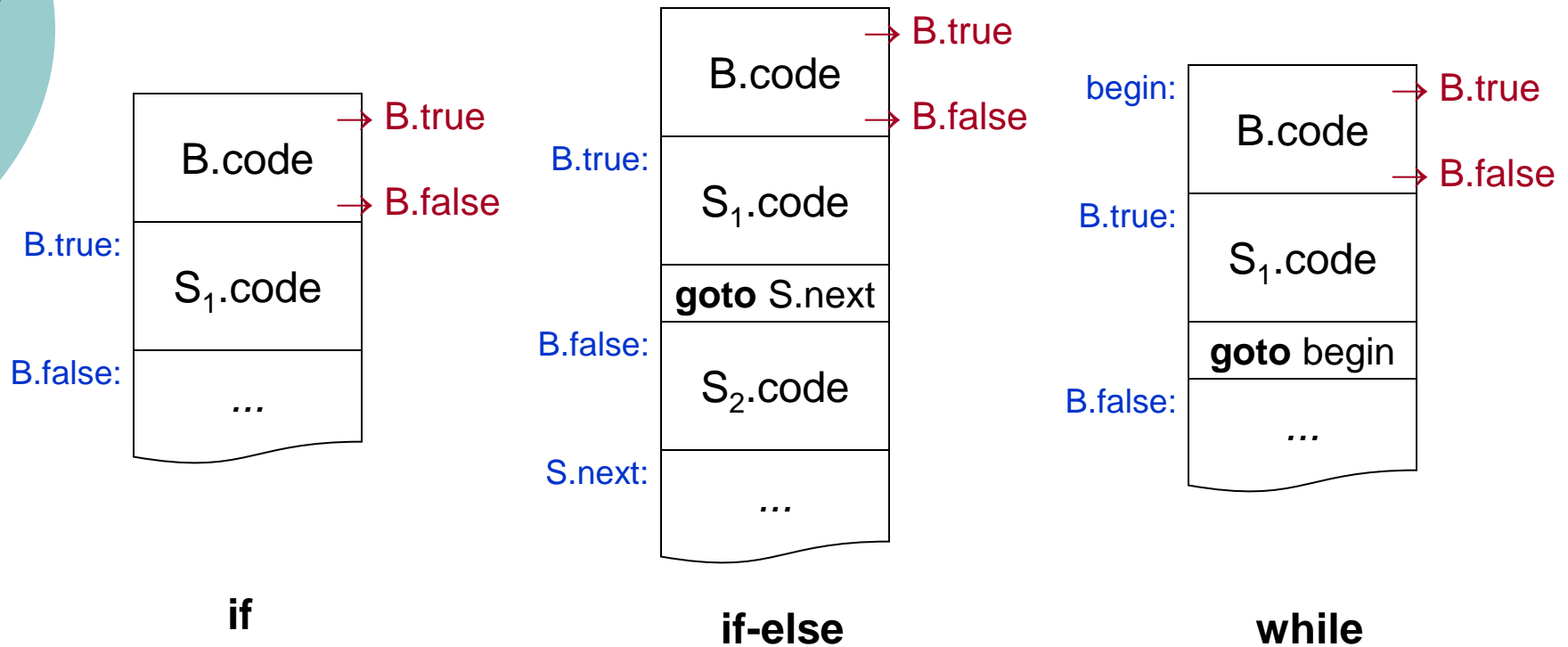
- Flow-of-Control Statements

- $S \rightarrow \text{if (B) } S_1$
- $S \rightarrow \text{if (B) } S_1 \text{ else } S_2$
- $S \rightarrow \text{while (B) } S_1$

- Short-circuit evaluation for **&&** and **||**

- For higher evaluation efficiency
- And ...

Generated Code Illustration



Syntax-Directed Definition for Flow-of-Control Statements

Where does
S.next come from?

Productions	Semantic Rules
$P \rightarrow S$	$S.next = \text{new Label}();$ $P.code = S.code \ \ label(S.next)$
$S \rightarrow \text{assign}$	$S.code = \text{assign.code}$
$S \rightarrow S_1$ S_2	$S_1.next = \text{new Label}();$ $S_2.next = S.next;$ $S.code = S_1.code \ \ label(S_1.next) \ \ S_2.code$
$S \rightarrow \text{if (B) } S_1$	$B.true = \text{new Label}();$ $B.false = S_1.next = S.next;$ $S.code = B.code \ \ label(B.true) \ \ S_1.code$
$S \rightarrow \text{if (B) } S_1$ $\text{else } S_2$	$B.true = \text{new Label}();$ $B.false = \text{new Label}();$ $S_1.next = S_2.next = S.next;$ $S.code = B.code \ \ label(B.true) \ \ S_1.code \ $ $\quad \quad \quad \text{gen('goto' S.next) \ \ label(B.false) \ \ } S_2.code$
$S \rightarrow \text{while (B)}$ S_1	$begin = \text{new Label}();$ $B.true = \text{new Label}();$ $B.false = S.next;$ $S_1.next = begin;$ $S.code = label(begin) \ \ B.code \ \ label(B.true) \ \ S_1.code \ \ \text{gen('goto' begin)}$

Avoid redundant
gotos

Syntax-Directed Definition for Booleans

Productions	Semantic Rules
$B \rightarrow B_1 \ \ B_2$	$B_1.true = B.true;$ $B_1.false = \text{new Label}();$ $B_2.true = B.true;$ $B_2.false = B.false;$ $B.code = B_1.code \ \ \text{label}(B_1.false) \ \ B_2.code$
$B \rightarrow B_1 \ \&\& \ B_2$	$B_1.true = \text{new Label}();$ $B_1.false = B.false;$ $B_2.true = B.true;$ $B_2.false = B.false;$ $B.code = B_1.code \ \ \text{label}(B_1.true) \ \ B_2.code$
$B \rightarrow ! \ B_1$	$B_1.true = B.false;$ $B_1.false = B.true;$ $B.code = B_1.code$
$B \rightarrow E_1 \ \text{relop} \ E_2$	$B.code = E_1.code \ \ E_2.code$ $\quad \ \ \text{gen}('if' \ E_1.addr \ \text{relop.op} \ E_2.addr \ 'goto' \ B.true)$ $\quad \ \ \text{gen}('goto' \ B.false)$
$B \rightarrow \text{true}$	$B.code = \text{gen}('goto' \ B.true)$
$B \rightarrow \text{false}$	$B.code = \text{gen}('goto' \ B.false)$

Short-Circuit
Evaluation

Syntax-Directed Translation: An Example

- Source code
 - **if** (x < 100 || x > 200 && x != y) x = 0
- Intermediate code

```
        if x < 100 goto L2
        goto L3
L3:    if x > 200 goto L4
        goto L1
L4:    if x != y goto L2
        goto L1
L2:    x = 0
L1:    ...
```

6. Backpatching and Flow-of-Control Statements

- In SDD for Flow-of-Control Statements
 - Where does **S.next** come from?
 - Only after all intermediate code are generated, can **S.next** be computed.
- In SDD for Booleans
 - Where do **B.true** and **B.false** come from?
 - Must be provided by the context of the boolean expressions.
 - The context depends on the result of **S.next**.



Design Motivation and Solution

- Motivation
 - One-pass code generation
- Solution
 - Using backpatching
- It is a general approach to dealing with initial values which must be computed at the end.

Backpatching for Boolean Expressions

- Translation scheme

$B \rightarrow B_1 \parallel M B_2$	<pre>{ backpatch(B₁.falseList, M.instruction); B.trueList = merge(B₁.trueList, B₂.trueList); B.falseList = B₂.falseList; }</pre>
$B \rightarrow B_1 \&\& M B_2$	<pre>{ backpatch(B₁.trueList, M.instruction); B.trueList = B₂.trueList; B.falseList = merge(B₁.falseList, B₂.falseList); }</pre>
$B \rightarrow ! B_1$	<pre>{ B.trueList = B₁.falseList; B.falseList = B₁.trueList; }</pre>
$B \rightarrow (B_1)$	<pre>{ B.trueList = B₁.trueList; B.falseList = B₁.falseList; }</pre>
$B \rightarrow E_1 \text{ relop } E_2$	<pre>{ B.trueList = new List(nextInstruction); B.falseList = new List(nextInstruction + 1); emit('if' E₁.addr relop.op E₂.addr 'goto __'); emit('goto __'); }</pre>
$B \rightarrow \text{true}$	<pre>{ B.trueList = new List(nextInstruction); emit('goto __'); }</pre>
$B \rightarrow \text{false}$	<pre>{ B.falseList = new List(nextInstruction); emit('goto __'); }</pre>
$M \rightarrow \varepsilon$	<pre>{ M.instruction = nextInstruction; }</pre>

Backpatching for Flow-of-Control Statements

- Translation scheme

```
S → if ( B ) M S1 { backpatch(B.trueList, M.instruction);  
                      S.nextList = merge(B.falseList, S1.nextList); }  
  
S → if ( B ) M1 S1 N else M2 S2  
    { backpatch(B.trueList, M1.instruction);  
      backpatch(B.falseList, M2.instruction);  
      S.nextList = merge(S1.nextList, N.nextList, S2.nextList); }  
  
S → while M1 ( B ) M2 S1  
    { backpatch(B.trueList, M2.instruction);  
      backpatch(S1.nextList, M1.instruction);  
      S.nextList = B.falseList;  
      emit('goto' M1.instruction); }  
  
S → { L }           { S.nextList = L.nextList; }  
S → A ;             { S.nextList = new List(); // Assignment or Atom }  
M → ε               { M.instruction = nextInstruction; }  
N → ε               { N.nextList = new List(nextInstruction);  
                     emit('goto __'); }  
  
L → L1 M S         { backpatch(L1.nextList, M.instruction);  
                     L.nextList = S.nextList; }  
L → S               { L.nextList = S.nextList; }
```

Exercise 9.1

- What is the translation result of input token string: **$x := A[y, z]$** ?
 - Tips: use the translation scheme for Pascal.

DBv1, pp.486

Exercise 9.2

- What is the translation result of input token string: **c + a[i][j]**?
 - Tips: use the translation scheme for C/C++.

DBv2, pp.385

Exercise 9.3

- What is the translation result of input token string: **$x < 100 \parallel x > 200 \&\& x \neq y$** ?
 - Tips: use the translation scheme for boolean expressions with backpatching.
 - Suppose that the start position of the generated code is 100.

DBv2, pp.412-414

Further Reading

- Dragon Book, 2nd Edition (DBv2)
 - Comprehensive Reading:
 - Section 6.2 on introduction to intermediate representations.
 - Section 6.5 on type checking.
 - Section 6.3, 6.4, 6.6 and 6.7 on translations of various program constructs.
 - Skip Reading:
 - Section 6.1 on AST and DAG.
 - Section 6.8 and 6.9 on translations of switches and procedures.

Enjoy the Course!

