《计算机体系结构-量化研究方法-第六版》

第四章:

1. PPT 内容

Page 4 Flynn's Taxonomy

Page 5 Advantages of SIMD architectures

Page 10 Example of vector architecture

Page 25 Optimizations

Page 26 1. A four lane vector unit

Page 31 Memory banks, 课本 page 298-299

Page 34-37

Page 45-46 Roofline Model 课本 Page 307-310

Page 47 C. Graphical Processing Unit GPU

Page 48 Threads, blocks, and grid (CUDA)

Page 49 NVIDIA GPU architecture 课本 Page 313-320

Page 52-55 Example: multiply two vectors of length 8192

Page 59-60 NVIDIA GPU memory structures 课本 Page 326-328

Page 61 Terminology (GPU)

Page 68-69 Conditional branching 课本 Page 323 - 326