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| **Equivalence Class Design** | | | | | | | | | |
| **Valid EC** |  |  |  |  | | **Invalid EC** | |  |  |
| **EC ID** | **Condition** | | | |  | **EC ID** | **Condition** | | |
| EC01 | {x1: 1000 <= x1 < 3000} = 2000 | | | |  | EC07 | {x1: x1 < 1000} = 999 | | |
| EC02 | {x1: 3000 <= x1 <= 5000} = 4000 | | | |  | EC08 | {x1: x1 > 5000} = 5001 | | |
| EC03 | {x2: 300 <= x2 < 550} = 450 | | | |  | EC09 | {x2: x2 < 300} = 299 | | |
| EC04 | {x2: 550 <= x2 <= 800} = 700 | | | |  | EC10 | {x2: x2 > 800} = 801 | | |
| EC05 | {x3: 500 <= x3 < 2000} = 1500 | | | |  | EC11 | {x3: x3 < 500} = 499 | | |
| EC06 | {x3: 2000 <= x3 <= 3000} = 2500 | | | |  | EC12 | {x3: x3 > 3000} = 3001 | | |
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| **Test Case Design and Test Results** | | | | | | | | | | | | | |
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| **Project Name:** | Income | | | | | | | **Project ID:** | |  | | | |
| **Test Strategy:** | Strong Robust Equivalence Class Testing | | | | | | | **Designer:** | |  | | | |
| **Test Environment:** | | **X1 = ใบพัด, X2 = มอเตอร์, X3 = กระจังหน้า-หลัง** | | | | | | | | | | | |
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| **Test Case ID** | **Input** | | | **Expected Result** | | | | | **Actual Result** | | | **Status (Pass/Fail/No run)** |
| X1 | X2 | X3 |
| TC01 | 2000 | 450 | 1500 | 63500 | | | | | 63500 | | | Pass |
| TC02 | 2000 | 450 | 2500 | 83500 | | | | | 83500 | | | Pass |
| TC03 | 2000 | 450 | 499 | -1 | | | | | -1 | | | Pass |
| TC04 | 2000 | 450 | 3001 | -1 | | | | | 93520 | | | Fail |
| TC05 | 2000 | 700 | 1500 | 71000 | | | | | 71000 | | | Pass |
| TC06 | 2000 | 700 | 2500 | 91000 | | | | | 91000 | | | Pass |
| TC07 | 2000 | 700 | 499 | -1 | | | | | -1 | | | Pass |
| TC08 | 2000 | 700 | 3001 | -1 | | | | | 101020 | | | Fail |
| TC09 | 2000 | 299 | 1500 | -1 | | | | | -1 | | | Pass |
| **Test Case ID** | **Input** | | | **Expected Result** | | | | | **Actual Result** | | | **Status (Pass/Fail/No run)** |
| X1 | X2 | X3 |
| TC10 | 2000 | 299 | 2500 | -1 | | | | | -1 | | | Pass |
| TC11 | 2000 | 299 | 499 | -1 | | | | | -1 | | | Pass |
| TC12 | 2000 | 299 | 3001 | -1 | | | | | -1 | | | Pass |
| TC13 | 2000 | 801 | 1500 | -1 | | | | | 74030 | | | Fail |
| TC14 | 2000 | 801 | 2500 | -1 | | | | | 94030 | | | Fail |
| TC15 | 2000 | 801 | 499 | -1 | | | | | -1 | | | Pass |
| TC16 | 2000 | 801 | 3001 | -1 | | | | | 104050 | | | Fail |
| TC17 | 4000 | 450 | 1500 | 83500 | | | | | 83500 | | | Pass |
| TC18 | 4000 | 450 | 2500 | 103500 | | | | | 103500 | | | Pass |
| TC19 | 4000 | 450 | 499 | -1 | | | | | -1 | | | Pass |
| TC20 | 4000 | 450 | 3001 | -1 | | | | | 113520 | | | Fail |
| TC21 | 4000 | 700 | 1500 | 91000 | | | | | 91000 | | | Pass |
| TC22 | 4000 | 700 | 2500 | 111000 | | | | | 111000 | | | Pass |
| TC23 | 4000 | 700 | 499 | -1 | | | | | -1 | | | Pass |
| TC24 | 4000 | 700 | 3001 | -1 | | | | | 121020 | | | Fail |

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| **Test Case ID** | **Input** | | | **Expected Result** | **Actual Result** | **Status (Pass/Fail/No run)** |
| X1 | X2 | X3 |
| TC25 | 4000 | 299 | 1500 | -1 | -1 | Pass |
| TC26 | 4000 | 299 | 2500 | -1 | -1 | Pass |
| TC27 | 4000 | 299 | 499 | -1 | -1 | Pass |
| TC28 | 4000 | 299 | 3001 | -1 | -1 | Pass |
| TC29 | 4000 | 801 | 1500 | -1 | 94030 | Fail |
| TC30 | 4000 | 801 | 2500 | -1 | 114030 | Fail |
| TC31 | 4000 | 801 | 499 | -1 | -1 | Pass |
| TC32 | 4000 | 801 | 3001 | -1 | 124050 | Fail |
| TC33 | 999 | 450 | 1500 | -1 | -1 | Pass |
| TC34 | 999 | 450 | 2500 | -1 | -1 | Pass |
| TC35 | 999 | 450 | 499 | -1 | -1 | Pass |
| TC36 | 999 | 450 | 3001 | -1 | -1 | Pass |
| TC37 | 999 | 700 | 1500 | -1 | -1 | Pass |
| TC38 | 999 | 700 | 2500 | -1 | -1 | Pass |
| **Test Case ID** | **Input** | | | **Expected Result** | **Actual Result** | **Status (Pass/Fail/No run)** |
| X1 | X2 | X3 |
| TC39 | 999 | 700 | 499 | -1 | -1 | Pass |
| TC40 | 999 | 700 | 3001 | -1 | -1 | Pass |
| TC41 | 999 | 299 | 1500 | -1 | -1 | Pass |
| TC42 | 999 | 299 | 2500 | -1 | -1 | Pass |
| TC43 | 999 | 299 | 499 | -1 | -1 | Pass |
| TC44 | 999 | 299 | 3001 | -1 | -1 | Pass |
| TC45 | 999 | 801 | 1500 | -1 | -1 | Pass |
| TC46 | 999 | 801 | 2500 | -1 | -1 | Pass |
| TC47 | 999 | 801 | 499 | -1 | -1 | Pass |
| TC48 | 999 | 801 | 3001 | -1 | -1 | Pass |
| TC49 | 5001 | 450 | 1500 | -1 | 93510 | Fail |
| TC50 | 5001 | 450 | 2500 | -1 | 113510 | Fail |
| TC51 | 5001 | 450 | 499 | -1 | -1 | Pass |
| TC52 | 5001 | 450 | 3001 | -1 | 123530 | Fail |

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| **Test Case ID** | **Input** | | | **Expected Result** | **Actual Result** | **Status (Pass/Fail/No run)** |
| X1 | X2 | X3 |
| TC53 | 5001 | 700 | 1500 | -1 | 101010 | Fail |
| TC54 | 5001 | 700 | 2500 | -1 | 121010 | Fail |
| TC55 | 5001 | 700 | 499 | -1 | -1 | Pass |
| TC56 | 5001 | 700 | 3001 | -1 | 131030 | Fail |
| TC57 | 5001 | 299 | 1500 | -1 | -1 | Pass |
| TC58 | 5001 | 299 | 2500 | -1 | -1 | Pass |
| TC59 | 5001 | 299 | 499 | -1 | -1 | Pass |
| TC60 | 5001 | 299 | 3001 | -1 | -1 | Pass |
| TC61 | 5001 | 801 | 1500 | -1 | 104040 | Fail |
| TC62 | 5001 | 801 | 2500 | -1 | 124040 | Fail |
| TC63 | 5001 | 801 | 499 | -1 | -1 | Pass |
| TC64 | 5001 | 801 | 3001 | -1 | 134060 | Fail |

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| **Scenario ID** | **Scenario Name** | **Test Case#** | **Pass** | **Fail** | **No run** | **Block** | **Remark** | **Defect ID** |
| EC-Lab5-01 | Income | 64 | 45 | 19 | 0 | 0 |  |  |
| **รวม** | | 64 | 45 | 19 | 0 | 0 |  |  |