

---

---

# SEcube Firmware's Programmer Guide

---

---



Project Report

Filippo Cottone | Pietro Scandale | Francesco Vaiana  
Luca Di Grazia | Julia Roca Garcia

Politecnico Di Torino  
MD in Embedded Systems

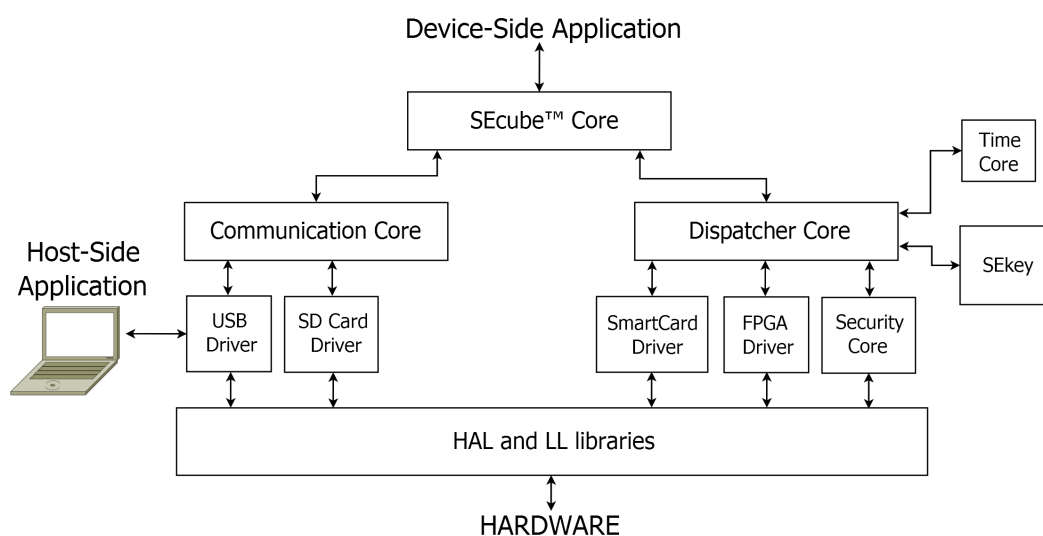
## Contents

<b>1</b>	<b>SEcube overview architecture</b>	<b>3</b>
<b>2</b>	<b>Communication Core</b>	<b>7</b>
2.1	Communication Data Structures . . . . .	7
2.2	Communication Functions . . . . .	7
<b>3</b>	<b>SEcube Core</b>	<b>9</b>
3.1	SEcube Core Data Structures . . . . .	9
3.2	SEcube Core Functions . . . . .	9
3.2.1	Command Functions . . . . .	9
<b>4</b>	<b>SEcube Core time</b>	<b>11</b>
4.1	SEcube Core time Functions . . . . .	11
<b>5</b>	<b>Memory</b>	<b>12</b>
5.1	Memory Data Structures . . . . .	12
5.2	Memory Functions . . . . .	12
<b>6</b>	<b>SE3 Keys</b>	<b>13</b>
6.1	SE3 Key Data Structures . . . . .	13
6.2	SE3 Key Functions . . . . .	13
<b>7</b>	<b>Dispatcher Core</b>	<b>15</b>
7.1	Dispatcher Data Structures . . . . .	15
7.2	Dispatcher Functions . . . . .	15
7.2.1	Command Functions . . . . .	16

<b>8</b>	<b>SEkey</b>	<b>17</b>
8.1	SEkey Function . . . . .	17
<b>9</b>	<b>Cryptographic Security Components</b>	<b>18</b>
9.1	Security Core . . . . .	18
9.1.1	Security Core Data Structures . . . . .	18
9.1.2	Security Core Functions . . . . .	18
9.2	FPGA . . . . .	20
9.3	Smartcard . . . . .	21
<b>10</b>	<b>Flash</b>	<b>22</b>
10.1	Flash Data Structures . . . . .	22
10.2	Flash Functions . . . . .	22
<b>11</b>	<b>Common</b>	<b>24</b>
11.1	Common Data Structures . . . . .	24
11.2	Common Functions . . . . .	24
11.2.1	Debug Tool Functions . . . . .	25

## SEcube overview architecture

The new developed architecture was designed to obtain a hierarchical firmware structure:



**Figure 1.1:** SEcube new layered architecture.

The request comes in a packet form: there are user written functions, that implement the interface to usb drivers, where there is the execution of its functionalities, allowing a communication through a special .secube file, located in the SD card. The host requires for a service by locking this file through operating system APIs and writing on it block-wise requests. A block is formed of 512 bytes. The device side will loop until the lock is released to serve the incoming request, locking in turn the same file. The host will loop until the locks became available with a certain deadline, and when it will be freed by the device side, it will understand that the response is arrived and will read on it the outcome of the request.

*se3\_proto\_recv* is the function to receive a block. It checks whether the block contains a special constant sequence of data, the magic sequence, that is used to let the firmware know the incoming blocks are not addressed to a general mass memory device, but they are operational elements directly referred to the SEcube firmware. Assuming that the device is ready to serve a new request, the block is magic and it has not been stored in the SDcard yet, it will be wrote in it, and the relative presence flags updated. If it has already been written yet, the incoming blocks may be a command or a data one. They will be treated in a different manner: data block will be directly forwarded to SDcard; command blocks, instead, will be unpacked and forwarded to the core. As mentioned before, the host is still locking the file where this data are stored, so no one is able to read this information.

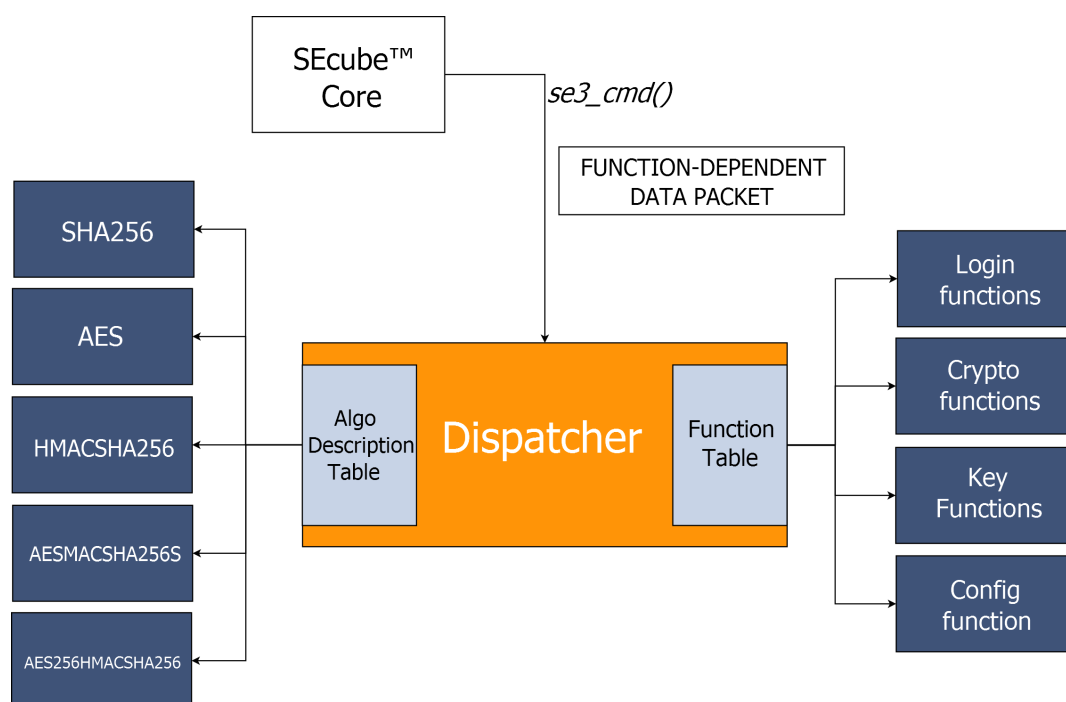
At the reset phase the SEcube core makes the initialization of the main data structures for performing the communication, then it waits in an infinite loop a request from the host side. Once the device got a new request, it will be unpacked and the right command is prepared. Here the command to be executed is chosen among all the available. This task is performed because the core contains a table of all the functions that SECUBE can run; the core understands whether the function can be execute by itself, otherwise the dispatcher is called.

The functions that the core can execute are *factory\_init*, to initialize the device, *echo* to share a message and receive it back exactly the same, *bootmode\_reset* to reset the device, and a special command, *SE3\_MIX*, that tells the device that the packets need further unpacking to be interpreted, and must be sent to dispatcher core to be executed, since it requires low level security operations.

Once the request is served, it is sent to the communication core back, that will implement the interface of the low level read call by the usb driver. This function, called *SE3\_proto\_send*, will receive buffer from the user: if this blocks are not "magic", the firmware must act in a transparent way, providing access to SDcard. Otherwise the firmware will understand whether a data block or a response block from the execution of a command is required and will forward this data.

The dispatcher manages all that is related to the cryptographic part of the device in a very "easy to develop" way, by offering in a simple manner crypto algorithms and the possibility to easily add new ones. Considering that the security core, the smartcard and the fpga must be accessed and programmed within the chip for security reasons, this kind of interface is necessary to achieve the security requirements.

The dispatcher module contains a table of functions that the core cannot perform by itself, but it delegates the dispatcher in order to perform security algorithms or to retrieve security information. So, the core sends a packet with the information about the functions that dispatcher has to execute, and the relative information. For example, if the core sends a cryptographic request, the right function is invoked in the dispatcher and the relative data structure is filled with the right information: in this case they are the algo for the current sessions, the direction (cryption or decryption) and the private key of the user. The latter is searched in the SEKey module and if it's present, the session is allocated and 2 variables are set with the information about the security policies to use and also the hardware module that must compute the task, otherwise it returns an error and abort the operation.

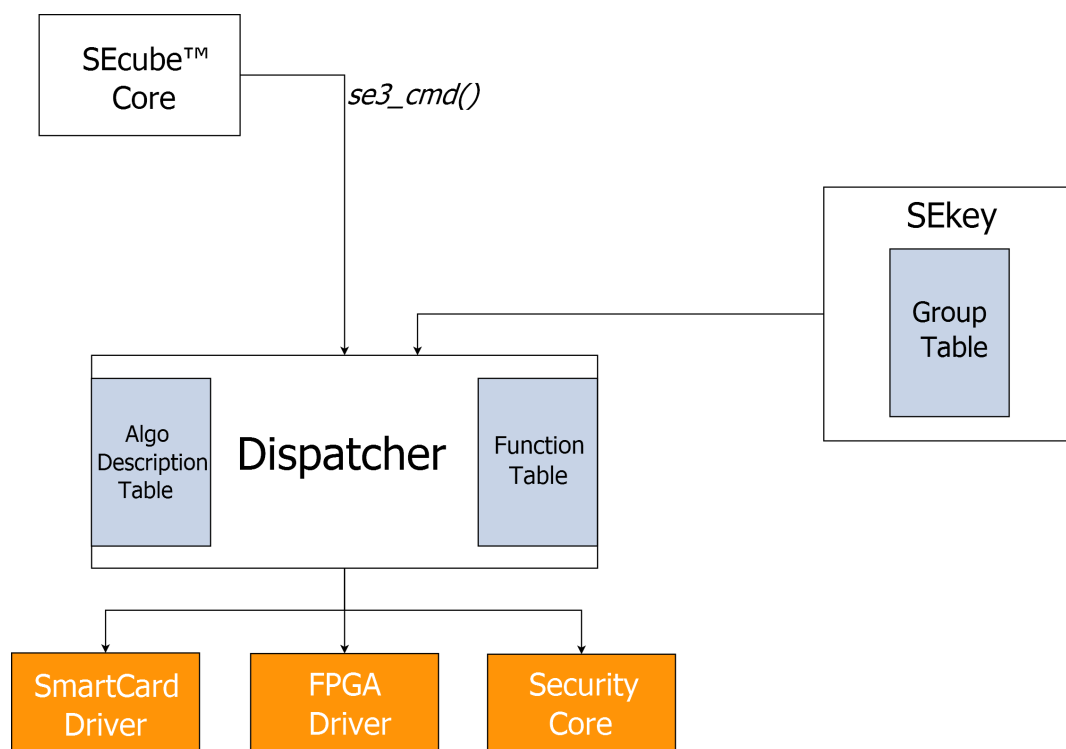


**Figure 1.2:** *SEcube Dispatcher functions.*

Thanks to SEKey module, it's possible to introduce the concept of groups, so every user has its own unique key and also a list of key for each group he belongs to. The group key is used to protect both "static data" (data at rest) and "data transfers" (data in motion) among the members of the group. In particular, the key is usually used, in the former case, to derive "reserved information" used by cryptographic algorithms, and, in the latter one, to set-up secure communication channels. The security policies allow specifying the cryptographic algorithm used to protect the information related to that group and the mechanism to be adopted for generating the session keys.

The group table and which algorithms and hardware to use are decided by the system administrator during the initialization of the device. So SECube provides the possibility of a user mode, who uses the functionalities offered by the device, and a superuser mode, that can configure the device adding algorithms and granting the privileges to next users thanks to SEkey module.

The dispatcher provides the interface to the other cores that can accelerate the execution of crypto algos, i.e. FPGA or Smartcard Applets. This comes in an easy-to-use way: SEkey sets a variable that will be used to select the right security component. This will directly communicate with security core, fpga, or smartcard interface that will return an error if the module is not able to serve the request, or perform the operation if they could.



**Figure 1.3:** SECube Dispatcher interface.

## 2.1 Communication Data Structures

- ***SE3\_COMM\_STATUS comm***: Contains information about host-device communication status and buffers;
- ***se3\_comm\_resp\_header resp\_hdr***: Header buffer containing information about the response to be forwarded to the host.
- ***se3\_comm\_req\_header req\_hdr***: Header buffer containing information about the incoming request buffer. It came filled of information if the incoming blocks are magic.
- ***s3\_storage\_range s3\_storage\_range***: SDIO read/write request buffer context.

## 2.2 Communication Functions

- ***void se3\_communication\_core\_init()***: Initializes the communication core structures;
- ***static bool block\_is\_magic(const uint8\_t\* buf)***: Check if a block of data contains the magic sequence, used to initialize the special protocol file.
- ***static int find\_magic\_index(uint32\_t block)***: Return the index of the corresponding protocol file block, or -1 if the block does not belong to the protocol file. The special protocol file is made up of multiple blocks. Each block is mapped to a block on the physical storage.
- ***static int32\_t se3\_storage\_range\_add(s3\_storage\_range\* range, uint8\_t lun, uint8\_t\* buf, uint32\_t block, enum s3\_storage\_range\_direction direction)***: Used to add requests to SDIO read/write buffer. Contiguous requests are processed with



a single call to the SDIO interface, as soon as a non-contiguous request is added.

- ***void se3\_proto\_request\_reset()***: Reset the protocol request buffer, making the device ready for a new request;
- ***static void handle\_req\_recv(int index, const uint8\_t\* blockdata)***: Handles a single block belonging to a protocol request. The data is stored in the request buffer. As soon as the request data is received completely, the device will start processing the request;
- ***int32\_t se3\_proto\_recv(uint8\_t lun, const uint8\_t\* buf, uint32\_t blk\_addr, uint16\_t blk\_len)***: User-written USB interface that implements the write operation of the driver; it forwards the data on the SD card if the data block does not contain the magic sequence, otherwise the data block is unpacked for further elaborations;
- ***static void handle\_resp\_send(int index, uint8\_t\* blockdata)***: Output a single block of a protocol response. If the response is ready, the data is taken from the response buffer, otherwise the 'not ready' state is returned;
- ***int32\_t se3\_proto\_send(uint8\_t lun, uint8\_t\* buf, uint32\_t blk\_addr, uint16\_t blk\_len)***: User-written USB interface that implements the read operation of the driver; it sends the data on the SD card if the data block does not contain the magic sequence, otherwise it handles the proto request.

### 3.1 SEcube Core Data Structures

- *uint8\_t se3\_session\_buf*: Buffer for sessions;
- *uint8\_t se3\_session\_index*: Array containing index for sessions;

### 3.2 SEcube Core Functions

- *void device\_init()*: Initializes all the useful core components and SEcube firmware architecture, including the communication core and the dispatcher core;
- *void device\_loop()*: endless loop to keep the core listening for any possible requests;
- *void se3\_cmd\_execute()*: unpacks the request buffer to get the command to be executed. It sets the handler if the requested command can be executed by the core itself, otherwise the dispatcher needs to be invoked. This handler is always forwarded to the *se3\_exec* function.
- *static uint16\_t se3\_exec(se3\_cmd\_func handler)*: Here the command is executed with the extracted parameters and the response is prepared with the correct return values. After that this function is invoked, the response buffer is ready to be sent to the host-side. The return value of the function is the number of blocks to be sent.

#### 3.2.1 Command Functions

This section will describe the functions that can be requested by the host-side.

- ***uint16\_t*** *echo(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: Share a message and receive it back exactly the same.
- ***uint16\_t*** *factory\_init(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: Initialize the device.
- ***uint16\_t*** *bootmode\_reset(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: Reset the device.
- ***static uint16\_t*** *invalid\_cmd\_handler(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: handler for invalid command request;

## 4.1 SEcube Core time Functions

- ***void*** *se3\_time\_init()*: It resets the two time variables: *now\_initialized* and *now*;
- ***uint64\_t*** *se3\_time\_get()*: It returns a copy of the variable *now*, because it is a private variable;
- ***void*** *se3\_time\_set(uint64\_t t)*: It sets the variable *now* with the argument value and *now\_initialized* to true.
- ***void*** *se3\_time\_inc()*: It increments timer using software.
- ***bool*** *get\_now\_initialized()*: It returns a copy of the variable *now\_initialized*, because it is a private variable;

## 5.1 Memory Data Structures

- *se3\_mem mem*: It is the structure of a memory allocator;

## 5.2 Memory Functions

- *void se3\_mem\_init(se3\_mem\* mem, size\_t index\_size, uint8\_t\*\* index, size\_t buf\_size, uint8\_t\* buf)*: It initializes the *se3\_mem* structure with the function argument values.
- *static void se3\_mem\_compact(uint8\_t\* p, uint8\_t\* end)*: It resizes the memory allocation of the pointer passed to the function and if it is possible reduce memory allocated.
- *static uint8\_t\* se3\_mem\_defrag(se3\_mem\* mem)*:: It finds the first free block, rebuilds pointer table and then returns first empty block.
- *int32\_t se3\_mem\_alloc(se3\_mem\* mem, size\_t size)*: It allocates the pointer with the size passed on argument;
- *uint8\_t\* se3\_mem\_ptr(se3\_mem\* mem, int32\_t id)*: It returns the pointer to entry in buffer;
- *void se3\_mem\_free(se3\_mem\* mem, int32\_t id)*: It releases the memory entry allocated for the pointer on the arguments;
- *void se3\_mem\_reset(se3\_mem\* mem)*: It releases all the memory entry allocated;

## Flash key structure

Disposition of the fields within the flash node:

0:3 id  
 4:7 validity  
 8:9 data\_size  
 10:11 name\_size  
 12:(12+data\_size-1) data  
 (12+data\_size):(12+data\_size+name\_size-1) name

## 6.1 SE3 Key Data Structures

- *se3\_flash\_key* key: It is the flash key structure of a single node.

## 6.2 SE3 Key Functions

- *bool se3\_key\_find(uint32\_t id, se3\_flash\_it\* it)*: It finds a key in the flash;
- *bool se3\_key\_remove(se3\_flash\_it\* it)*: It removes a key in the flash;
- *bool se3\_key\_new(se3\_flash\_it\* it, se3\_flash\_key\* key)*: It adds a key in the flash;
- *void se3\_key\_read(se3\_flash\_it\* it, se3\_flash\_key\* key)*: It reads a key in the flash;
- *bool se3\_key\_equal(se3\_flash\_it\* it, se3\_flash\_key\* key)*: It checks if a key is equal to a key stored in the flash;

- ***void*** *se3\_key\_read\_data*(*se3\_flash\_it\** *it*, *uint16\_t* *data\_size*, *uint8\_t\** *data*): It reads the key's data from a key node;
- ***bool*** *se3\_key\_write*(*se3\_flash\_it\** *it*, *se3\_flash\_key\** *key*): It writes the key data to a flash node;
- ***void*** *se3\_key\_fingerprint*(*se3\_flash\_key\** *key*, *const uint8\_t\** *salt*, *uint8\_t\** *fingerprint*): It produces a salted key fingerprint;

This module is invoked when the request coming from the host needs operations from security hardwares. It holds information about the login status, the allocated sessions, and tables containing all security functions that can be invoked from the outside. All of the security functions have a precise format, specified by the **se3\_cmd\_func** type.

The dispatcher core will filter and manage all the request intended to the security core, FPGA or Smartcard. All the functions are saved in Matrix of handlers, sized  $SE3\_N\_HARDWARE * SE3\_CMD1\_MAX$ , where  $SE3\_N\_HARDWARE$  is the number of available hardware and  $SE3\_CMD1\_MAX$  is the number of functions, that SEcube can execute.

## 7.1 Dispatcher Data Structures

- **se3\_comm\_req\_header** *req\_hdr*: Header buffer containing information about the security function. It needs to be decrypted.
- **SE3\_LOGIN\_STATUS** *login\_struct*: Contains the useful status data for login operations;
- **se3\_cmd\_func** *handlers*: Table of function where all the possible operation of SECube are stored. It is organized as a matrix where at each row corresponds a different hardware or software implementation;

## 7.2 Dispatcher Functions

- **void** *se3\_dispatcher\_init()*: initializes the dispatcher data structures and the security components (Security core, FPGA, Smartcard) and sets the access level for all the users, both in read and write;



- ***void*** *set\_req\_hdr(se3\_comm\_req\_header req\_hdr\_i)*: sets the req\_hdr data structure to the structure passed as parameter;
- ***static void*** *login\_cleanup()*: clean the security sessions information and access data;

### 7.2.1 Command Functions

- ***uint16\_t*** *dispatcher\_call(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: executes a security function, by using the implementation chosen by SEkey.
- ***uint16\_t*** *config(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: set or get configuration record from the request buffer;
- ***uint16\_t*** *key\_edit(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: insert, delete or update key;
- ***uint16\_t*** *key\_list(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: list all keys in device;
- ***uint16\_t*** *challenge(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: Get a login challenge from the server for the authentication;
- ***uint16\_t*** *login(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: respond to challenge, completing login operation and the authentication;
- ***uint16\_t*** *logout(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: Log out and release resources; set or get configuration record from the request buffer
- ***uint16\_t*** *error(uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp)*: handler for invalid command request;

The following functions are provided to interface the Dispatcher Core to the Key Manager SEkey.

## 8.1 SEkey Function

- ***bool sekey\_get\_implementation\_info(uint8\_t\* algo\_implementation, uint8\_t\* crypto\_algo, uint8\_t\* key)***: given a key, if it is present inside the SEkey key list this function will return the possible security component that the key allows you to use and the security algorithm that should be performed;
- ***bool sekey\_get\_auth(uint8\_t \*key)***: given a key, if it is present inside the SEkey key list a true boolean value is return, false otherwise;

## Cryptographic Security Components

All the following components implement their own cryptographic algorithm basing on the component nature.

### 9.1 Security Core

The security core is the microprocessor-based cryptographic component able to encrypt or decrypt data, initialize and manage the cryptographic algorithms and the relative context for each session.

#### 9.1.1 Security Core Data Structures

- *se3\_algo\_descriptor* *algo\_table* [SE3\_ALGO\_MAX]: this array contains the Cryptographic algorithm handlers and information about the contained algorithms (init and update functions, name of algorithm, ...).
- *SE3\_SECURITY\_INFO* *se3\_security\_info*: contains information about the security sessions, including the algorithm for each session and the records;

#### 9.1.2 Security Core Functions

- *void* *se3\_security\_core\_init*(): Initializes the Security Core structure;
- *static bool* *record\_find*(*uint16\_t* *record\_type*, *se3\_flash\_it\** *it*): Given a record type (user pin or admin pin), it returns a true boolean value if the type is found in the flash memory and the passing iterator is set to the memory location;
- *bool* *record\_set*(*uint16\_t* *type*, *const uint8\_t\** *data*): Set data of a record; if a flash operation fails, the Hwerror flag is set;

- **bool** *record\_get*(*uint16\_t type, uint8\_t\* data*): get data from a record; the data will be returned by using the *data* pointer;
- **uint16\_t** *crypto\_init*(*uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp*): Initialize a crypto context; it sets the request parameters from the *req* buffer (algo, mode, key\_id, session ID) by checking whether the read data are consistent with the expectable ones;
- **uint16\_t** *crypto\_update*(*uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp*): Use a crypto context, by calling the update function relative to the chosen algorithm;
- **uint16\_t** *crypto\_set\_time*(*uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp*): Set device time for key validity;
- **uint16\_t** *crypto\_list*(*uint16\_t req\_size, const uint8\_t\* req, uint16\_t\* resp\_size, uint8\_t\* resp*): Get list of available algorithms;
- **void** *se3\_payload\_cryptoinit*(*se3\_payload\_cryptoctx\* ctx, const uint8\_t\* key*): Initialize the crypto algorithms;
- **bool** *se3\_payload\_encrypt*(*se3\_payload\_cryptoctx\* ctx, uint8\_t\* auth, uint8\_t\* iv, uint8\_t\* data, uint16\_t nblocks, uint16\_t flags, uint8\_t crypto\_algo*): encrypt the *data* buffer by using the algorithm assigned by SEkey decribed by the *crypto\_algo* input parameter;
- **bool** *se3\_payload\_decrypt*(*se3\_payload\_cryptoctx\* ctx, const uint8\_t\* auth, const uint8\_t\* iv, uint8\_t\* data, uint16\_t nblocks, uint16\_t flags, uint8\_t crypto\_algo*): decrypt the *data* buffer by using the algorithm assigned by SEkey decribed by the *crypto\_algo* input parameter;

## 9.2 FPGA

In order to add functions related to FPGA HW component, the corresponding Dispatcher Matrix handler column must be filled and the function must be developed inside the *se3\_FPGA.c* file. Furthermore, additional information on the Key Manager SEkey must be provided to allow the functioning of the security component.

## 9.3 Smartcard

In order to add functions related to Smartcard HW component, the corresponding Dispatcher Matrix handler column must be filled and the function must be developed inside the *se3\_smartcard.c* file. Furthermore, additional information on the Key Manager SEkey must be provided to allow the functioning of the security component.

# 10

## Flash

Structure of flash:

0:31 magic  
32:2047 index  
2048:131071 data

The data section is divided into 2016 64-byte blocks. Each byte of the index stores the type of the corresponding data block. A special value (SE3\_FLASH\_TYPE\_CONT) indicates that the block is the continuation of the previous one. If the block is invalid, its type is 0. If it has not been written yet, the type is 0xFF.

### 10.1 Flash Data Structures

- *se3\_flash\_it it*: Data structure to iterate over the on-chip flash memory. It contains the node address, its size, the type of the block, and the relative offset.
- *SE3\_FLASH\_INFO flash*: Data structure that contains informations about the active sector number, the base address, the type of the block, the data, the first free position, the value of memory used and allocated.

### 10.2 Flash Functions

- *bool se3\_flash\_init()*: Initialize flash;
- *void se3\_flash\_it\_init(se3\_flash\_it\* it)*: Initialize flash iterator;
- *bool se3\_flash\_it\_next(se3\_flash\_it\* it)*: Increment flash iterator;

- ***bool se3\_flash\_it\_new(se3\_flash\_it\* it, uint8\_t type, uint16\_t size)***: Allocate new node in the flash and points the iterator to the new node;
- ***bool se3\_flash\_it\_write(se3\_flash\_it\* it, uint16\_t off, const uint8\_t\* data, uint16\_t size)***: Write to flash node;
- ***bool se3\_flash\_it\_delete(se3\_flash\_it\* it)***: Delete flash node;
- ***bool se3\_flash\_pos\_delete(size\_t pos)***: Delete flash node by index;
- ***size\_t se3\_flash\_unused()***: Get unused space in the flash memory, including the space marked as invalid. If space is available, it does not mean that flash sectors will not be swapped. It return unused space in bytes;
- ***bool se3\_flash\_canfit(size\_t size)***: Check if there is enough space for new node;
- ***void se3\_flash\_info\_setup(uint32\_t sector, const uint8\_t\* base)***: Initializes the structures for flash management, selecting a sector and its base address;
- ***bool se3\_flash\_bootmode\_reset(uint32\_t addr, size\_t size)***: Reset the USEcube device to boot mode by erasing the signature - zeroise.
- ***static bool flash\_fill(uint32\_t addr, uint8\_t val, size\_t size)***: Fill the flash memory cells with the value val starting from the address addr for size value;
- ***static bool flash\_zero(uint32\_t addr, size\_t size)***: Fill the flash memory cells with the value 0 starting from the address addr for size value;
- ***static bool flash\_program(uint32\_t addr, const uint8\_t\* data, size\_t size)***: Write the values contained in \*data starting from the address addr for size value;
- ***static bool flash\_erase(uint32\_t sector)***: Erase the selected sector of the memory;
- ***static bool flash\_swap()***: Swap the sector



The `se3_common.c` file contains all the data structures and functions that are shared among all the components. It implies the developed Debug functions.

**WARNING:** The developed functions were tested and validated by using a **16 GB FAT32** SD card.

## 11.1 Common Data Structures

- *SE3\_SERIAL serial*: Indicates whether the serial number has been set (by `FACTORY_INIT`);
- *uint8\_t se3\_magic[SE3\_MAGIC\_SIZE]*: It contains the magic sequence;

## 11.2 Common Functions

- *uint16\_t se3\_req\_len\_data(uint16\_t len\_data\_and\_headers)*: Compute length of data in a request in terms of `SE3_COMM_BLOCK` blocks;
- *uint16\_t se3\_req\_len\_data\_and\_headers(uint16\_t len\_data)*: Compute length of data in a request accounting for headers;
- *uint16\_t se3\_resp\_len\_data(uint16\_t len\_data\_and\_headers)*: Compute length of data in a response in terms of `SE3_COMM_BLOCK` blocks;
- *uint16\_t se3\_resp\_len\_data\_and\_headers(uint16\_t len\_data)*: Compute length of data in a response accounting for headers;
- *uint16\_t se3\_nblocks(uint16\_t len)*: Compute number of `SE3_COMM_BLOCK` blocks, given length in Bytes;

### 11.2.1 Debug Tool Functions

In the following, the developed functions usage and a simple practical example are presented:

- **bool** *se3\_create\_log\_file()*: Create the user-accessible file on the SD card, called *se3\_trace\_log.txt* used as trace log buffer. For sake of simplicity, the buffer will have a fixed (oversized) dimension, able to acquire about ten thousands debug strings;
- **bool** *se3\_debug\_sd\_flush(uint32\_t start\_address, uint32\_t end\_address)*: flushes the data contained in the addresses between *start\_address* and *end\_address*; it's also called inside *se3\_create\_log\_file()* to erase the content of the previous trace file;
- **char\*** *se3\_debug\_create\_string(char\* string)*: prepares the input string to be written in the SD card; the output string will be passed to the *se3\_write\_trace(char\* buf, uint32\_t blk\_addr)* function;
- **bool** *se3\_write\_trace(char\* buf, uint32\_t blk\_addr)*: writes the string buffer *buf* in the address *blk\_addr* of the SD card. In the developed code, a global variable, *debug\_address*, is used to write the strings one after the other following a time line, in order to keep track of the temporal dependencies among the function calls.

```
1 //creates the se3_trace_log.txt file and flushes it
2 se3_create_log_file();
3
4 /* write string on SD card on address 'debug_address' and updates the
   address for the next write operation */
5 se3_write_trace(se3_debug_create_string("\nCiao!\0"), debug_address++);
6
7 //Flushes the first 100 strings of the previous debug execution
8 se3_debug_sd_flush(DATA_BASE_ADDRESS, DATA_BASE_ADDRESS+100);
```