

A cluster of various spheres in white, gold, and blue with horizontal stripes, arranged in a group on the left side of the slide.

Computer organization

Lab2

Assembly language-MIPS(1)

Load & Store

Machine Language & Assembly Language

- **Machine Instruction** : a **binary representation** used for communication with a computer system.
- **Assembly Instruction**: a **symbolic representation** of machine language
- **Assembler**: translate assembly codes into binary instructions

Data	Text
Text	
User Text Segment [00400000]..[00440000]	
[00400000]	8fa40000 lw \$4, 0(\$29) ; 183: lw \$a0 0(\$sp) # argc
[00400004]	27a50004 addiu \$5, \$29, 4 ; 184: addiu \$a1 \$sp 4 # argv
[00400008]	24a60004 addiu \$6, \$5, 4 ; 185: addiu \$a2 \$a1 4 # envp
[0040000c]	00041080 sll \$2, \$4, 2 ; 186: sll \$v0 \$a0 2
[00400010]	00c23021 addu \$6, \$6, \$2 ; 187: addu \$a2 \$a2 \$v0
[00400014]	0c100009 jal 0x00400024 [main] ; 188: jal main



Assembly Language based on MIPS

➤ Data declaration

- Data declaration section starts with “**. data**”.
- The declaration means **a piece of memory is required to be allocated**. The declaration usually includes **lable** (name of address on this meomory unit), **size**(optional), and **initial value**(optional).

➤ Code definition

- Code definition starts with “**.text**”, includes **basic instructions, extended instructions, labels of the code**(optional). At the end of the code, “**exit**” system service should be called.

- **Comments:** Comments start from “**#**” till the end of current line



Data Types and Literals

In MIPS32

➤ Q: What's the size of Register in MIPS32?

➤ Unit Conversion

➤ **1 word = 32bit = 2*half word(2*16bit) = 4* byte(4*8bit)**

➤ Instruction and Data Storage:

➤ **Instructions are all 32 bits(1 word)**

➤ **A character requires 1 byte of storage**

➤ Literals:

➤ **Characters enclosed in single quotes. e.g. 'C'**

➤ **Strings enclosed in double quotes. E.g. "a String"**

➤ **Numbers in code. e.g. 10**





Data Declaration

name:	storage_type	value(s)
-------	--------------	----------

example

```
var1:      .word   3          # create a single integer:
                                #variable with initial value 3

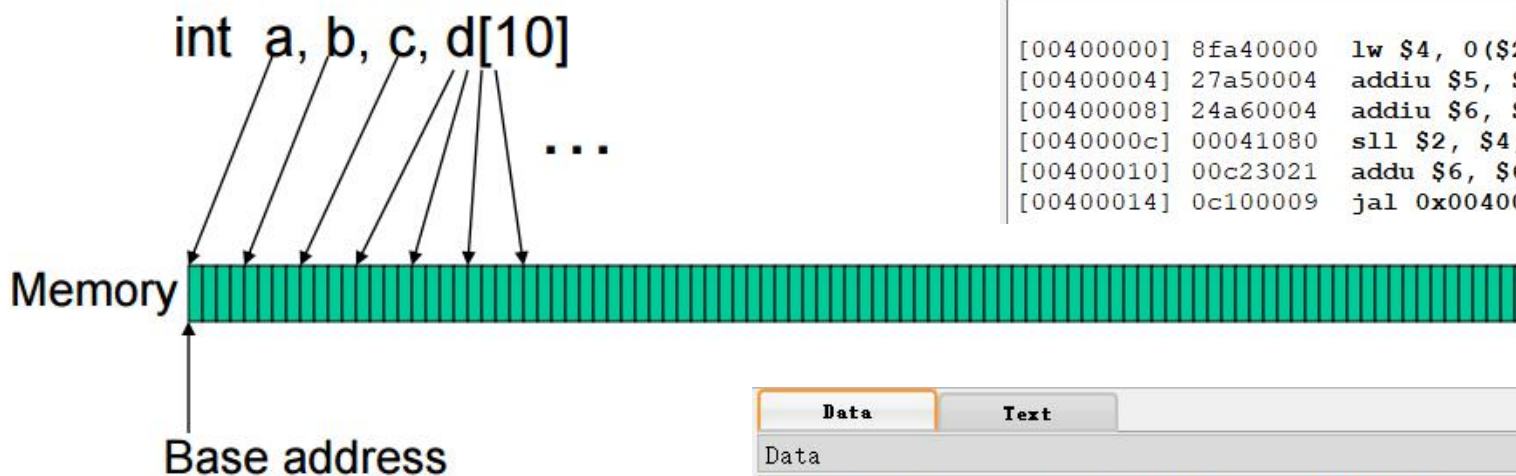
array1:    .byte   'a','b'    # create a 2-element character
                                # array with elements initialized:
                                # to a and b

array2:    .space  40         # allocate 40 consecutive bytes,
                                # with storage uninitialized
                                # could be used as a 40-element
                                # character array, or a
                                # 10-element integer array;
                                # a comment should indicate it.

string1:   .asciiz "Print this.\n"      #declare a string
```


Memory

- The compiler organizes data in memory... it knows the location of every variable (saved in a table)... it can fill in the appropriate mem-address for load-store instructions



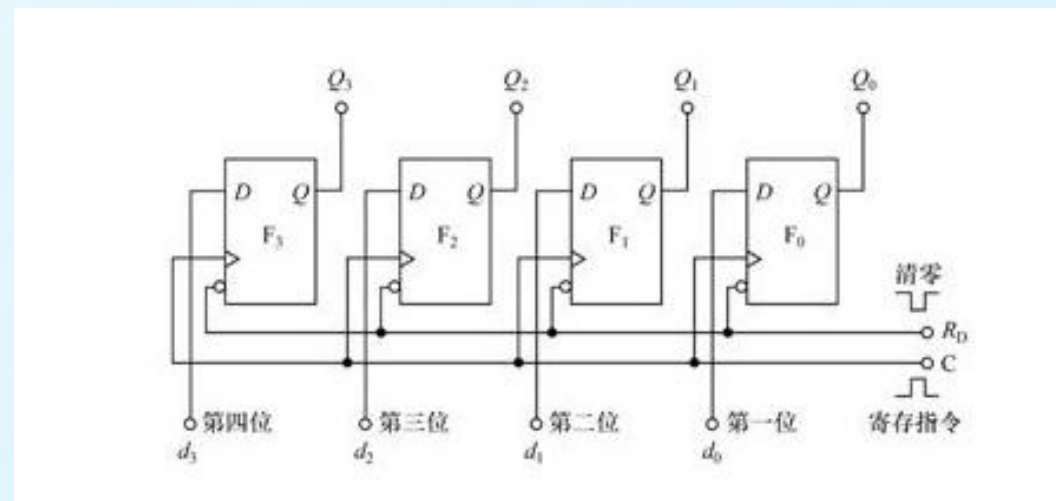
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Data	Text
Data	
User data segment [10000000]..[10040000]	
[10000000]..[1000ffff]	00000000
[10010000]	6c6c6568 6e49206f 6e726574 000a7465 h e l l o I n t e r n e t . .
[10010010]..[1003ffff]	00000000

Register (1)

- **Registers** are small storage areas used to store data in the CPU, which are used to temporarily store the data and results involved in the operation.
- All MIPS **arithmetic** instructions **MUST** operate on registers
- The **size of registers** in MIPS32 is **32 bits**

Registers	Coproc 1	Coproc 0	
Name	Number		Value
\$zero	0		0x00000000
\$at	1		0x10010000
\$v0	2		0x0000000a
\$v1	3		0x00000000
\$a0	4		0x10010000
\$a1	5		0x00000000
\$a2	6		0x00000000
\$a3	7		0x00000000
\$t0	8		0x00000063
\$t1	9		0x00000000





Register (2)

REGISTER NAME, NUMBER, USE, CALL CONVENTION

NAME	NUMBER	USE	PRESERVED ACROSS A CALL?
\$zero	0	The Constant Value 0	N.A.
\$at	1	Assembler Temporary	No
\$v0-\$v1	2-3	Values for Function Results and Expression Evaluation	No
\$a0-\$a3	4-7	Arguments	No
\$t0-\$t7	8-15	Temporaries	No
\$s0-\$s7	16-23	Saved Temporaries	Yes
\$t8-\$t9	24-25	Temporaries	No
\$k0-\$k1	26-27	Reserved for OS Kernel	No
\$gp	28	Global Pointer	Yes
\$sp	29	Stack Pointer	Yes
\$fp	30	Frame Pointer	Yes
\$ra	31	Return Address	No



MIPS Instruction: Load & Store

- In MIPS
 - Access the data in **memory** could ONLY be invoked by two types of instruction: **load** and **store**.
 - All the **calculation** are based on the data in **Registers**.

Name	Example	Comments
32 registers	<code>\$s0-\$s7, \$t0-\$t9, \$zero, \$a0-\$a3, \$v0-\$v1, \$gp, \$fp, \$sp, \$ra, \$at</code>	Fast locations for data. In MIPS, data must be in registers to perform arithmetic, register <code>\$zero</code> always equals 0, and register <code>\$at</code> is reserved by the assembler to handle large constants.
2^{30} memory words	<code>Memory[0], Memory[4], ..., Memory[4294967292]</code>	Accessed only by data transfer instructions. MIPS uses byte addresses, so sequential word addresses differ by 4. Memory holds data structures, arrays, and spilled registers.



Load (Load to Register)

```
lw      register_destination, RAM_source
        # copy word (4 bytes) at
        # source_RAM location
        # to destination register.
        # load word -> lw

lb      register_destination, RAM_source
        # copy byte at source RAM
        # location to low-order byte of
        # destination register,
        # and sign -e.g. tend to
        # higher-order bytes
        # load byte -> lb

li      register_destination, value
        #load immediate value into
        #destination register
        #load immediate --> li
```



Store (Store to Memory)

```
sw      register_source, RAM_destination
        #store word in source register
        # into RAM destination

sb      register_source, RAM_destination
        #store byte (low-order) in
        #source register into RAM
        #destination
```



The Address of the Target Unit in the Memory

- **The address is the value of “label”**

- The value of “label” is determined by the Assembler according to the assembly source code.

- **The address need to be got from the Register**

- Using the content in register as address.

- **The address need to be caculated by Baseline + offset**

- Using the sum of the baseline address and offset as address.



The Address is the value of “label”

- Load the address which is labeled by “var1” into the Register “t0” .

```
la      $t0, var1
```

- NOTIC : “var1” could be either a label of data or a label of a MIPS instruction.

```
.data
str:    .asciiz "the answer = "
.text
li $v0, 4          # system call code for print_str
la $a0, str         # address of string to print
syscall           # print the string
```



The Address need to be got from the Register

- **Load** the word from the memory unit whose address is in the register “t0” to the register “t2”.

```
lw          $t2, ($t0)
```

- **Store** the word from the register “t2” to the memory unit whose address is in the register “t0”.

```
sw          $t2, ($t0)
```



The Address is caculated by Baseline + offset

- Load the word from the memory unit whose **address is in the sum of 4 and the value in register "t0"** to the register "t2" .

```
lw          $t2, 4($t0)
```

- Store the word in register "t2" to the memory unit whose **address is the sum of -12 and the value in the register "t0"** .

```
sw          $t2, -12($t0)
```

- **Baselin + offset** is applied widely while process on the Array and stack.

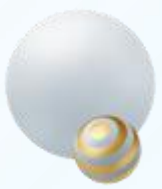
Demo1

Q: what's the output of the code?

```
Edit Execute
lab2_ls.asm
1  .data
2      str: .asciiz "A"
3  .text
4  main:
5      la $a0, str
6      li $v0, 4
7      syscall
8      lb $t0, ($a0)
9      addi $t0, $t0, 32
10     sb $t0, str
11     syscall
12     li $v0, 10
13     syscall
```

```
.data
      arrayx: .word 11
.text
main:
      lw $a0, arrayx
      li $v0, 1
      syscall
      li $v0, 10
      syscall
```

```
.data
      arrayx: .word 11
.text
main:
      la $t0, arrayx
      move $a0, $t0
      li $v0, 1
      syscall
      li $v0, 10
      syscall
```

Arithmetic Instructions(1)

add	\$t0,\$t1,\$t2	# \$t0 = \$t1 + \$t2; add as signed # (2's complement) integers
sub	\$t2,\$t3,\$t4	# \$t2 = \$t3 - \$t4
addi	\$t2,\$t3, 5	# \$t2 = \$t3 + 5; "add immediate" # (no sub immediate)
addu	\$t1,\$t6,\$t7	# \$t1 = \$t6 + \$t7;
addu	\$t1,\$t6,5	# \$t1 = \$t6 + 5; # add as unsigned integers
subu	\$t1,\$t6,\$t7	# \$t1 = \$t6 - \$t7;
subu	\$t1,\$t6,5	# \$t1 = \$t6 - 5 # subtract as unsigned integers



Arithmetic Instructions(2)

mult	\$t3,\$t4	# multiply 32-bit quantities in \$t3 # and \$t4, and store 64-bit # result in special registers Lo # and Hi: (Hi,Lo) = \$t3 * \$t4
div	\$t5,\$t6	# Lo = \$t5 / \$t6 (integer quotient) # Hi = \$t5 mod \$t6 (remainder)
mfhi	\$t0	# move quantity in special register Hi # to \$t0: \$t0 = Hi
mflo	\$t1	# move quantity in special register Lo # to \$t1: \$t1 = Lo, used to get at # result of product or quotient



Demo2

```
.data
str1: .ascii "13/4 quotient is: "
str2: .ascii " , remainder is : "

.text
main:
    la $a0, str1
    li $v0, 4
    syscall

    li $t0, 13
    li $t1, 4
    divu $t0, $t1
    mflo $a0
    li $v0, 1
    syscall

    la $a0, str2
    li $v0, 4
    syscall

    mfhi $a0
    li $v0, 1
    syscall

    li $v0, 10
    syscall
```

Q1: What's processing of "mflo" and "mfhi" ?

Q2: What's the usage of registers "hi" and "lo"?

Q3: What's the value of "str2" ?

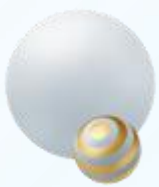
Q4: Is there any instruction doing "move to" ?

Q5: What's the output of this demo?

Demo 3

answer the questions in the comments of following code

```
1 .data
2     name:    .space 16      #malloc 16 byte , not initialize ##### name value : 0x10010000
3     mick:    .ascii "mick\n" # malloc 4+1 = 5byte = 5 * ascii(byte)
4     alice:   .asciiz "alice\n" ##### what's the value of alice ?
5     tony:    .asciiz "tony\n" ##### what's the value of tony ?
6     chen:    .asciiz "chen\n"
7
8 .text
9 main:
10     la $t0,name           #using name value which is an address, load this address to $t0
11
12     la $t1,mick
13     sw $t1,($t0)          #1,get value of $t0, use it as the address of a piece of memory
14     la $t1,alice
15     sw $t1,4($t0)         #baseline : the content of $t0 , offset :4
16     la $t1,tony
17     sw $t1,8($t0)
18     la $t1,chen
19     sw $t1,12($t0)
20
21     li $v0,4
22     lw $a0,0($t0)
23     syscall               #what's the output while this syscall is done
24
25     li $v0,10
26     syscall
```

=====in JAVA/C=====

```
int  i,j,k;           //i,j,k are labels of address in memory  
  
k = i+j;
```

=====in MIPS=====

1) i,j(memory) ----->>>> \$t0,\$t1 (register)

load (target: Register): lb lw lh ld ; li , la.

2) add \$t2,\$t0,\$t1

3) \$t2 (register) ----->>>> k(memory)

store (target: Memory) : sb sw sh sd.

