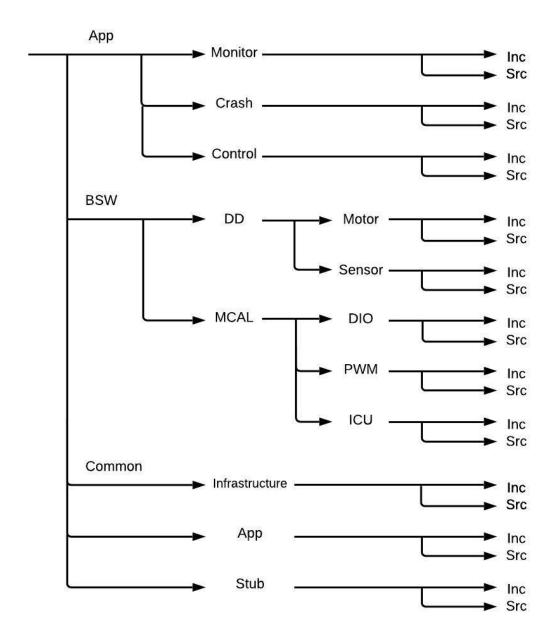
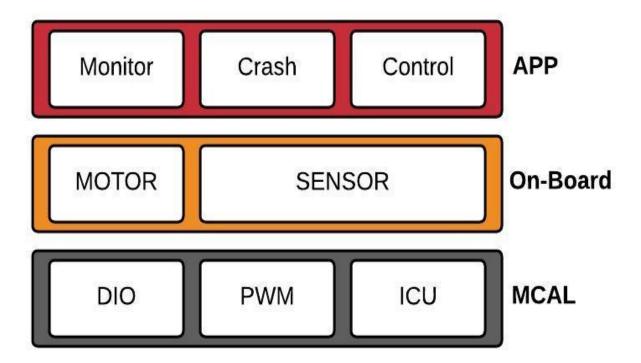


### Folder Structure



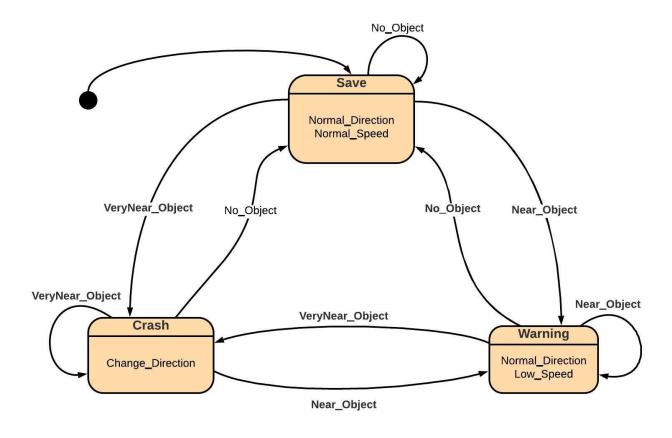
# 1- Static Design

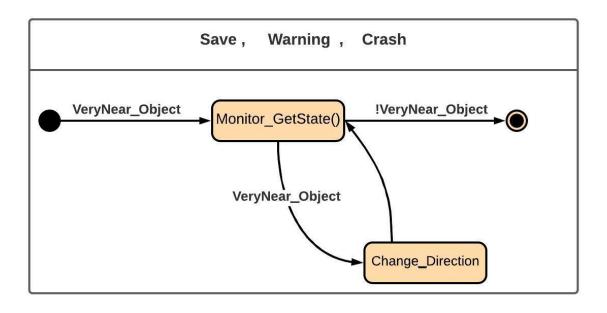
## **Layered Architecture:**



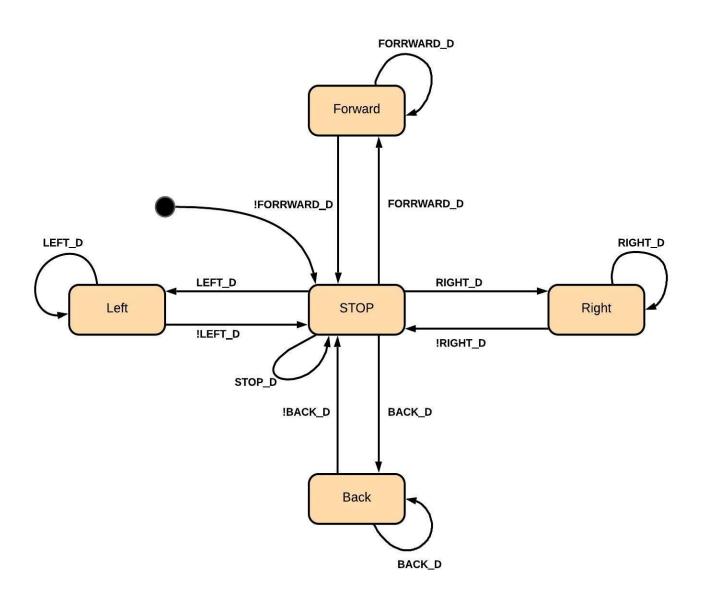
## 2-Dynamic Design

#### A- Crash module state machine:



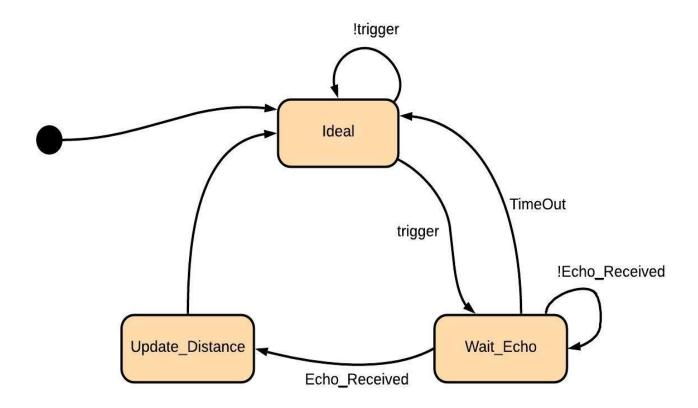


#### **B- Control module state machine:**

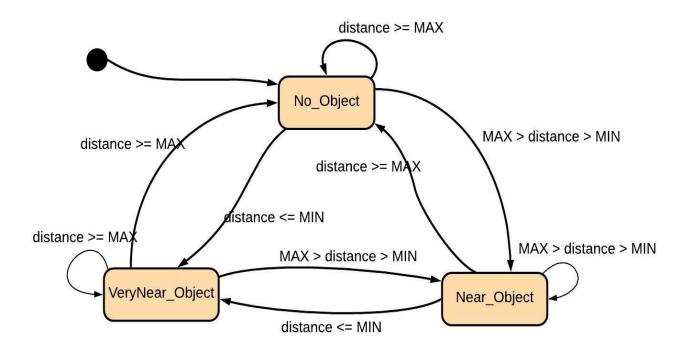


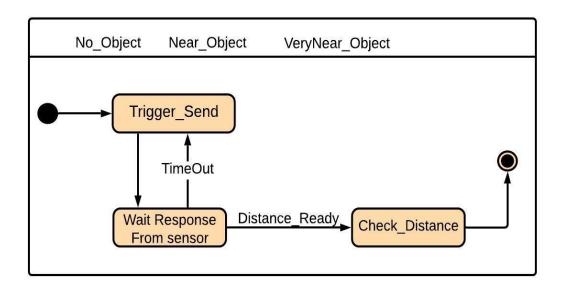
#### **C- Monitor stack state machine :**

1- Sensor module state machine:



#### 2- Monitor module state machine:





# **D- Sequence Diagram**

