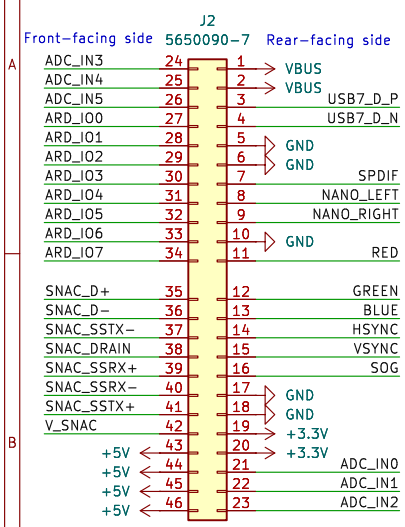
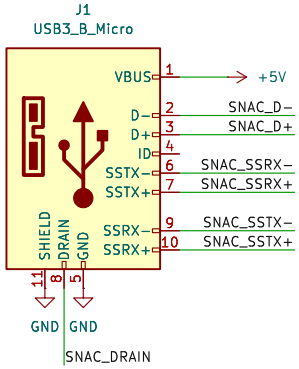
Note that mistakes were made with net names on the original Multisystem schematic up to and including Issue 4. The correct connections are detailed below:

Signal name	Multisystem net should be	Erroneous Multisystem net	Multisystem USB 3.0 Pin
RX	D+	D+	3
TX	D−	D−	2
RTS	SSTX−	SSRX+	8
CTS	DRAIN	DRAIN	7
DTR	SSRX+	SSTX−	6
DSR	SSRX−	SSTX+	5
IO6	SSTX+	SSRX−	9

Additional notes:
V_SNAC is the voltage selected with the switch on the Multisystem (3.3V or 5V) and the voltage level of all the SNAC pins is shifted within the Multisystem to the selected voltage.
SOG is a 'sync on green' signal generated by the MiSTer core (not by the Multisystem PCB) and originates directly from pin 9 of the Arduino header which also carries the unshifted SNAC signals. The SOG signal is not level shifted.
VBUS is the overcurrent−protected 5V from the USB overcurrent protection IC.
NANO_LEFT and NANO_RIGHT are unfiltered audio outputs directly from the DE10.





TP1 and TP2 can be used with a wire link to strengthen the USB connector

USB3.0B − Edge
D− − 36
D+ − 35
STDA_SSTX− − 40
GND_DRAIN − 38
STDA_SSRX− − 37
STDA_SSTX+ − 39
VBUS − 43−46
GND − 5,6,17,18

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