Note that mistakes were made with net names on the original Multisystem schematic up to and including Issue 4. The correct connections are detailed below: Signal Multisystem net Erroneous Erroneous Multisystem Multisystem net USB 3.0 Pin should be Front-facing side 5650090-7 Rear-facing side D+ ADC_IN3 TX → VBUS 2 > VBUS RTS SSTX-SSRX+ ADC_IN4 DRAIN DRAIN CTS USB7_D_P ADC_IN5 26 SSRX+ SSTX-ARD_IO0 27 USB7_D_N DSR SSRX-ARD_IO1 28 5 GND 106 SSTX+ SSRX-6 GND ARD_IO2 29 ARD_IO3 30 SPDIF NANO_LEFT Additional notes: ARD_IO4 31 8 V_SNAC is the voltage selected with the switch on the Multisystem (3.3V or 5V) 32 33 ARD_IO5 NANO_RIGHT and the voltage level of all the SNAC pins is shifted within the Multisystem 10 GND ARD_IO6 to the selected voltage. 34 ARD_IO7 SOG is a 'sync on green' signal generated by the MiSTer core (not by the Multisystem PCB) and originates directly from pin 9 of the Arduino header which also carries the unshifted SNAC signals. The SOG signal is not level shifted. RED SNAC_D+ VBUS is the overcurrent-protected 5V from the USB overcurrent protection IC. SNAC_D-36 13 BLUE NANO_LEFT and NANO_RIGHT are unfiltered audio outputs directly from the DE10. SNAC_SSTX-37 HSYNC SNAC_DRAIN 38 VSYNC 15 SNAC_SSRX+ 39 16 SOG SNAC_SSRX-40 17 GND 18 GND SNAC_SSTX+ 41 19 +3.3V V_SNAC 20 > +3.3V +5٧ ADC_INO 44 +5∀ ← ADC_IN1 +5∀ ← ADC_IN2 J1 USB3_B_Micro VBUS**□** → +5V SNAC D-SNAC_D+ D+c ID= SNAC_SSRX-SSTX-6 SNAC_SSRX+ SSTX+= SHIELD DRAIN GND SNAC_SSTX-SSRX-10 SNAC_SSTX+ SSRX+= 7 0 \rightarrow GND GND SNAC_DRAIN TP1 **♦** GND GND TP1 and TP2 can be used with a wire link to strengthen the USB connector USB3.0B – Edge – 36 - 35 STDA_SSTX- - 40 GND_DRAIN - 38 STDA_SSRX- - 37 STDA_SSTX+ - 39 Sheet: / File: Multisystem-Cart.kicad_sch Title: Title goes here - 43-46 VBUS Size: A4 GND - 5,6,17,18 Date: Rev: 1 KiCad E.D.A. 8.0.8 ld: 1/1