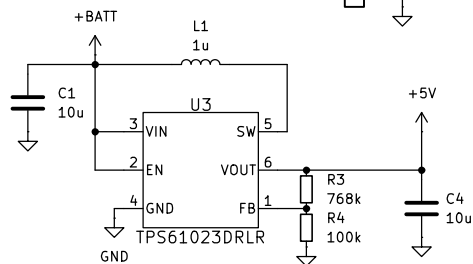
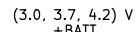


Power

The diagram illustrates the power supply section of a Raspberry Pi 4B. It features a +5V input from a USB-C port, connected to a 5V regulator (U3, TPS61023DRLR). The regulator has a 10uF capacitor (C1) on its input and a 10uF capacitor (C4) on its output. The output is connected to a 5V pin header (J1, PWR) and a 5V pin header (J2, 5V). The regulator's feedback pin (FB) is connected to a 100k resistor (R4) to ground and a 768k resistor (R3) to the output. The regulator's SW pin (5) is connected to an inductor (L1, 1uH) and a 10uF capacitor (C2) to ground. The regulator's EN pin (2) is connected to a 10k resistor (R1) to ground and a 10k resistor (R2) to the output. The regulator's VIN pin (3) is connected to the input and the output. The regulator's GND pin (4) is connected to ground. The output is also connected to a 5V pin header (J2, 5V).

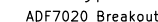
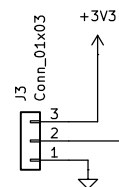
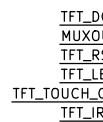
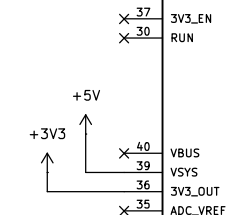
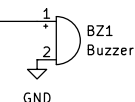


Pinout diagram for the ADF7020 Breakout board:

Pin	Function
1	DATA_CLK
2	INT/LOCK
3	SCLK
4	SDATA
5	MUXOUT
6	
7	
8	CLKOUT
9	DATA I/O
10	DATA I/O
11	ADF_ADCINP
12	SREAD
13	SLE
14	CE_70XX

Power connections: +3V3 to Pin 1, GND to Pin 8.

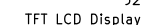
Board label: J4 ADF7020 Breakout

[illegible]

Header for LHM2121 Evaluation Board

Pinout diagram for the TFT LCD Display (J2):

- Pin 1: +5V
- Pin 2: (Unconnected)
- Pin 3: TFT_CS
- Pin 4: TFT_RST
- Pin 5: TFT_DC
- Pin 6: TFT_SDI
- Pin 7: TFT_SCK
- Pin 8: TFT_LED
- Pin 9: TFT_SDO
- Pin 10: TFT_SCK
- Pin 11: TFT_TOUCH_CS
- Pin 12: TFT_SDI
- Pin 13: TFT_SDO
- Pin 14: TFT_JRQ



LCD