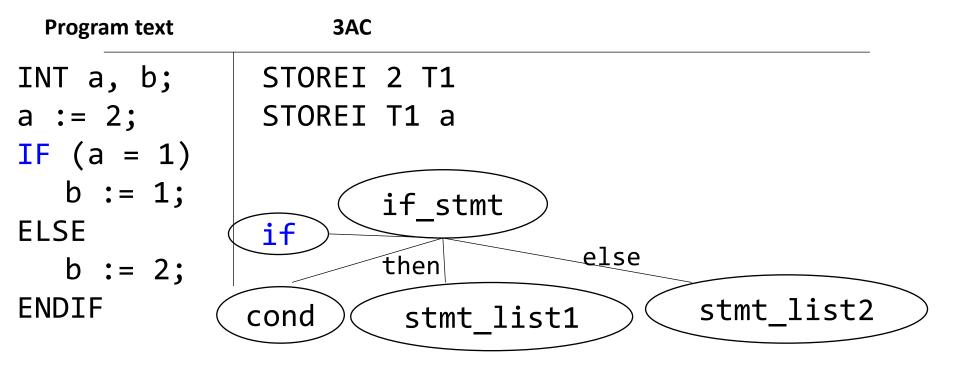
CS406: Compilers Spring 2022

Week 7: Semantic Processing: Intermediate Code Generation (if, do-while, for)

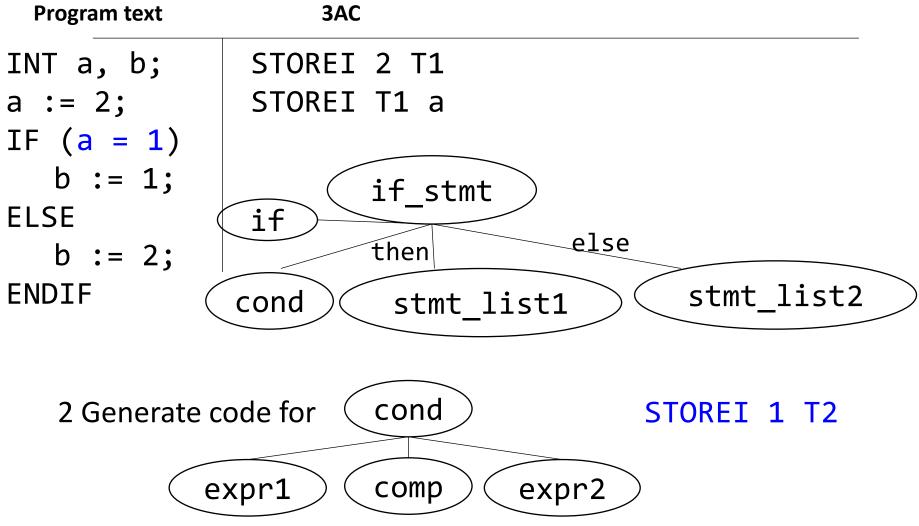
CS406, IIT Dharwad

If construct with semantic actions

- If_stmt->if #start_if <b_expr> #testif then
 <stmt_list> <else_part> endif; #gen_out_label
- Else_part->else #gen_jump #gen_else_label<stmt list>

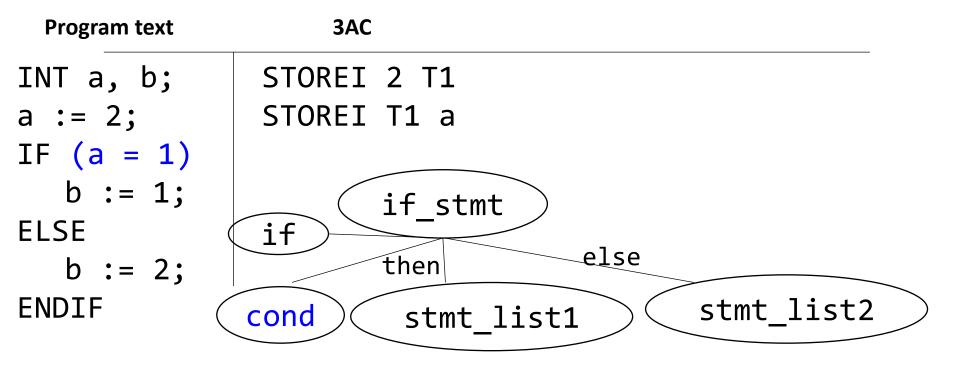


1 Generate out label and store it in semantic record of if_stmt(label1) The #start if routine is responsible for this



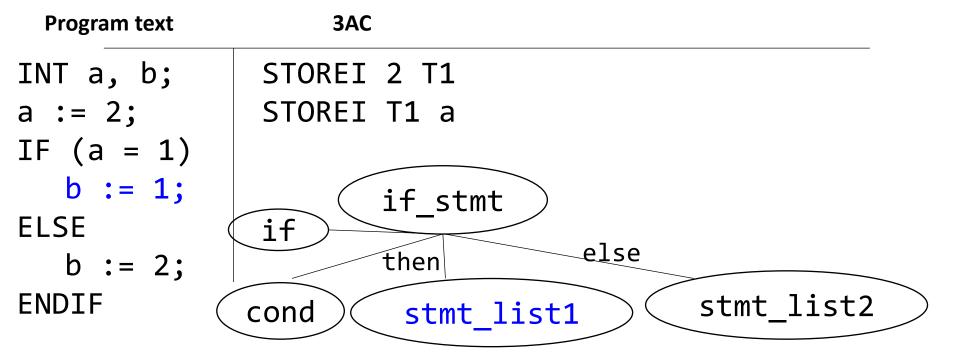
Program text 3AC STOREI 2 T1 INT a, b; a := 2;STOREI T1 a IF (a = 1)b := 1;if stmt **ELSE** if else then b := 2;**ENDIF** stmt list2 cond stmt list1

2. Store the result of calling process_op, STOREI 1 T2 where op is "=", in the node cond (bool_expr1=false)

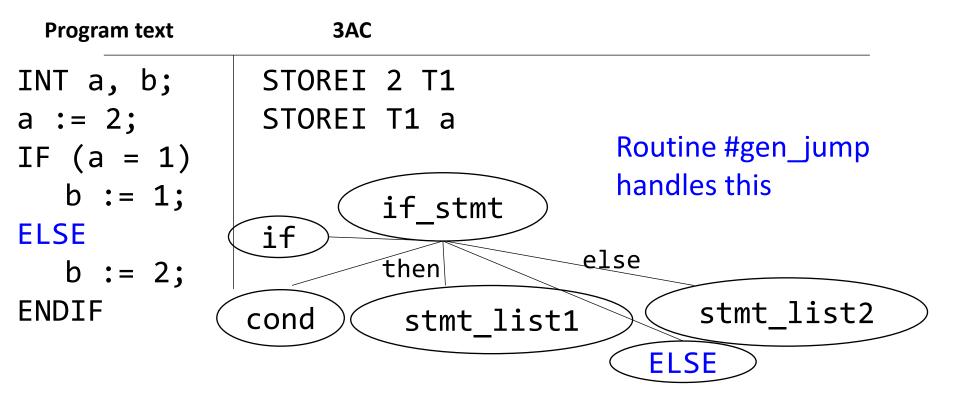


2. Cond has been matched. Generate statement: JUMP0 T2 label1

The #testif routine handles this

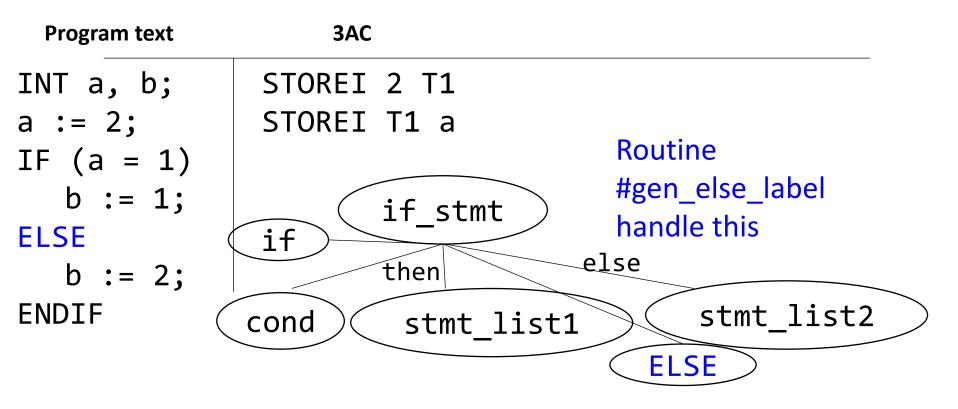


3. Generate code for stmt_list1
STOREI 1 T3
STOREI T3 b



4. Generate a label (label2 for out of if block --- out label) and generate unconditional jump to out label (label2).

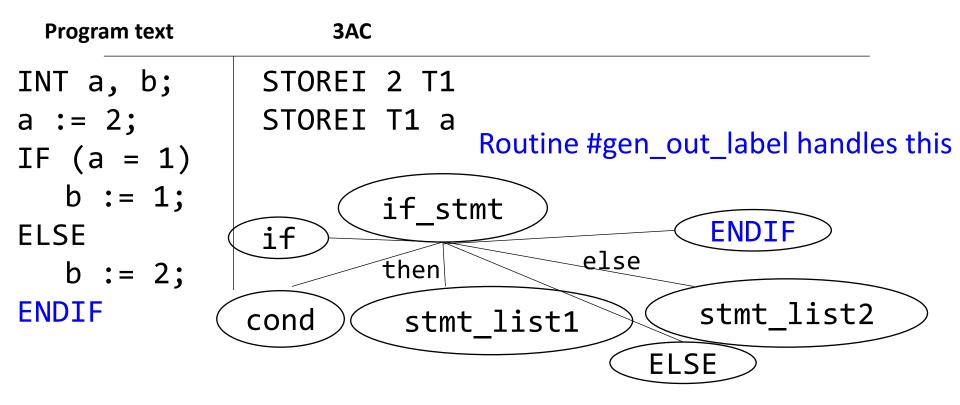
JUMP label2



5. Associate else part label (label1) with address of next instruction i.e. generate a statement: LABEL label1

Program text 3AC STOREI 2 T1 INT a, b; a := 2;STOREI T1 a IF (a = 1)b := 1;if stmt ELSE if else then b := 2;**ENDIF** stmt list2 cond stmt list1 **ELSE**

5. Generate code for stmt_list2 STOREI 2 T4 STOREI T4 b



5. Associate out label (label2) with address of next instruction i.e. generate a statement: LABEL label2

Observations

- We added semantic actions with tokens IF, ELSE, ENDIF
- Generated code is equivalent but not exact
 - e.g. "NE a T2 label1" is replaced with an equivalent "JUMP0 bool_expr label1"
- Done in one pass?

Will this approach work when generating machine code directly?

3AC **Program text** INT a, b; a := 2;|STOREI T1 a IF (a = 1) | STOREI 1 T2 //a = 1? b := 1; | NE a T2 label1 ELSIF (TRUE) | STOREI 1 T3 //b := 1 b := 2; | STOREI T3 b JUMP label2 //to out label ENDIF LABEL label1 //elsif label STOREI 1 T4 //TRUE can be handled by checking 1 = 1? STOREI 1 T5 NE T4 T5 label3 //jump to the next elsif label STOREI 2 T6 //b := 2 STOREI T6 b JUMP label2 //jump to out label LABEL label3 //out label LABEL label2 //out label

do-while

• do{S}while(B); //S is executed at least once and again and again... while B remains true

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```
LOOP:
     <stmt_list>
     <bool_expr>
     j<!op> OUT
     jmp LOOP
OUT:
```

repeat-until

 repeat(S)until(B); //S is executed at least once and again and again and again... while B remains false

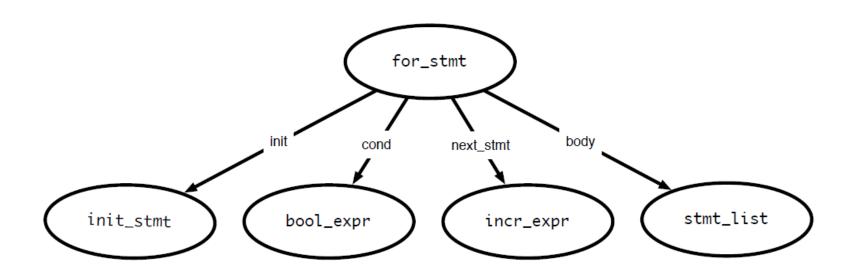
repeat-until

 repeat(S)until(B); //S is executed at least once and again and again and again... while B remains false

```
LOOP:
    <stmt_list>
    <bool_expr>
    j<!op> LOOP
OUT:
```

For loops

```
for (<init_stmt>;<bool_expr>;<incr_stmt>)
    <stmt_list>
end
```



Generating code: for loops

```
for (<init_stmt>;<bool_expr>;<incr_stmt>)
  <stmt_list>
end
                 <init_stmt>
              L00P:
                 <bool_expr>
                 j<!op> OUT
                 <stmt_list>
              INCR:
                 <incr_stmt>
                 jmp LOOP
              OUT:
```

- Execute init_stmt first
- Jump out of loop if bool_expr is false
- Execute incr_stmt after block, jump back to top of loop
- Question: Why do we have the INCR label?

Suggested Reading

- Alfred V. Aho, Monica S. Lam, Ravi Sethi and Jeffrey D.Ullman: Compilers: Principles, Techniques, and Tools, 2/E, AddisonWesley 2007
 - Chapter 2 (2.8), Chapter 6(6.2, 6.3, 6.4)
- Fisher and LeBlanc: Crafting a Compiler with C
 - Chapter 7 (7.1, 7.3), Chapter 11 (11.2)