CS323: Compilers Spring 2023

Week 6: Semantic Processing (contd..)

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Intermediate Representation

- Compilers need to synthesize code based on the 'interpretation' of the syntactic structure
- Code can be generated with the help of AST or can directly do it in semantic actions (recall: SDTs augment grammar rules with program fragments. Program fragments contain semantic actions.)
- Generated code can be directly executed on the machine or an intermediate form such as 3-address code can be produced.

- What is it? sequence of elementary program instructions
 - Linear in structure (no hierarchy) unlike AST
 - Format:

```
op A, B, C //means C = A op B.
//op: ADDI, MULI, SUBF, DIVF, GOTO, STOREF etc.
```

• E.g.

program text

3-address code

- Why is it needed? To perform significant optimizations such as:
 - common sub-expression elimination
 - statically analyze possible values that a variable can take etc.

How?

Break the long sequence of instructions into "basic blocks" and operate on/analyze a graph of basic blocks

- How is it generated? Choices available:
 - 1. Do a post-order walk of AST
 - Generate/Emit code as a string/data_object (seen later) when you visit a node
 - Pass the code to the parent node

```
Parent generates code for self after the code for children is generated. The generated code is appended to code passed by children and passed up the tree data_object generate_code() {
    //preprocessing code
    data_object lcode=left.generate_code();
    data_object rcode=right.generate_code();
    return generate_self(lcode, rcode);
}
```

2. Can generate directly in semantic routines or after building AST

Generating 3AC directly in semantic routines.

```
INT x;

x:=3*4+5+6+7;

MULI 3 4 T1

ADDI T1 5 T2

ADDI T2 6 T3

ADDI T3 7 T4

STOREI T4 x

x = 3*4+5+6+7 is broken into:

t1 = 3*4;

t2 = 5+t1;

t3 = 6+t2;

x = t4
```

 Walk the AST in post-order and infer at an internal node (labelled op) that it computes a constant expression



L-values and R-values

 Need to distinguish between meaning of identifiers appearing on RHS and LHS of an assignment statement

- L-values: addresses which can be loaded from or stored into
- R-values: data often loaded from address
 - Expressions produce R-values
- Assignment statements: L-value := R-value;

a refers to memory location named a. We are storing into that memory location (L-value) a refers to data stored in the memory location named a. We are loading from that memory location to produce R-value

Temporaries

• Earlier saw the use of temporaries e.g.

```
INT x; ADDF x y T1 
FLOAT y, z; STOREF T1 z 
z:=x+y;
```

- Think of them as unlimited pool of registers with memory to be allocated later
- Optionally declare them in 3AC. Name should be unique and should not appear in program text

```
INT x
FLOAT y z T1
ADDF x y T1
STOREF T1 z
```

Temporary can hold L-value or R-value

Temporaries and L-value

• Yes, a temporary can hold L-value. Consider:

```
a := &b; //& is address-of operator. R-value
of a is set to L-value of b.
//expression on the RHS produces data that is
an address of a memory location.
```

Recall: L-Value = address which can be loaded from or stored into, R-Value = data (often) loaded from addresses.

Take L-value of b, don't load from it, treat it as an R-value and store the resulting data in a temporary

Dereference operator

Consider:

```
*a := b; //* is dereference operator. R-value
of a is set to R-value of b.
//expression on the LHS produces data that is
an address of a memory location.
```

a appearing on LHS is loaded from to produce R-value. That R-value is treated as an address that can be stored into.

Take R-value of a, treat it as an L-value (address of a memory location) and then store RHS data

Summary: pointer operations & and * mess with meaning of L-value and R-values

Observations

- Identifiers appearing on LHS are (normally) treated as L-values. Appearing on RHS are treated as R-values.
 - So, when you are visiting an id node in an AST, you cannot generate code (load-from or store-into) until you have seen how that identifier is used. => until you visit the parent.
- Temporaries are needed to store result of current expression
- a data_object should store:
 - Code
 - L-value or R-Value or constant
 - Temporary storing the result of the expression

Simple cases

- Generating code for constants/literals
 - Store constant in temporary
 - Optional: pass up flag specifying this is a constant
- Generating code for identifiers
 - Generated code depends on whether identifier is used as Lvalue or R-value
 - Is this an address? Or data?
 - One solution: just pass identifier up to next level
 - Mark it as an L-value (it's not yet data!)
 - Generate code once we see how variable is used

Generating code for expressions

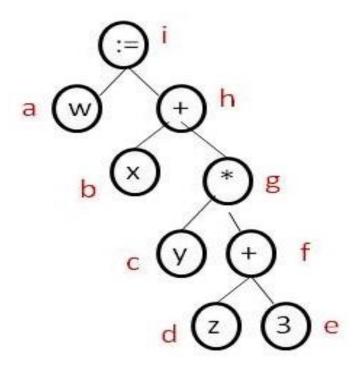
- Create a new temporary for result of expression
- Examine data-objects from subtrees
- If temporaries are L-values, load data from them into new temporaries
 - Generate code to perform operation
 - In project, no need to explicitly load (variables can be operands)
- If temporaries are constant, can perform operation immediately
 - No need to perform code generation!
- Store result in new temporary
 - Is this an L-value or an R-value?
- Return code for entire expression

AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node a:

Temp: w

Type: I-value

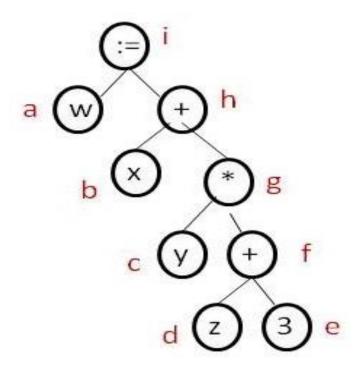


AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node b:

Temp: x

Type: I-value

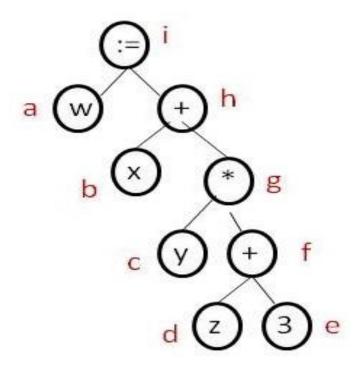


AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node c:

Temp: y

Type: I-value

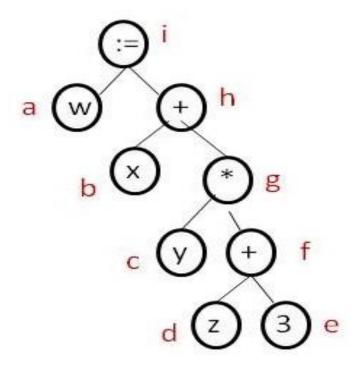


AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node d:

Temp: z

Type: I-value

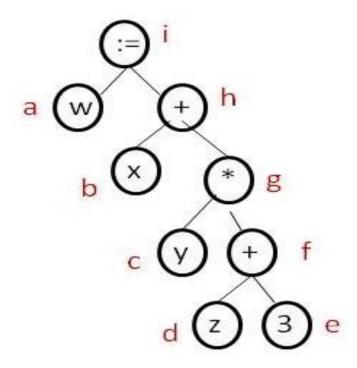


AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node e:

Temp: 3

Type: constant



AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node f:

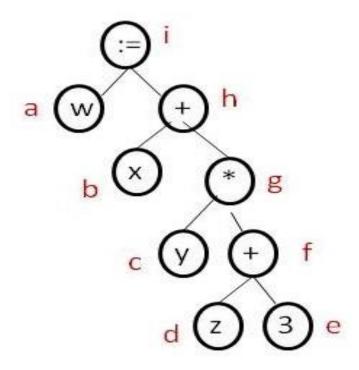
Temp: T1

Type: R-value

Code:

LD z T2

ADD T2 3 T1



AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

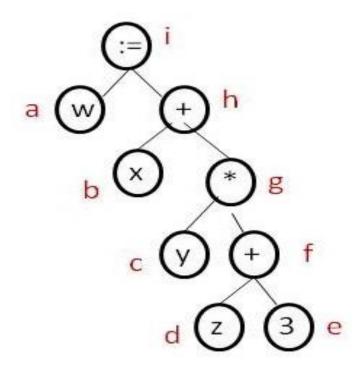
Visit Node g:

Temp: T3

Type: R-value

Code:

LD y T4 LD z T2 ADD T2 3 T1 MUL T4 T1 T3



AST for
$$\longrightarrow$$
 $w:=x+y*(z+3);$

Visit Node h:

Temp: T5

Type: R-value

Code:

LD x T6

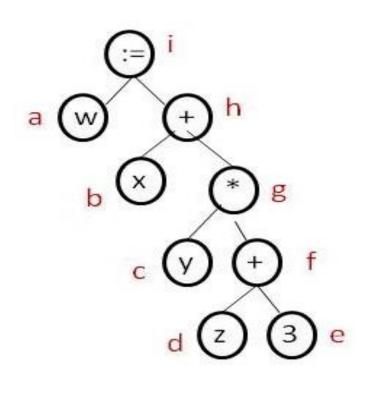
LD y T4

LD z T2

ADD T2 3 T1

MUL T4 T1 T3

ADD T6 T3 T5



```
AST for
       W:=X+Y*(Z+3);
Visit Node i:
   Temp: NA
   Type: NA
   Code:
       LD x T6
       LD y T4
       LD z T2
       ADD T2 3 T1
       MUL T4 T1 T3
       ADD T6 T3 T5
```

ST T5 w