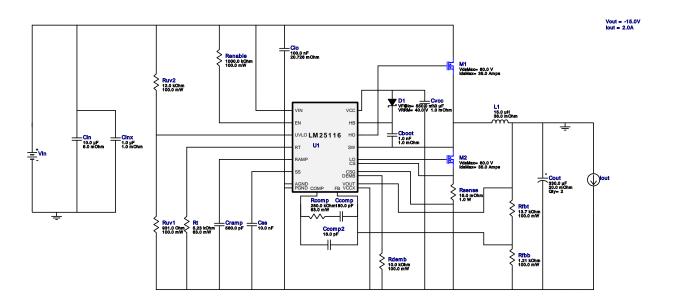
$\begin{aligned} & \text{VinMin} = 17.0 \text{V} \\ & \text{VinMax} = 20.0 \text{V} \\ & \text{Vout} = -15.0 \text{V} \\ & \text{Iout} = 2.0 \text{A} \end{aligned}$

Device = LM25116MH/NOPB Topology = Inverting_Buck_Boost Created = 2023-03-23 10:25:22.176 BOM Cost = \$5.44 BOM Count = 25 Total Pd = 1.75W

WEBENCH® Design Report

Design: 32 LM25116MH/NOPB LM25116MH/NOPB 17V-20V to -15.00V @ 2A

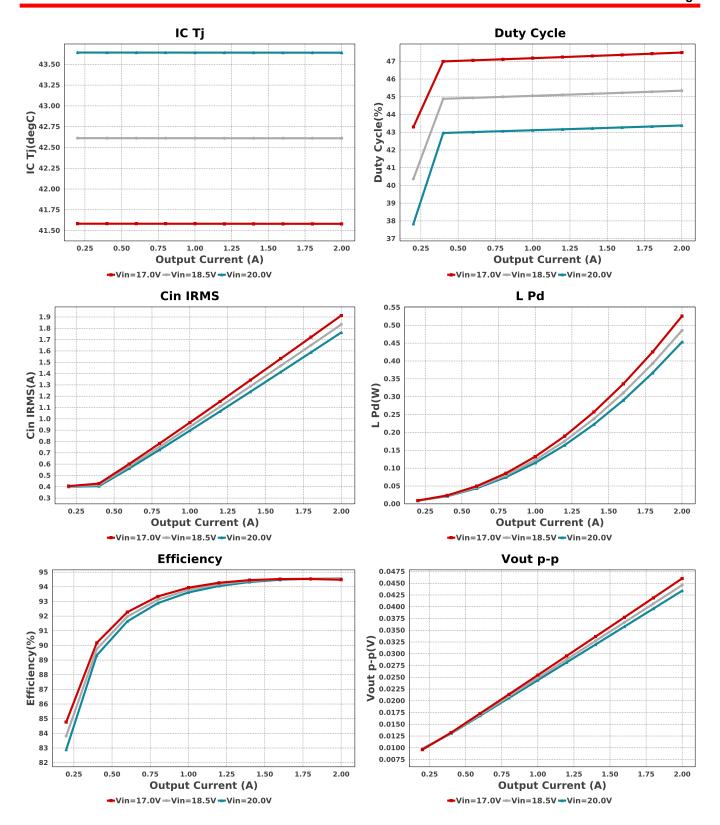


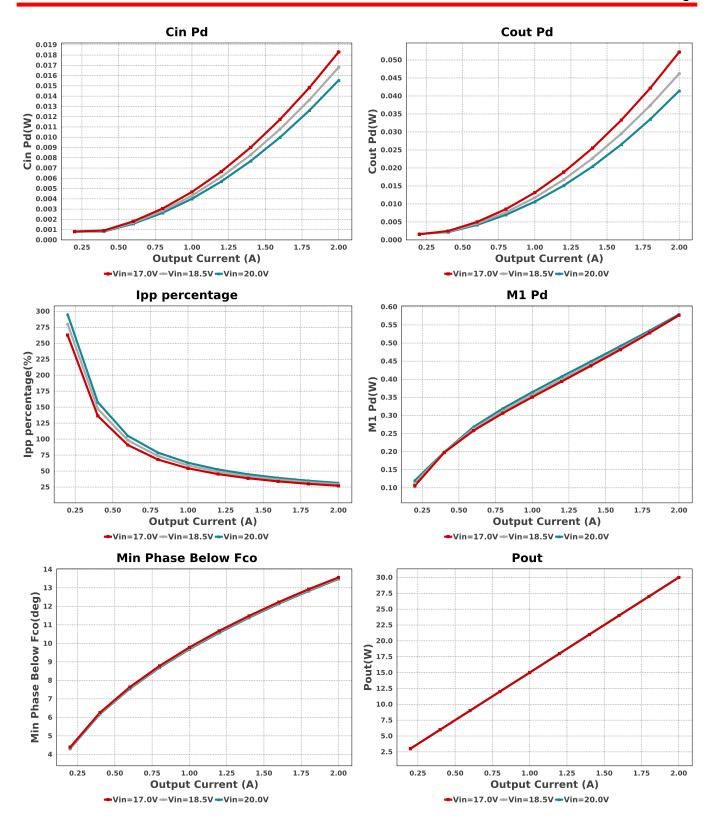
Electrical BOM

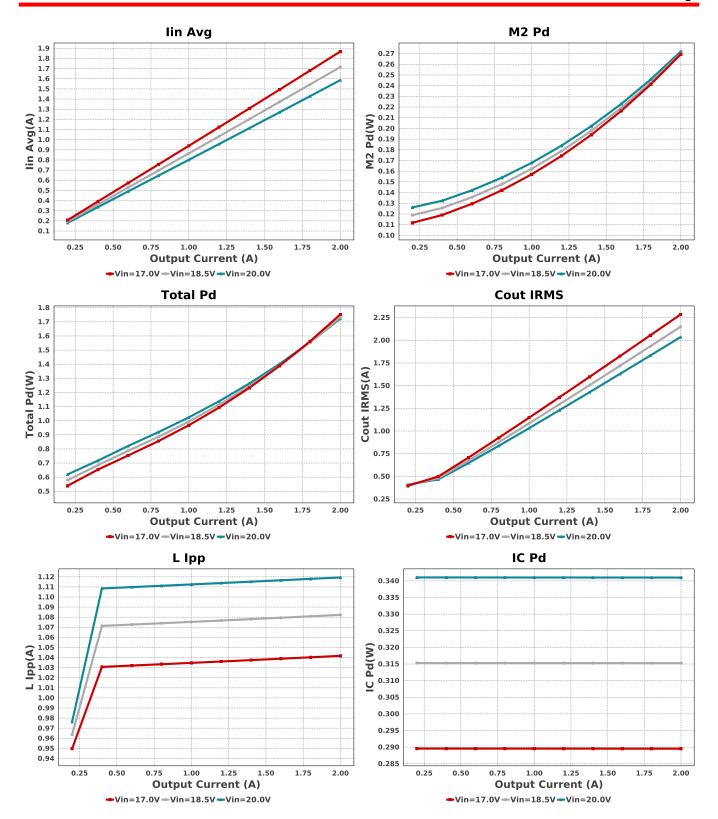
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	Kemet	C0603C102J5RACTU Series= X7R	Cap= 1.0 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Ccomp	Yageo	CC0603JRNPO9BN181 Series= C0G/NP0	Cap= 180.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Ccomp2	Samsung Electro- Mechanics	CL10C160JB8NNNC Series= C0G/NP0	Cap= 16.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Cin	Samsung Electro- Mechanics	CL32B106KBJNNWE Series= X7R	Cap= 10.0 uF ESR= 5.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.17	1210_270 15 mm ²
Cinx	Taiyo Yuden	TMK212B7105KG-T Series= X7R	Cap= 1.0 uF ESR= 1.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.03	0805 7 mm ²
Cio	TDK	C2012X7R2A104K125AA Series= X7R	Cap= 100.0 nF ESR= 20.726 mOhm VDC= 100.0 V IRMS= 1.456 A	1	\$0.03	0805 7 mm ²
Cout	Panasonic	EEHZC1E331P Series= ZC	Cap= 330.0 uF ESR= 20.0 mOhm VDC= 25.0 V IRMS= 2.0 A	2	\$1.01	SM_RADIAL_10BMM 160 mm²
Cramp	Samsung Electro- Mechanics	CL10C561JB8NFNC Series= C0G/NP0	Cap= 560.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²

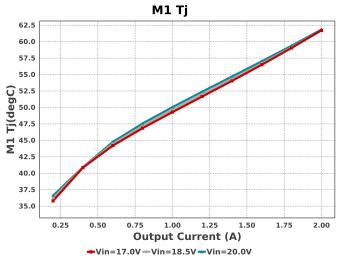
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Css	TDK	C1608C0G1H103J080AA Series= C0G/NP0	Cap= 10.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.06	0603 5 mm ²
Cvcc	Taiyo Yuden	TMK212B7105KG-T Series= X7R	Cap= 1.0 uF ESR= 1.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.03	0805 7 mm ²
D1	Fairchild Semiconductor	SS24FL	VF@Io= 550.0 mV VRRM= 40.0 V	1	\$0.09	SOD-123F 12 mm ²
L1	NIC Components	NPI52W150MTRF	L= 15.0 μH 36.0 mOhm	1	\$0.26	
		00040540004			00.04	IND_NPI52W 358 mm²
M1	Texas Instruments	CSD18543Q3A	VdsMax= 60.0 V IdsMax= 35.0 Amps	1	\$0.24	DNH0008A 18 mm ²
M2	Texas Instruments	CSD18543Q3A	VdsMax= 60.0 V IdsMax= 35.0 Amps	1	\$0.24	DNH0008A 18 mm ²
Rcomp	Vishay-Dale	CRCW0402280KFKED Series= CRCWe3	Res= 280.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rdemb	Vishay-Dale	CRCW060310K0FKEA Series= CRCWe3	Res= 10.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Renable	Vishay-Dale	CRCW06031M00FKEA Series= CRCWe3	Res= 1000.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rfbb	Yageo	RC0603FR-071K21L Series= ?	Res= 1.21 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rfbt	Vishay-Dale	CRCW060313K7FKEA Series= CRCWe3	Res= 13.7 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rsense	Stackpole Electronics Inc	CSRN2010FK15L0 Series= ?	Res= 15.0 mOhm Power= 1.0 W Tolerance= 1.0%	1	\$0.17	2010 32 mm ²
Rt	Vishay-Dale	CRCW04025K23FKED Series= CRCWe3	Res= 5.23 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Ruv1	Vishay-Dale	CRCW0603931RFKEA Series= CRCWe3	Res= 931.0 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	■ 0603 5 mm ²
Ruv2	Yageo	RC0603FR-0712KL Series= ?	Res= 12.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	■ 0603 5 mm ²
U1	Texas Instruments	LM25116MH/NOPB	Switcher	1	\$1.98	0

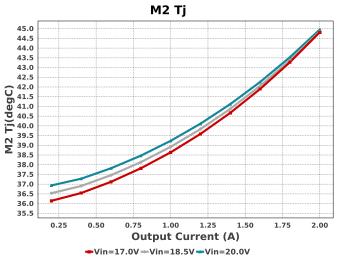
MXA20A 71 mm²

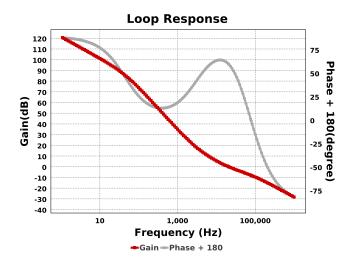












Operating Values

P -	. a.m.ig			
#	Name	Value	Category	Description
1.	Cin IRMS	1.913 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	18.3 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	2.284 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	52.187 mW	Capacitor	Output capacitor power dissipation
5.	IC Pd	289.53 mW	IC	IC power dissipation
6.	IC Tj	41.581 degC	IC	IC junction temperature
7.	ICThetaJA	40.0 degC/W	IC	IC junction-to-ambient thermal resistance
8.	lin Avg	1.868 A	IC	Average input current
9.	lpp percentage	27.023 %	Inductor	Inductor ripple current percentage (with respect to average inductor
	-			current)
10.	L lpp	1.042 A	Inductor	Peak-to-peak inductor ripple current
11.	L Pd	525.49 mW	Inductor	Inductor power dissipation
12.	M1 Pd	576.91 mW	Mosfet	M1 MOSFET total power dissipation
13.	M1 Tj	61.73 degC	Mosfet	M1 MOSFET junction temperature
14.	M2 Pd	269.36 mW	Mosfet	M2 MOSFET total power dissipation
15.	M2 Tj	44.815 degC	Mosfet	M2 MOSFET junction temperature
16.	Cin Pd	18.3 mW	Power	Input capacitor power dissipation
17.	Cout Pd	52.187 mW	Power	Output capacitor power dissipation
18.	IC Pd	289.53 mW	Power	IC power dissipation
19.	L Pd	525.49 mW	Power	Inductor power dissipation
20.	M1 Pd	576.91 mW	Power	M1 MOSFET total power dissipation
21.	M2 Pd	269.36 mW	Power	M2 MOSFET total power dissipation
22.	Total Pd	1.752 W	Power	Total Power Dissipation
23.	BOM Count	25	System	Total Design BOM count
			Information	•
24.	Cross Freq	19.477 kHz	System	Bode plot crossover frequency
	•		Information	
25.	Duty Cycle	47.493 %	System	Duty cycle
			Information	•
26.	Efficiency	94.482 %	System	Steady state efficiency
	•		Information	•

.,			0.1	
#	Name	Value	Category	Description
27.	FootPrint	922.0 mm ²	System Information	Total Foot Print Area of BOM components
28.	Frequency	516.71 kHz	System Information	Switching frequency
29.	Gain Marg	-6.686 dB	System Information	Bode Plot Gain Margin
30.	lout	2.0 A	System Information	lout operating point
31.	Low Freq Gain	119.663 dB	System Information	Gain at 1Hz
32.	Min Phase Below Fco	13.563 deg	System Information	Minimum Phase Before the Cross-Over Frequency
33.	Mode	ССМ	System Information	Conduction Mode
34.	Phase Marg	56.947 deg	System Information	Bode Plot Phase Margin
35.	Pout	30.0 W	System Information	Total output power
36.	Total BOM	\$5.44	System Information	Total BOM Cost
37.	Vin	17.0 V	System Information	Vin operating point
38.	Vout	-15.0 V	System Information	Operational Output Voltage
39.	Vout Actual	14.972 V	System Information	Vout Actual calculated based on selected voltage divider resistors
40.	Vout Tolerance	3.382 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
41.	Vout p-p	46.022 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

9			
Name	Value	Description	
lout	2.0	Maximum Output Current	
VinMax	20.0	Maximum input voltage	
VinMin	17.0	Minimum input voltage	
Vout	-15.0	Output Voltage	
base_pn	LM25116	Base Product Number	
source	DC	Input Source Type	
Та	30.0	Ambient temperature	

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 17.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

- 1. Master key: B9BB535380F6DCECC9D62E6CF388EC6F[v1]
- 2. LM25116 Product Folder: http://www.ti.com/product/LM25116: contains the data sheet and other resources.

Important Notice and Disclaimer

TI provides technical and reliability data (including datasheets), design resources (including reference designs), application or other design advice, web tools, safety information, and other resources AS IS and with all faults, and disclaims all warranties. These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

Providing these resources does not expand or otherwise alter TI's applicable Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with TI products.