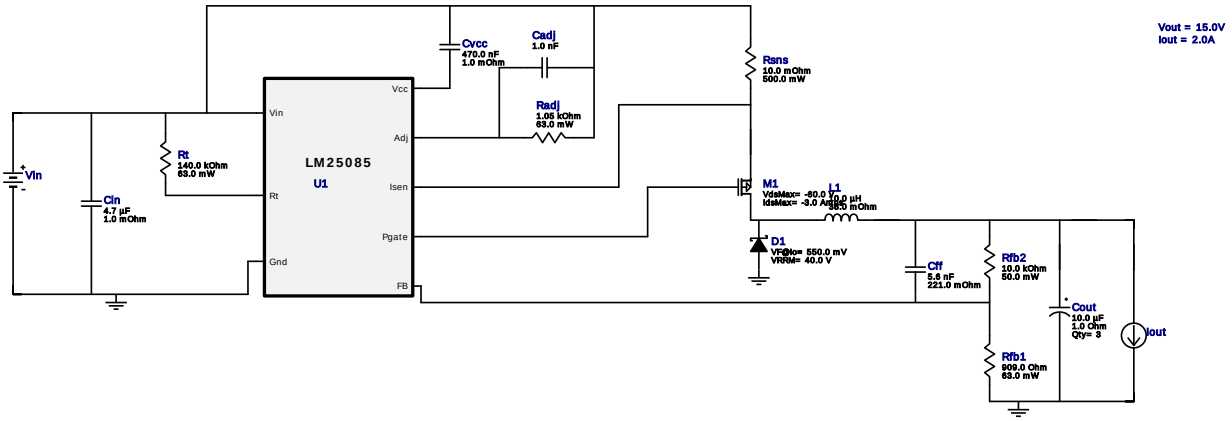
VinMin = 17.0V
VinMax = 20.0V
Vout = 15.0V
Iout = 2.0A

Device = LM25085MY/NOPB
Topology = Buck
Created = 2023-03-23 05:21:42.111
BOM Cost = $11.85
BOM Count = 16
Total Pd = 1.33W

WEBENCH® Design Report

Design : 26 LM25085MY/NOPB
LM25085MY/NOPB 17V-20V to 15.00V @ 2A



Electrical BOM

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cadj	Samsung Electro-Mechanics	CL21C102JBCNNNC Series= C0G/NP0	Cap= 1.0 nF VDC= 50.0 V IRMS= 0.0 A	1	$0.01	0805 7 mm²
Cff	Kemet	C1206C562K5RACTU Series= X7R	Cap= 5.6 nF ESR= 221.0 mOhm VDC= 50.0 V IRMS= 326.0 mA	1	$0.07	1206 11 mm²
Cin	TDK	C2012X5R1H475K125AB Series= X5R	Cap= 4.7 uF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 4.3 A	1	$0.12	0805 7 mm²
Cout	Vishay-Sprague	293D106X9063E2TE3 Series= 293D	Cap= 10.0 uF ESR= 1.0 Ohm VDC= 63.0 V IRMS= 580.0 mA	3	$3.39	3216-18 11 mm²
Cvcc	Taiyo Yuden	TMK212BJ474KD-T Series= X5R	Cap= 470.0 nF ESR= 1.0 mOhm VDC= 20.0 V IRMS= 0.0 A	1	$0.02	0805 7 mm²
D1	Fairchild Semiconductor	SS14FL	VF@Io= 550.0 mV VRRM= 40.0 V	1	$0.03	SOD-123F 12 mm²
L1	TDK	VLP8040T-100M	L= 10.0 µH 38.0 mOhm	1	$0.22	VLP8040 113 mm²
M1	Fairchild Semiconductor	FDC5614P	VdsMax= -60.0 V IdsMax= -3.0 Amps	1	$0.25	SOT-23-6 15 mm²
Radj	Vishay-Dale	CRCW04021K05FKED Series= CRCW..e3	Res= 1.05 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²
Rfb1	Vishay-Dale	CRCW0402909RFKED Series= CRCW..e3	Res= 909.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²

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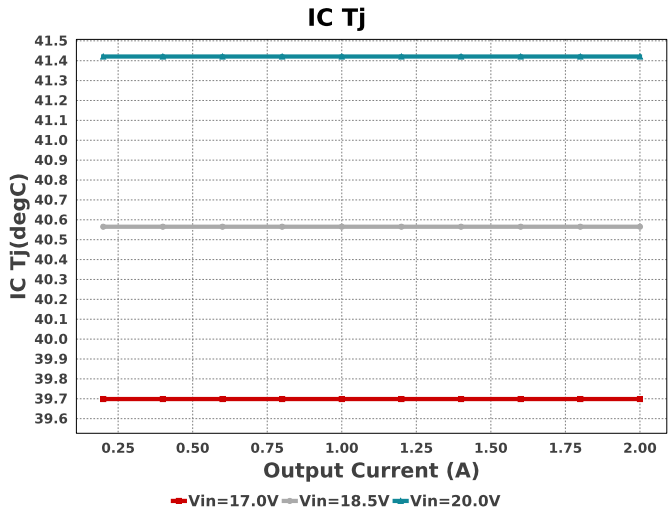
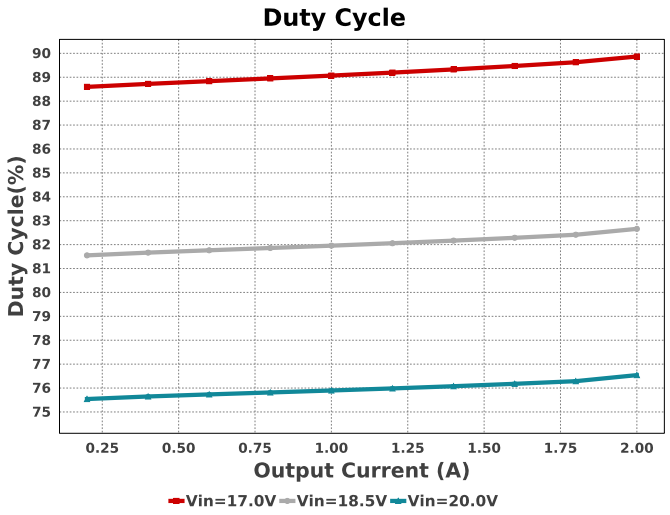
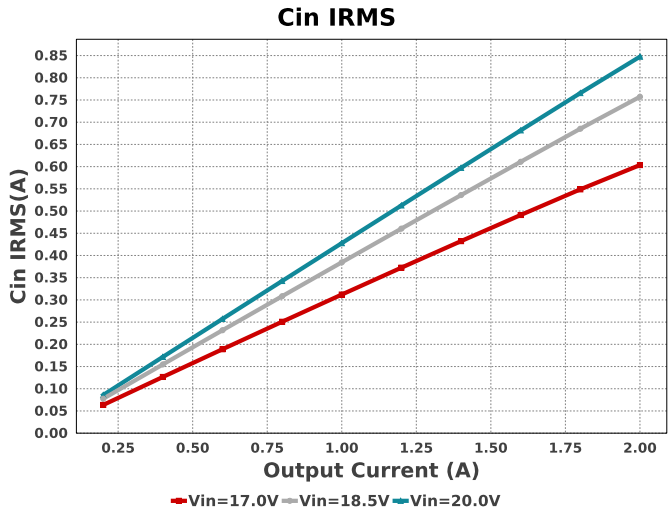
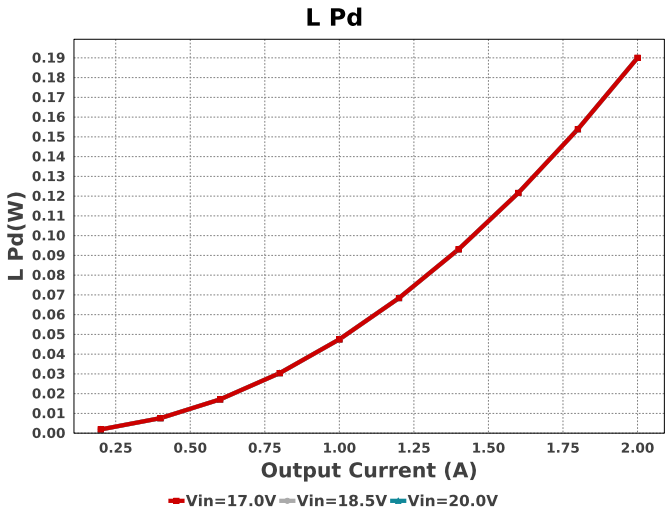
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Rfb2	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rsns	Stackpole Electronics Inc	CSR1206FK10L0 Series= ?	Res= 10.0 mOhm Power= 500.0 mW Tolerance= 1.0%	1	$0.11	1206 11 mm²
Rt	Vishay-Dale	CRCW0402140KFKED Series= CRCW..e3	Res= 140.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²
U1	Texas Instruments	LM25085MY/NOPB	Switcher	1	$0.81	MUY08A 24 mm²









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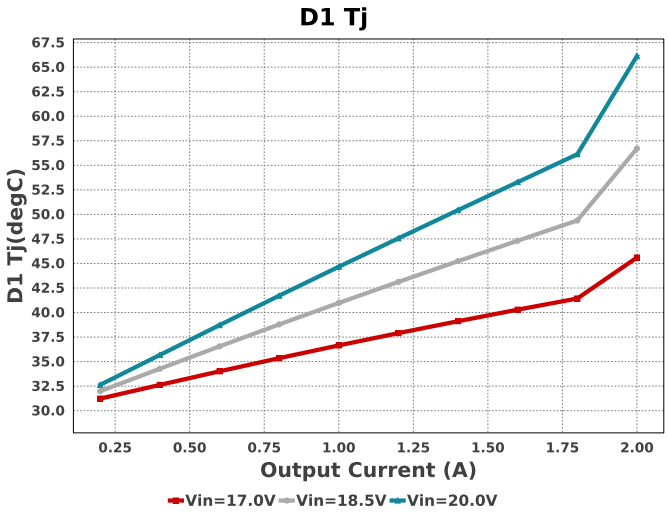

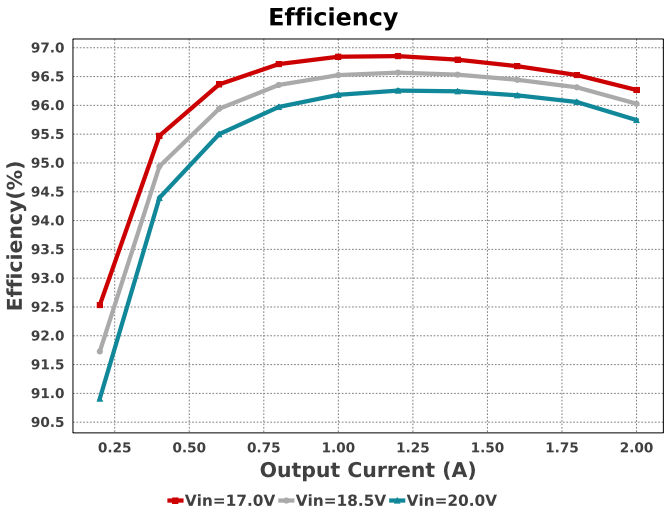

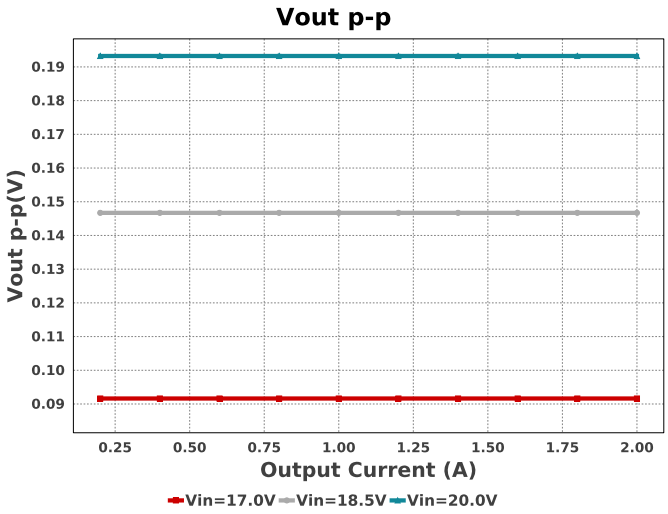

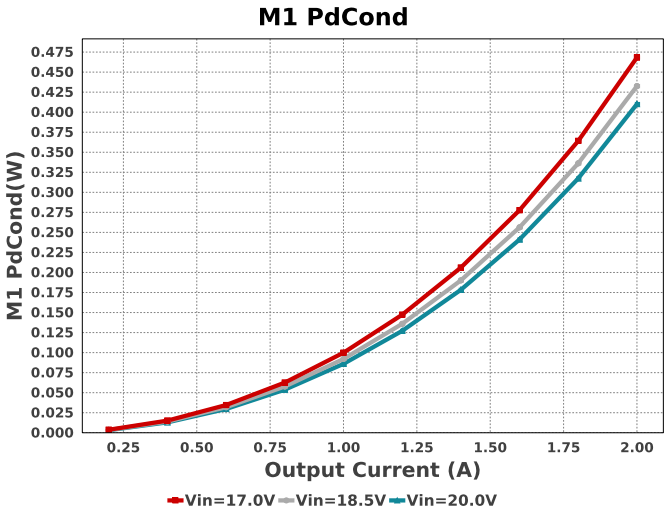

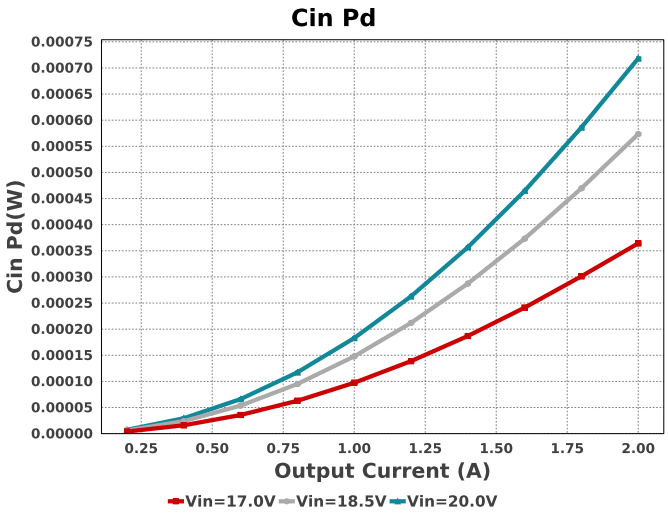

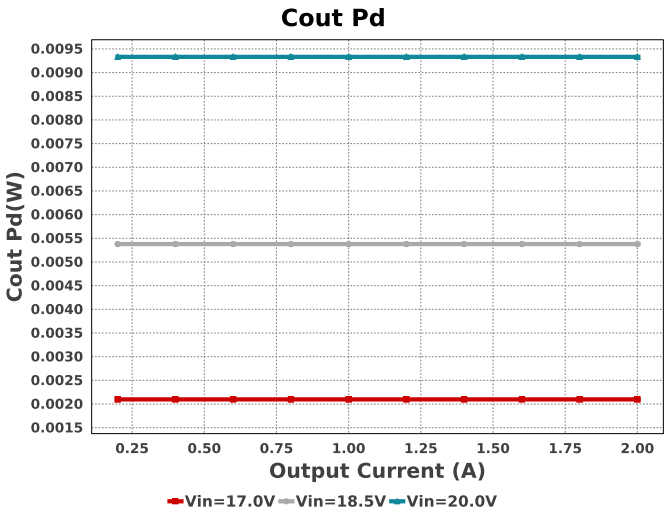

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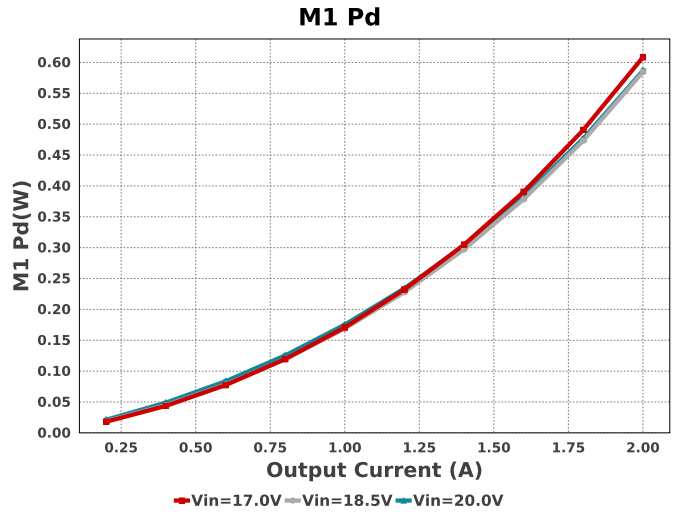

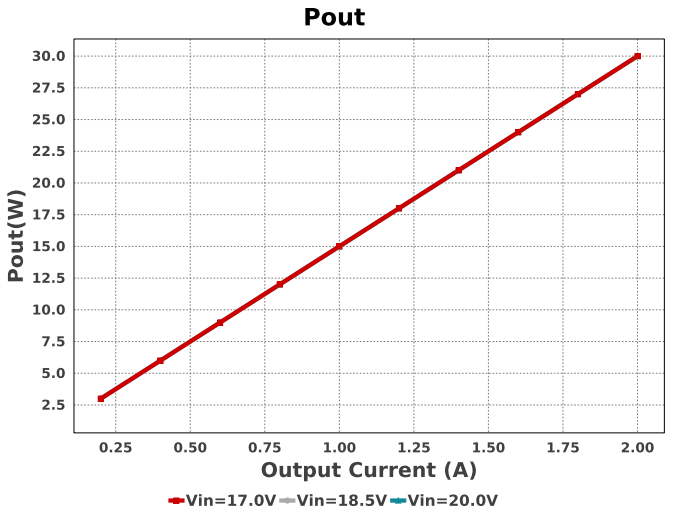

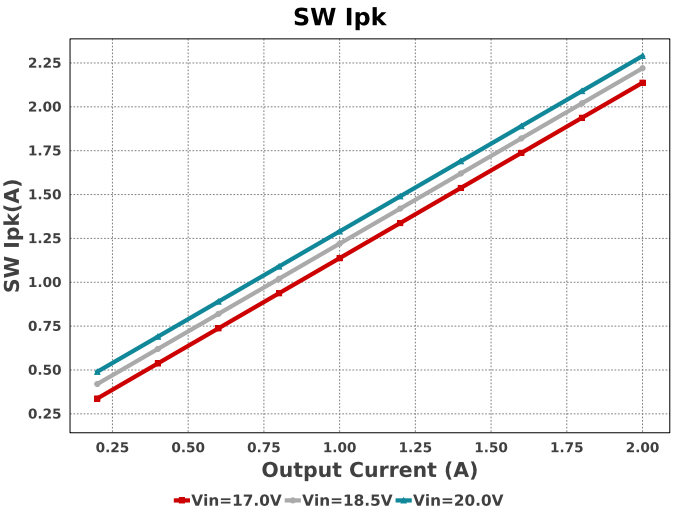

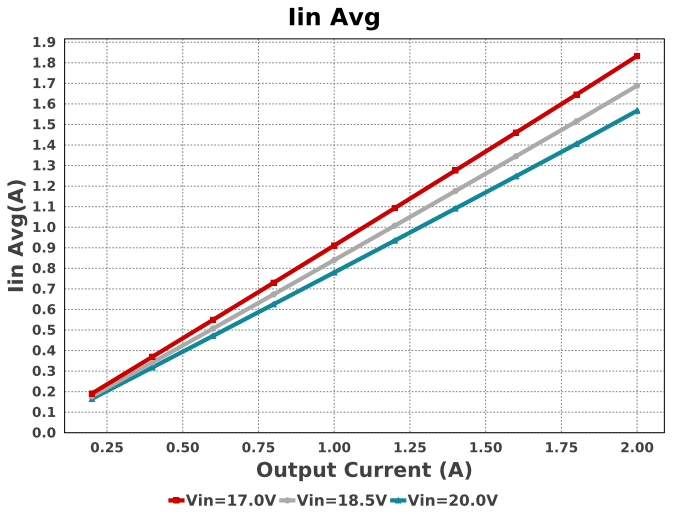

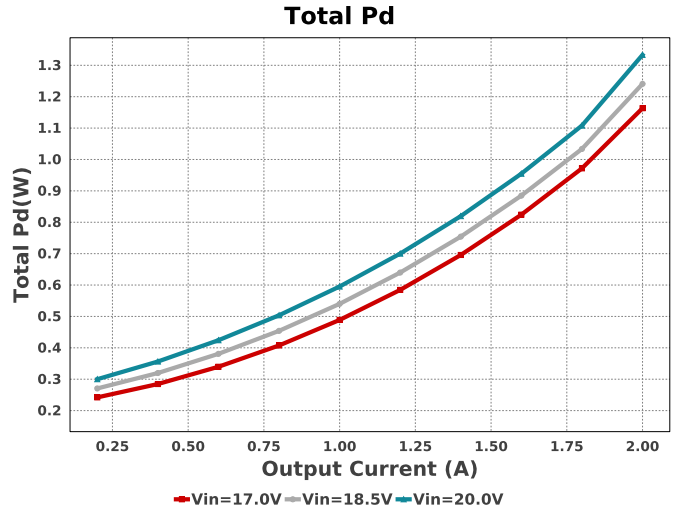

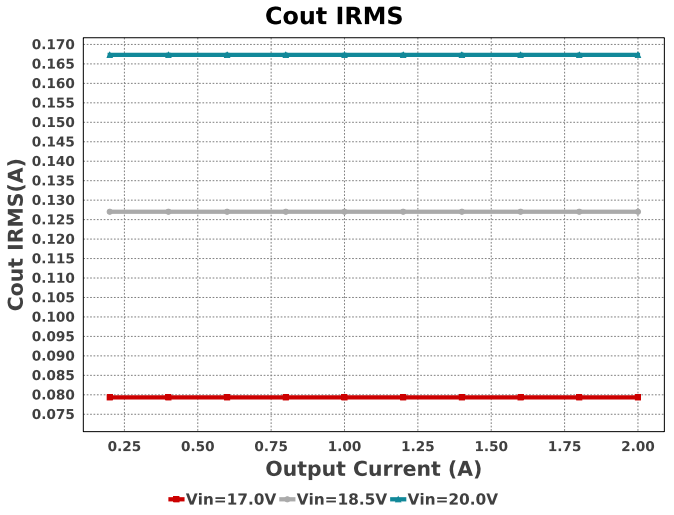

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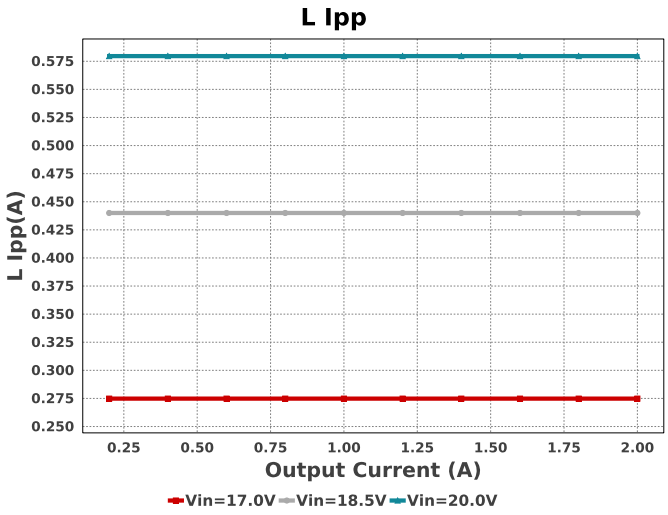

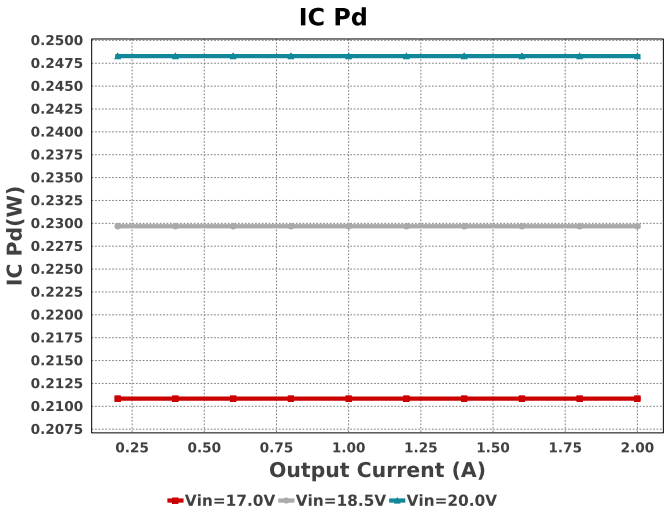

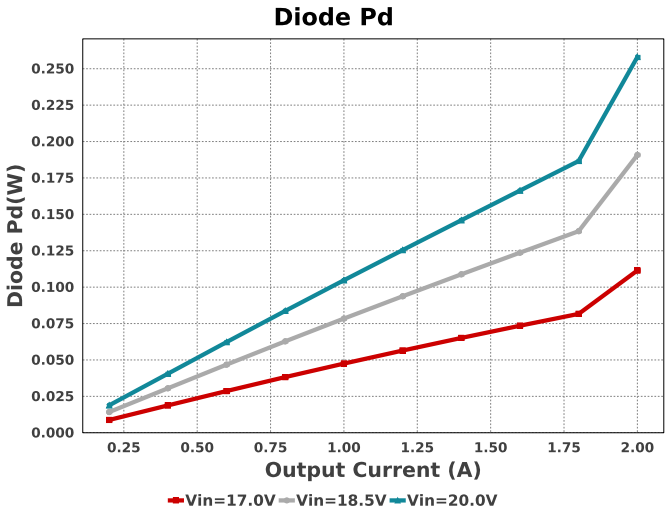

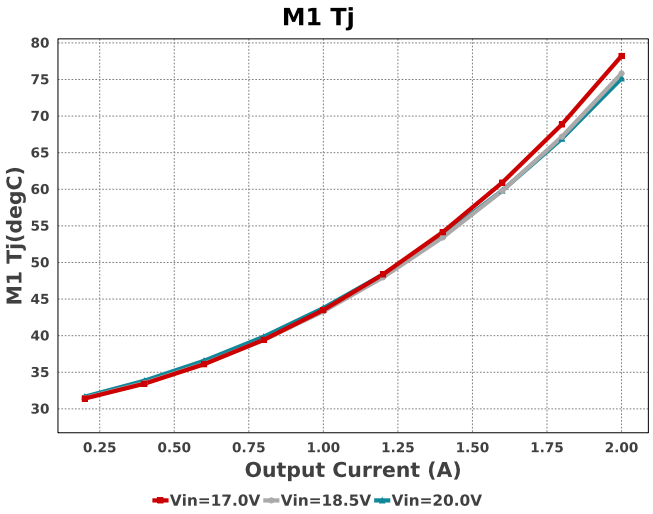

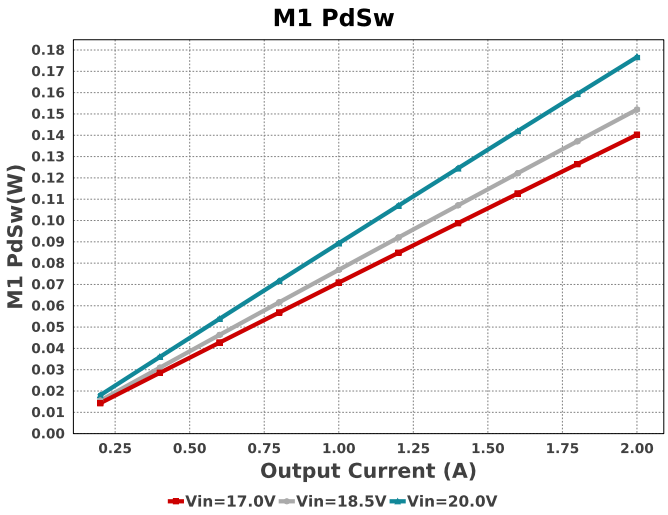

Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	847.484 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	718.27 µW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	167.319 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	9.332 mW	Capacitor	Output capacitor power dissipation
5.	D1 Tj	66.127 degC	Diode	D1 junction temperature
6.	Diode Pd	258.07 mW	Diode	Diode power dissipation
7.	IC Pd	248.28 mW	IC	IC power dissipation
8.	IC Tj	41.421 degC	IC	IC junction temperature
9.	IC Tolerance	25.0 mV	IC	IC Feedback Tolerance
10.	ICThetaJA	46.0 degC/W	IC	IC junction-to-ambient thermal resistance
11.	Iin Avg	1.567 A	IC	Average input current

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#	Name	Value	Category	Description
12.	L Ipp	579.609 mA	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	190.0 mW	Inductor	Inductor power dissipation
14.	M1 Pd	565.98 mW	Mosfet	M1 MOSFET total power dissipation
15.	M1 PdCond	409.29 mW	Mosfet	M1 MOSFET conduction losses
16.	M1 PdSw	156.69 mW	Mosfet	M1 MOSFET switching losses
17.	M1 Tj	75.144 degC	Mosfet	M1 MOSFET junction temperature
18.	Cin Pd	718.23 µW	Power	Input capacitor power dissipation
19.	Cout Pd	9.332 mW	Power	Output capacitor power dissipation
20.	Diode Pd	258.05 mW	Power	Diode power dissipation
21.	IC Pd	248.28 mW	Power	IC power dissipation
22.	L Pd	190.0 mW	Power	Inductor power dissipation
23.	M1 Pd	586.84 mW	Power	M1 MOSFET total power dissipation
24.	M1 PdCond	410.14 mW	Power	M1 MOSFET conduction losses
25.	M1 PdSw	176.7 mW	Power	M1 MOSFET switching losses
26.	Total Pd	1.333 W	Power	Total Power Dissipation
27.	BOM Count	16	System Information	Total Design BOM count
28.	Duty Cycle	76.541 %	System Information	Duty cycle
29.	Efficiency	95.745 %	System Information	Steady state efficiency
30.	FootPrint	249.0 mm^2	System Information	Total Foot Print Area of BOM components
31.	Frequency	620.236 kHz	System Information	Switching frequency
32.	Iout	2.0 A	System Information	Iout operating point
33.	Mode	CCM	System Information	Conduction Mode
34.	Pout	30.0 W	System Information	Total output power
35.	SW Ipk	2.29 A	System Information	Peak switch current
36.	Total BOM	$11.854	System Information	Total BOM Cost
37.	Vin	20.0 V	System Information	Vin operating point
38.	Vout	15.0 V	System Information	Operational Output Voltage
39.	Vout Actual	15.001 V	System Information	Vout Actual calculated based on selected voltage divider resistors
40.	Vout Tolerance	3.889 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
41.	Vout p-p	193.242 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	2.0	Maximum Output Current
VinMax	20.0	Maximum input voltage
VinMin	17.0	Minimum input voltage
Vout	15.0	Output Voltage
base_pn	LM25085	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

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WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

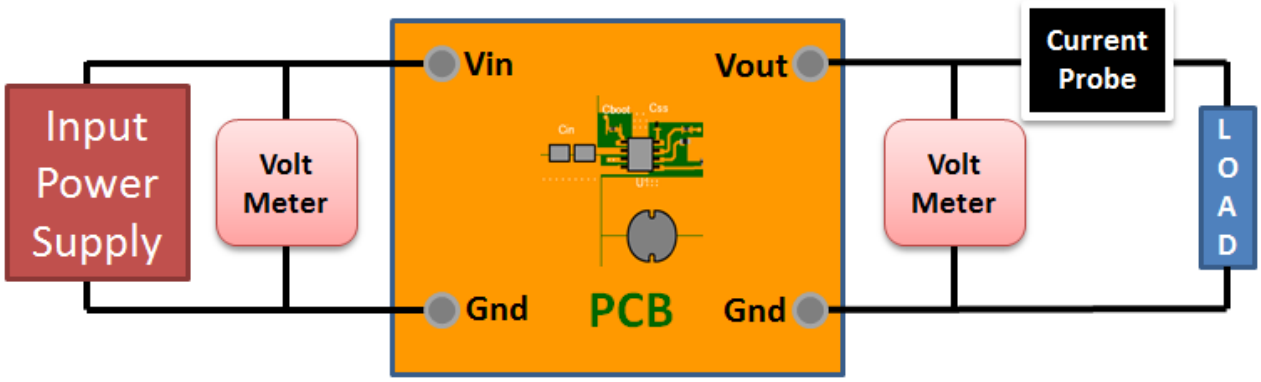
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 17.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

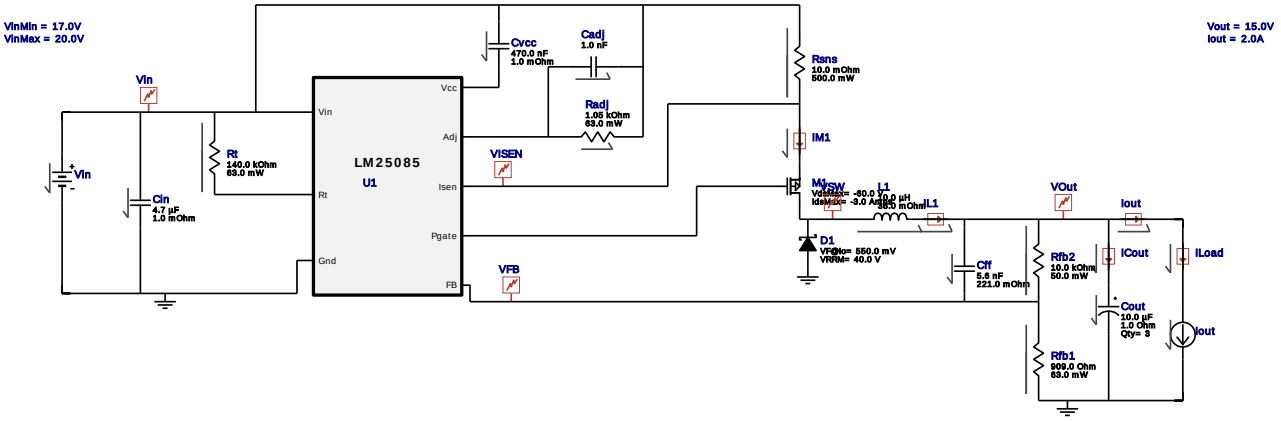


WEBENCH® Electrical Simulation Report

Design Id = 26

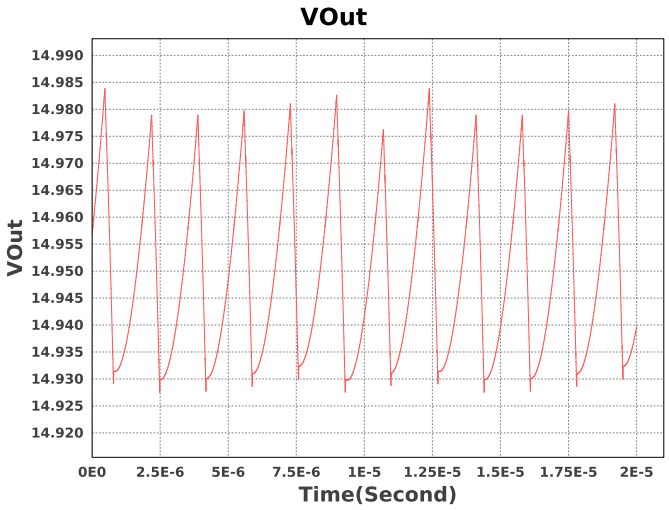
sim_id = 2

Simulation Type = Steady State



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	Iout	I	Load Current	2.0 A



Design Assistance

1. For a Constant On Time device to be stable, we need to provide a ripple at the feedback comparator. There are various methods to implement the ripple. Depending on the circuit complexity vs. the allowable ripple, we have three options to choose from. The simplest option, 'Low Complexity', would require only a high ESR cap at the output. This means that the BOM count will be small, but the output voltage ripple will be quite large. The 'optimal solution' would require a feed-forward cap in parallel with the upper feedback resistor to AC couple the ripple to the feedback node. This increases the BOM count slightly, but now we have more control over the output voltage ripple. If the output voltage requirement is very tight, then the best option is to go for the 'Low Output Ripple' solution. In this option we can go with very low ESR output caps and have very good control over the output voltage ripple

2. Master key : B9BB535380F6DCECC9D62E6CF388EC6F[v1]

3. **LM25085** Product Folder : http://www.ti.com/product/LM25085 : contains the data sheet and other resources.

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