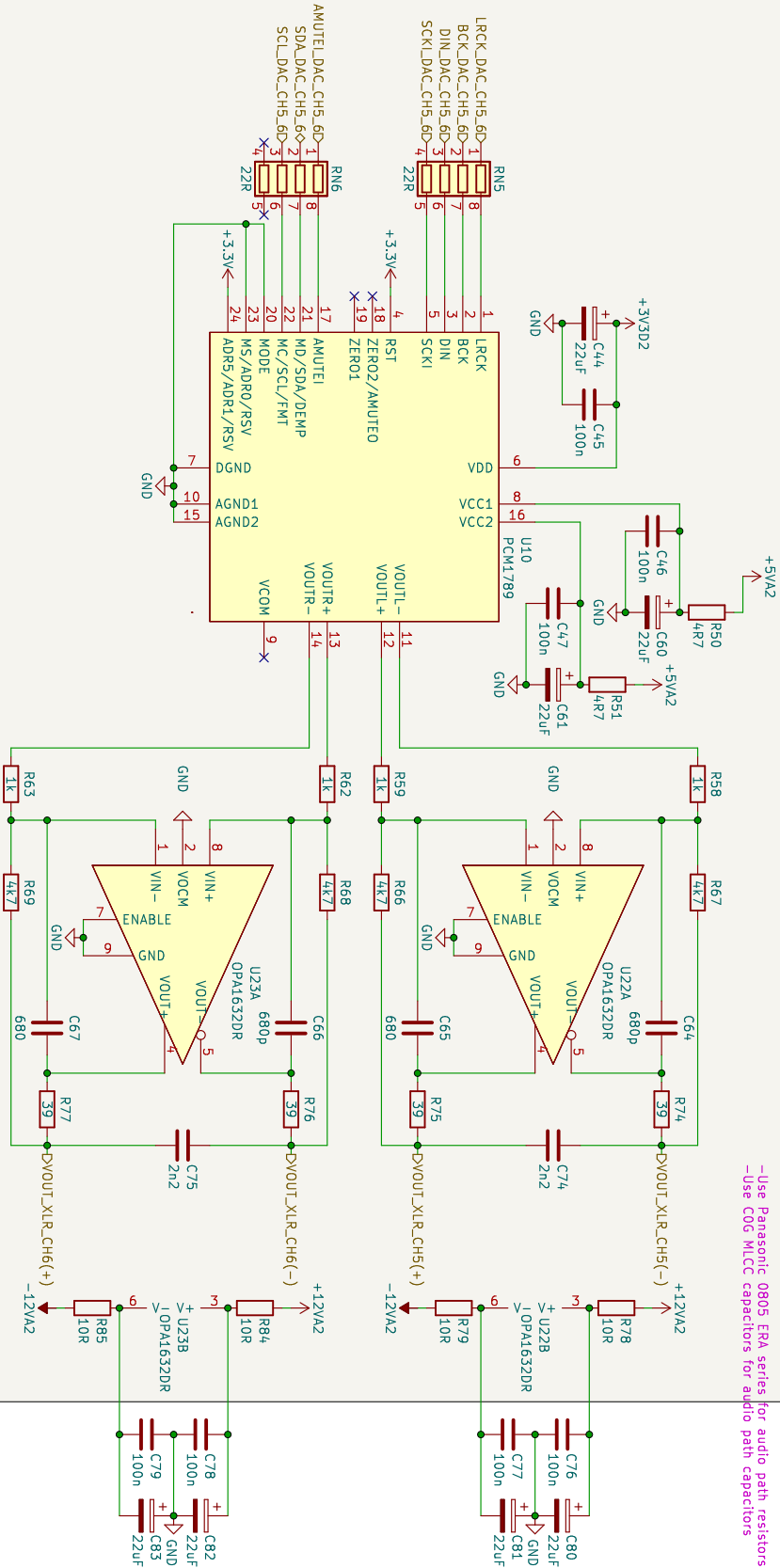








-Use Panasonic 0805 ERA series for audio path resistors  
-Use C0G MLCC capacitors for audio path capacitors



Sheet:  
File: DAC\_CH5\_6.kicad\_sch

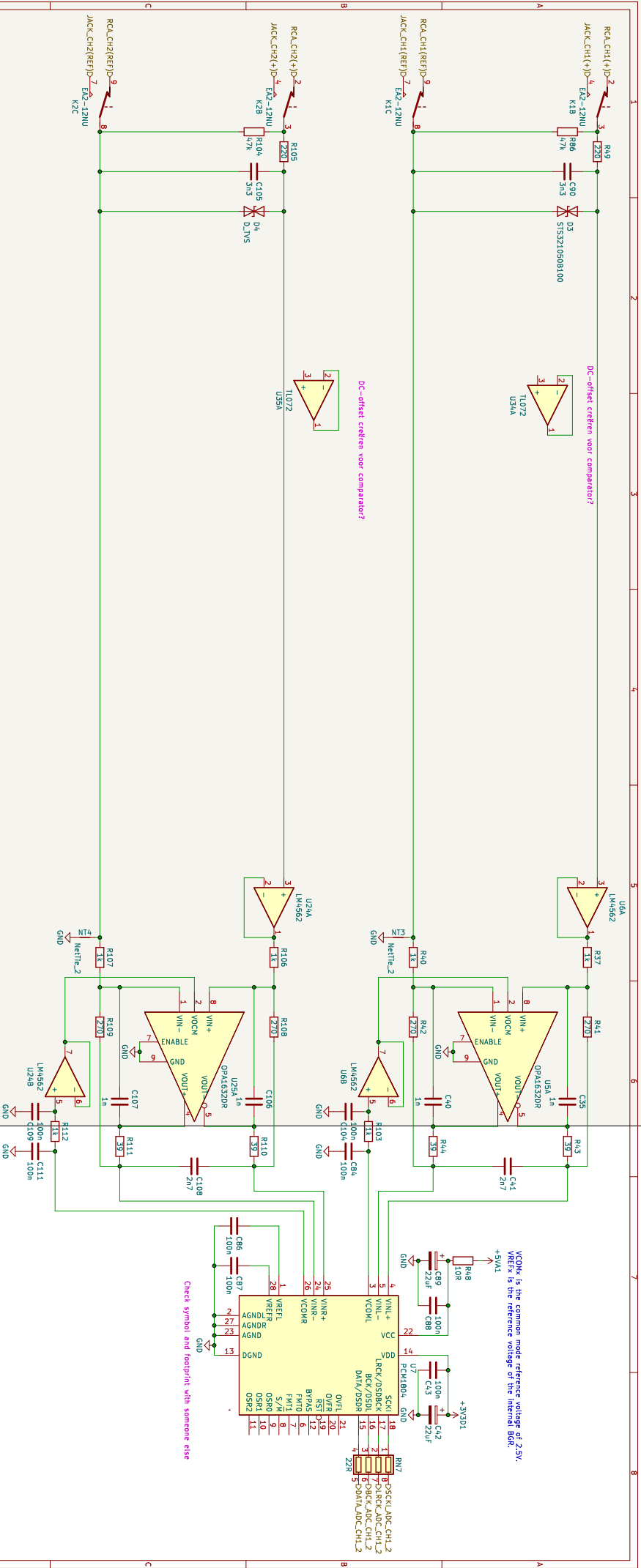
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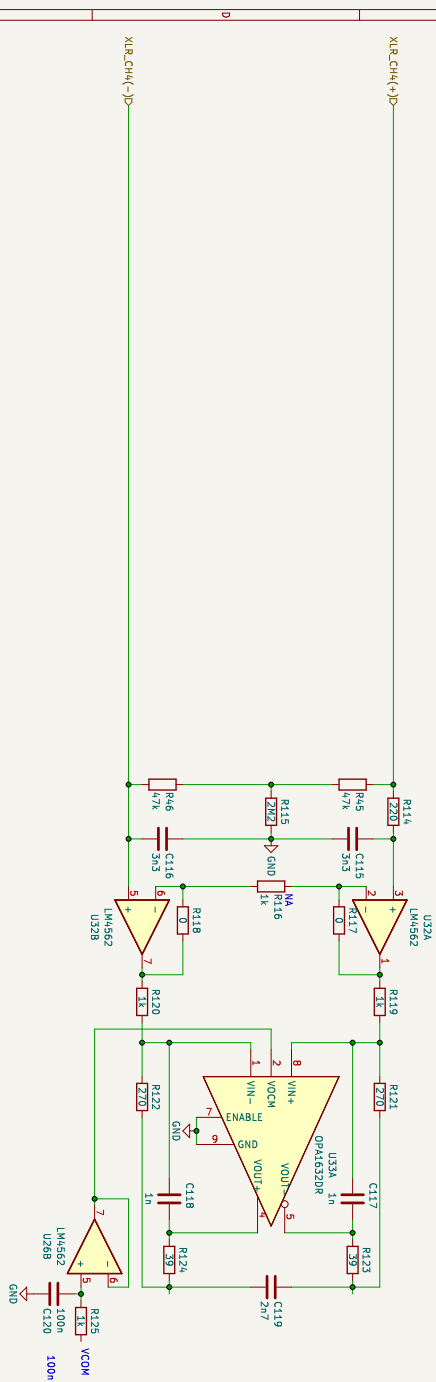
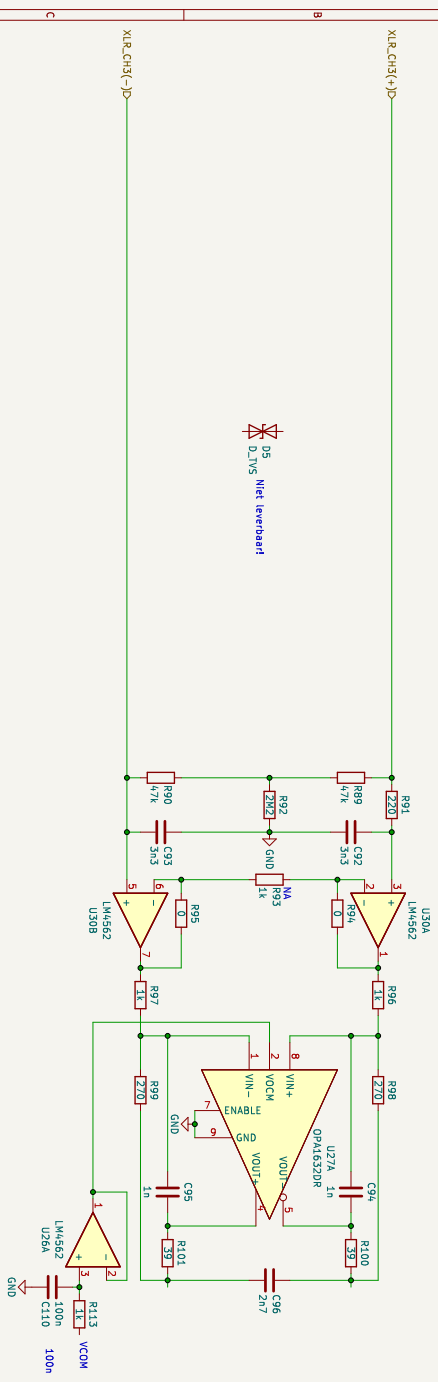
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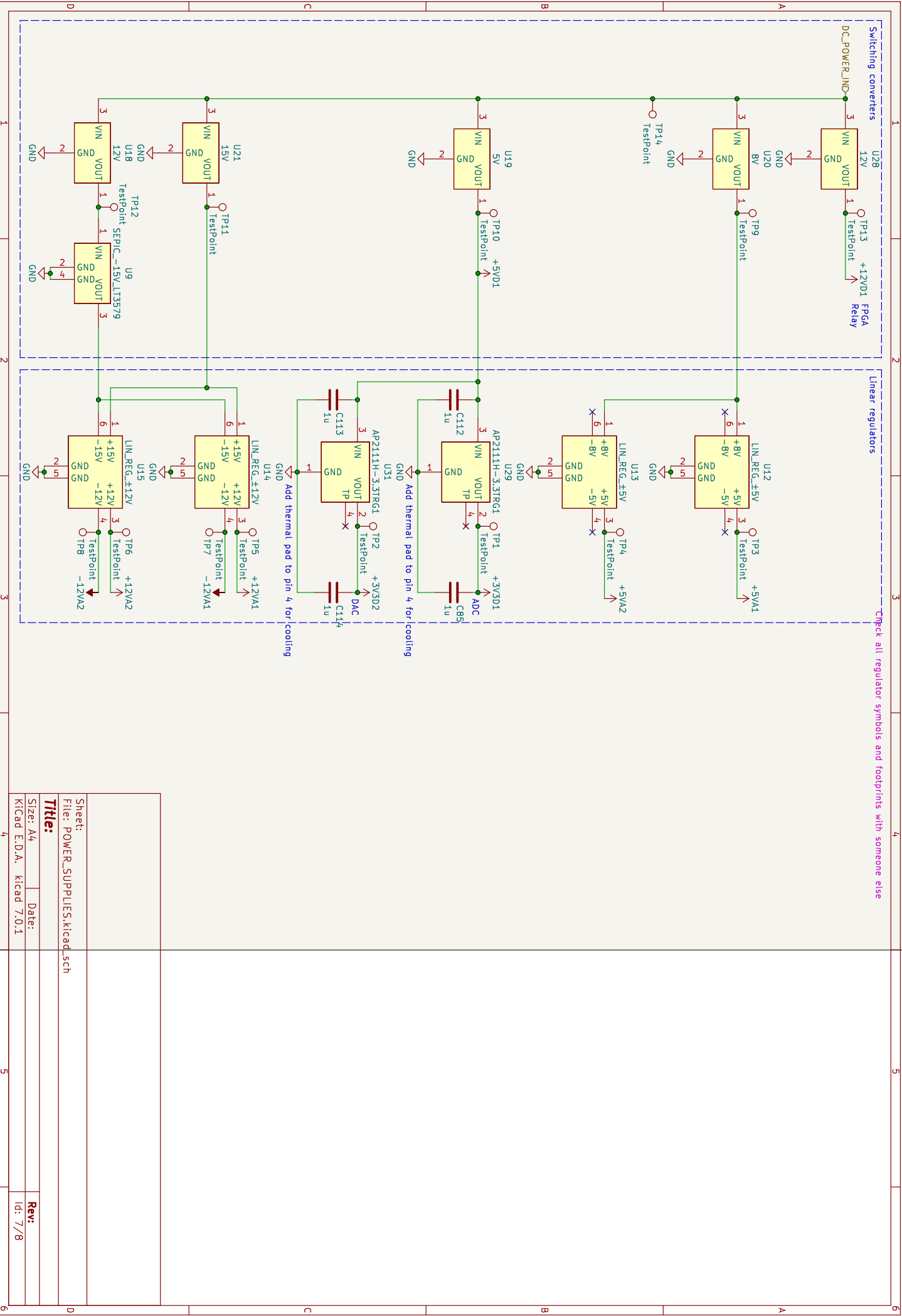
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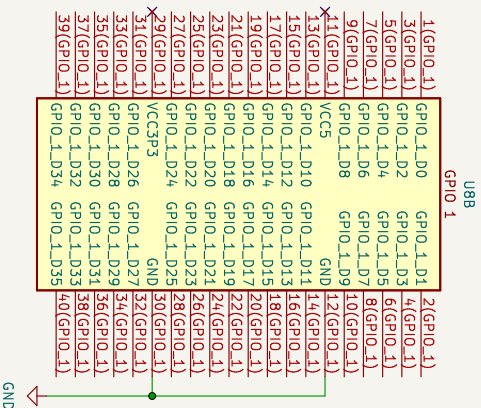
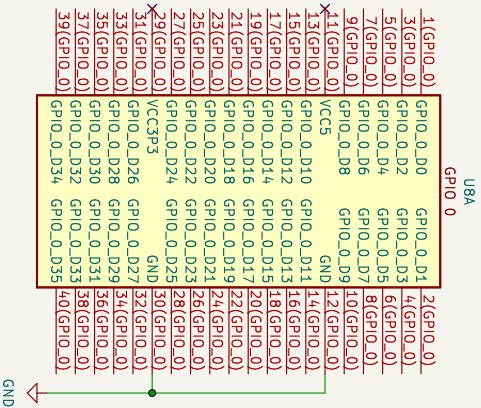
Kicad E.D.A. Kicad 7.0.1

Rev:

Id: 7/8

Check symbol and footprint with someone else

– Checken of deze pin assignment mogelijk is dmv een test programma.  
– Checken hoe erg de crosstalk aanwezig is dmv een test programma.



D5CKL\_DAC.CH1\_2  
D1RCK\_DAC.CH1\_2  
D3CK\_DAC.CH1\_2  
D4DATA\_DAC.CH1\_2  
D1NPUT\_SELECTOR\_RELAY.CH1  
D1NPUT\_SELECTOR\_RELAY.CH2

USB port of FPGA can be used as USB audio input

QU1\_RX  
DU1\_TX

DAC.CH1.CH2

DAC.CH3.CH4

DAC.CH5.CH6

D5CKL\_DAC.CH3\_4  
D1N\_DAC.CH3\_4  
D1RCK\_DAC.CH3\_4  
D3CK\_DAC.CH3\_4  
D4MUTEL\_DAC.CH3\_4  
D5DATA\_DAC.CH3\_4  
D5CKL\_DAC.CH1\_2  
D5CKL\_DAC.CH5\_6  
D5CKL\_DAC.CH5\_6

Title:

Sheet:  
File: FPGA.kicad\_sch

Size: A4 Date:

Kicad E.D.A. Kicad 7.0.1

Rev:

Id: B/8