Legend Input select (CH1) **Must have Should have** VU-meter Buffer Other page 2 / Gain (CH1/CH2) Relay driver PGA2311 −ZCEN (HIGH) } Zero crossing enabled PCM1804 -OSR0 (HIGH) STS321050B100 -MUTE (HIGH) - Unmuted RCA (CH1) ← OSR1 (HIGH) } QUAD mode —OSR2 (HIGH) Stereo volume control Differential LPF SPDT signal relay Passive RF filter ESD protection Buffer PCM1804\_A (CH1) with ofset HPF active BYPAS (LOW) HPF active 6.35mm jack (CH1) —S/!M (LOW) - Master mode EA2-12NJ —FMT0 (HIGH) By default the gain - I2S format Vcom should be around 2 to LPF —FMT1 (LOW) Buffer ~RST EA2-12NJ STS321050B100 RCA (CH2) Differential LPF Stereo volume control PCM1804\_B (CH2) SCKI (24 576 MHz) SPDT signal relay Passive RF filter ESD protection Buffer IC\_B with ofset EMI reduction series 3<sub>/</sub> I2S 6.35mm jack (CH2) I2S resistors Vcom LPF Relay driver Buffer Buffer VU-meter **FPGA** Input select (CH2) Difference amplifier VU-meter with a gain of 0.125 PCM1804 ✓—OSR0 (HIGH) TPD2E007 —OSR1 (HIGH) ├QUAD mode Differential LPF —OSR2 (HIGH) Differential passive XLR (CH3) ESD protection Buffer with DC-ofset and a PCM1804\_A (CH3) RF filter —BYPAS (LOW) ├HPF active ←——S/!M (LOW) Master mode —FMT0 (HIGH) - I2S format Vcom LPF —FMT1 (LOW) Buffer ~RST TPD2E007 Differential LPF Differential passive with DC-ofset and a PCM1804\_B (CH4) XLR (CH4) ESD protection Buffer SCKI (24 576 MHz) RF filter EMI reduction series 3 / I2S I2S resistors Vcom Buffer LPF Difference amplifier UDD32C03L01 VU-meter with a gain of 0.125 USB data (CH5) USB (CH5) ESD protection