Activities	Sub-activities	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Reporting	Plan of Approach	■	■																		
	SRD			■																	
	SDD				■	■	■														
	MDD							■	■	■	■	■	■	■	■	■	■	■	■		
	Mid-term presentation										■										
	Final presentation																		■	■	
Researching	Researching			■	■																
Design hardware	Design first iteration					■	■	■													
	Design second iteration								■	■	■										
	Design third iteration											■	■	■							
	Design fourth iteration														■	■	■				
Design firmware	Control battery emulator								■	■	■										
	Control communication between the modules								■	■											
Realization	Create PCB							■			■			■			■				
	Program PCB							■			■			■			■				
Verification	Verify first iteration								■												
	Verify second iteration											■									
	Verify third iteration														■						
	Verify fourth iteration																	■			